Fast Ethernet Consortium

100BASE-X PCS Test Suite Version 4.0

Technical Document



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MODIFICATIONS

- April 30, 2015, Version 4.0 Released
 - Removed the testing of statistics.
 - Additional minor changes
- March 7, 2013, Version 3.4 Released
 - Updated references from IEEE 802.3 Standard, 2005 to IEEE 802.3 Standard, 2012.
 - Revisions were made to the documented procedures of all tests.
 - Additional minor changes
- 2006, Version 3.3 Released
 - Updated references to Standards from IEEE 802.3 Standard, 2002 to IEEE 802.3 Standard, 2005.

ACKNOWLEDGMENTS

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INTRODUCTION

Overview

The University of New Hampshire's InterOperability Laboratory (IOL) is an institution designed to improve the interoperability of standards based products by providing an environment where a product can be tested against other implementations of a standard. This suite of tests is designed to determine if a product conforms to some of the specifications defined in Clause 24 of the IEEE 802.3-2012 Standard. Successful completion of all tests contained in this suite does not guarantee that the tested device will operate with other devices. However, combined with satisfactory operation in the IOL's interoperability test bed, these tests provide a reasonable level of confidence that the Device Under Test (DUT) will function properly in many Fast Ethernet environments.

Organization of Tests

The tests contained in this document are organized to simplify the identification of information related to a test and to facilitate in the actual testing process. Each test contains an identification section that describes the test and provides cross-reference information. The discussion section covers background information and specifies why the test is to be performed. Tests are grouped by similar functions and further organized by technology. Each test contains the following information:

Test Number

The Test Number associated with each test follows a simple grouping structure. Listed first is the Test Group Number followed by the test's number within the group. This allows for the addition of future tests to the appropriate groups of the test suite without requiring the renumbering of the subsequent tests.

Purpose

The purpose is a brief statement outlining what the test attempts to achieve. The test is written at the functional level.

References

The references section lists cross-references to the IEEE 802.3 standards and other documentation that might be helpful in understanding and evaluating the test and results.

Resource Requirements

The requirements section specifies the hardware, and test equipment that will be needed to perform the test. The items contained in this section are special test devices or other facilities, which may not be available on all devices.

Last Modification

This specifies the date of the last modification to this test.

Discussion

The discussion covers the assumptions made in the design or implementation of the test as well as known limitations. Other items specific to the test are covered here.

Test Setup

The setup section describes the configuration of the test environment. Small changes in the configuration should be included in the test procedure.

Procedure

The procedure section of the test description contains the step-by-step instructions for carrying out the test. It provides a cookbook approach to testing, and may be interspersed with observable results.

Observable Results

The observable results section lists specific items that can be examined by the tester to verify that the DUT is operating properly. When multiple values are possible for an observable result, this section provides a short discussion on how to interpret them. The determination of a pass or fail for a certain test is often based on the successful (or unsuccessful) detection of a certain observable result.

Possible Problems

This section contains a description of known issues with the test procedure, which may affect test results in certain situations.

TABLE OF CONTENTS

MODIFICATIONS	2
ACKNOWLEDGMENTS	3
INTRODUCTION	4
TABLE OF CONTENTS	
Test #24.1.1 - End of Stream Delimiter Test	7
Test #24.1.2 - Invalid Data Symbol Test	<u>9</u>
Test #24.1.3 - False Carrier Detect	11
ANNEX A (informative) Table of Definitions	13

Test #24.1.1 - End of Stream Delimiter Test

Purpose: To verify that RX_ER is asserted when there is no stream termination

sequence, ESD (/T/R/), following the SSD.

References:

• IEEE 802.3 Standard, 2012 - sections 22.2.1.5, 24.2.4.4.4, and Figure 24-11: Receive state diagram.

Resource Requirements:

• A testing station capable of encoding (decoding) data nibbles to (from) five-bit code groups as specified in clause 24 and sending (receiving) these code groups using the signaling method described in clause 25 or clause 26.

Last Modification: April 30, 2015

Discussion: Following detection of the SSD, the signal RX_DV is asserted. The RX_ER signal is asserted upon decoding any symbol following the SSD, which is not either, a valid data symbol or a defined stream termination sequence. Simultaneous assertion of RX_DV and RX_ER will cause the Reconciliation sublayer to force the MAC to detect a FrameCheckError. Refer to subclause 22.2.1.5 and Figure 24-11: Receive state diagram. The DUT is sent valid frames with the ESD (/T/R/) removed. The DUT is also sent frames with an invalid ESD is placed at the end of the frame. These two circumstances should cause the reception of idle symbols while RX_DV is asserted, thus causing RX_ER to occur. In the third case, a valid ESD terminates the frame and is followed by each of the 32 code groups before idle resumes. These frames should be properly accepted.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. balanced copper, multi-mode fiber, etc.).

Procedure:

Description of Test Frames:

NO_ESD test frame: The test frame is comprised of an SSD, a valid test frame with proper checksums and 32-bit CRC values, but no ESD (/T/R/).

VALID_ESD test frames: The test frames are comprised of an SSD, a valid test frame with proper checksums and 32-bit CRC values, a valid ESD (/T/R/) and an additional code group immediately following the frame. This is repeated to include each code group as defined by the 802.3u standard for a total of 32 frames.

INVALID_ESD test frames: The test frames are comprised of an SSD, a valid test frame with proper checksums and 32-bit CRC values, and an invalid ESD. The invalid ESDs are (/H/H/), (/H/K/), (/H/K/), (/H/K/), (/H/T/), (/J/H/), (/J/J/), (/J/K/), (/J/R/), (/J/T/), (/K/H/), (/K/J/), (/K/K/), (/K/R/), (/K/T/), (/R/H/), (/R/J/), (/R/K/), (/R/R/), (/R/T/), (/T/H/), (/T/J/), (/T/H/), (/T/J/), (/T/T/). Each frame with an invalid ESD should be dropped by the DUT.

- 1. The testing station is instructed to transmit the NO_ESD test frame*1 to the DUT.
- 2. The testing station then transmits a valid frame*1, the NO_ESD test frame, and then another valid frame*1. These frames are each separated by approximately a minimum interPacketGap.
- 3. Steps 1 and 2 are repeated for both the VALID_ESD test frames (observable result b) and the INVALID ESD test frames (observable result c).

Note *1: If the DUT is an End Station the frames used should be request frames such as ARP or Ping Frames that will elicit a reply.

Observable Results:

- a. The DUT should not respond to the NO_ESD test frame. The reception of either preceding or following valid frames should be unaffected.
- b. The DUT should respond to each of the VALID_ESD test frames. The reception of either preceding or following valid frames should be unaffected.
- c. The DUT should not respond to each of the INVALID_ESD test frames. The reception of either preceding or following valid frames should be unaffected.

Possible Problems: None.

Test #24.1.2 - Invalid Data Symbol Test

Purpose: To verify that an error (RX_ER) is detected when an invalid data symbol is sent

following the transmission of the SSD (/J/K/)

References:

• IEEE 802.3 Standard, 2012 - sections 4.2.4.1.2, 4.2.4.1.3, 24.2.4.4.3, 24.2.2.1.7 and 22.2.1.5

Resource Requirements:

• A testing station capable of encoding (decoding) data nibbles to (from) five-bit code groups as specified in clause 24 and sending (receiving) these code groups using the signaling method described in clause 25 or clause 26. The testing stations should be capable of transmitting violation code groups.

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Discussion: Following detection of the SSD, the signal RX_DV is asserted. The RX_ER signal is asserted upon decoding any symbol following the SSD which is not either a valid data symbol or a defined stream termination sequence. Simultaneous assertion of RX_DV and RX_ER will cause the Reconciliation sublayer to force the MAC to detect a FrameCheckError. Refer to subclause 22.2.1.5 and Figure 24-11: Receive state diagram. In this test, all valid data symbols will be replaced with all combinations of the invalid symbols. This is done to ensure that when an invalid symbol is detected, RX_ER is asserted rather than arbitrarily replacing the invalid symbols with valid data symbols.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. balanced copper, multi-mode fiber, etc.).

Procedure:

Test Frame Description:

In this test, the testing station sources a valid test frame with a data field containing all valid data symbols (0 thru F). Each data symbol is individually replaced with each of the following invalid codes: 00000, 00001, 00010, 00011, 00100, 00101, 00110, 01000, 01100, 10000, 11001 as well as the 5 Control Codes /J/, /K/, /T/, /R/, and /I/. Thus, 256 different invalid frames are tested.

- 1. The testing station transmits one test frame*1 to the DUT.
- 2. The testing station then transmits a valid frame*1, the test frame, and then another valid frame*1. These frames are each separated by approximately a minimum interPacketGap.
- 3. Steps 1 and 2 are repeated for all remaining test frames.

Note *1: If the DUT is an End Station the frames used should be request frames such as ARP or Ping Frames that will elicit a reply.

Observable Results:

- a. The DUT should not respond to frames with an invalid symbol.
- b. The reception of either preceding or following valid frames should be unaffected.

Possible Problems: None.

Test #24.1.3 - False Carrier Detect

Purpose: To verify that the device under test can detect false carrier events.

References:

• IEEE 802.3 Standard, 2012 – sections 22.2.2.7, 22.2.2.8, 22.2.2.10, Table 22-2, sections 24.2.2.1.4, 24.2.4.4.2, 24.3.4.3, and figure 24-14.

Resource Requirements:

• A testing station capable of encoding (decoding) data nibbles to (from) five-bit code groups as specified in clause 24 and sending (receiving) these code groups using the signaling method described in clause 25 or clause 26.

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Discussion: After channel activity is detected, the Receive process first aligns the incoming code-bits on code-group boundaries for subsequent data decoding. This is achieved by scanning the rx_bits vector for a SSD (/J/K/). Detection of the SSD causes the Receive process to enter the START OF STREAM J state.

Well-formed streams contain SSD (/J/K/) in place of the first 8 preamble bits. In the event that something else is sensed immediately following the detection of carrier, a False Carrier Indication is signaled to the MII by asserting the RX_ER and setting RXD to 1110 while RX_DV remains deasserted.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. balanced copper, multi-mode fiber, etc.).

Procedure:

- 1. Let bad_ssd be a vector of 10 code-bits and let bad_ssd[0] be fixed at ZERO. Initialize bad_ssd[9:1] to the code-bit pattern "111111101". Command the testing station to send bad_ssd (most significant bit first) followed by the remainder of a valid test frame *1 (excluding the SSD). The testing station will monitor transmit activity from the device under test.
- 2. Shift bad_ssd[9:1] left one code-bit, discarding the carry bit and setting bad_ssd[1] to ONE. Command the testing station to send bad_ssd followed by the remainder of a valid test frame*1 (excluding the SSD). The testing station will monitor transmit activity from the device under test.
- 3. Repeat step 2 until bad_ssd[9:2] contains the pattern "01111 111", which is the last one sent.
- 4. Set bad_ssd[9:5] to the /J/ code group and set bad_ssd[4:0] to the code-bit pattern "00000". Command the testing station to send bad_ssd followed by the remainder of a valid frame*1 (excluding the SSD). The testing station will monitor transmit activity from the device under test.

- 5. Increment bad_ssd[4:0]. Command the testing station to send bad_ssd followed by the remainder of a valid frame*1 (excluding the SSD). The testing station will monitor transmit activity from the device under test.
- 6. Repeat step 4 until bad_ssd[4:0] exceeds "11111". Skip the iteration in which bad_ssd[4:0] equals "10001" as this is the /K/ code-group (this makes bad_ssd[9:0] /J/K/, the valid start of stream delimiter).
- 7. Valid frames*1 separated by approximately a minimum IPG are sent preceding and following each one of the Test Frames to ensure that the reception of a false carrier event does not affect the reception of valid frames.

Note *1: If the DUT is an End Station the frames used should be request frames such as ARP or Ping Frames that will elicit a reply.

Observable Results:

- a. The DUT should not respond to each of the Test Frames.
- b. The reception of a valid frame preceding or following each of the test frames should not be affected.

Possible Problems: None.

12

ANNEX A (informative) Table of Definitions

(informative)

Table of Acronym Definitions

8802-3	ISO/IEC 8802-3 (IEEE Std 802.3)
ANSI	American National Standards Institute

CRC cyclic redundancy check
DTE data terminal equipment
DUT Device Under Test
ESD End-of-Stream delimiter
FCS frame check sequence

IPG interPacketGap

ISO International Organization for Standardization

LAN local area network
LLC logical link control
MAC medium access control
MDI medium dependent interface
MII media independent interface
PCS physical coding sublayer
PHY Physical Layer entity

PMA physical medium attachment PMD physical medium dependent RS reconciliation sublayer

RX_DV An MII Signal (see IEEE 802.3 section 22.2.2.6) RX_ER An MII Signal (see IEEE 802.3 section 22.2.2.8)

SFD start-of-frame delimiter SSD Start-of-Stream Delimiter