

# **Gigabit Ethernet Consortium** Clause 28 Auto-Negotiation Next Page Exchange Test Suite v2.3 Report

UNH-IOL — 121 Technology Drive, Suite 2 — Durham, NH 03824 — +1-603-862-0090 Consortium Manager: Gerard Nadeau — <u>grn@iol.unh.edu</u> — +1-603-862-0166

Bob T. Vendor Computer Inc. 752 Orlando Lane Durham, NH 05852 February 8, 2006 Report Rev. 1.0

Enclosed are the results from the Clause 28 Auto-Negotiation Next Page Exchange testing performed on:

Device Under Test (DUT):Computer HBL7845 NICHardware Version:Not AvailableFirmware Version:Not AvailableSoftware Version:1005Miscellaneous:PHY: Int LCX72A; Magnetics: BET 04M S558-5999-T7

The test suite referenced in this report is available at the UNH-IOL website:

ftp://ftp.iol.unh.edu/pub/ethernet/test suites/Next Page Test Suite/Next Page Exchange Test/ Suite v2.3.pdf

Issues Observed While Testing

28.2.4 – Reception of Toggle Bit: The DUT was observed to have issues with reception of the Toggle bit.

For specific details regarding issues please see the corresponding test result.

Testing Completed 02/08/2006

John Q. Tester jonny5@iol.unh.edu

John Q. Reviewer johngreviewer@iol.unh.edu

Review Completed 02/08/2006

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## **Result Key**

The following table contains possible results and their meanings:

Result	Interpretation
PASS	The Device Under Test (DUT) was observed to exhibit conformant behavior.
PASS with	The DUT was observed to exhibit conformant behavior however an additional explanation of the
Comments	situation is included, such as due to time limitations only a portion of the testing was performed.
FAIL	The DUT was observed to exhibit non-conformant behavior.
Warning	The DUT was observed to exhibit behavior that is not recommended.
Informative	Results are for informative purposes only and are not judged on a pass of fail basis.
Refer to	From the observations, a valid pass or fail could not be determined. An additional explanation of
Comments	the situation is included.
Not Applicable	The DUT does not support the technology required to perform these tests.
Not Available	Due to testing station or time limitations, the tests could not be performed.
Borderline	The observed values of the specified parameters are valid at one extreme, and invalid at the other.
Not Tested	Not tested due to the time constraints of the test period.

# **Test Setup**

All tests were completed using the UNH-IOL created Python Board. This board allows us to view signaling transmitted and received before establishing a link, along with viewing the type of link signaling a device is transmitting. Some of our testing tools can be viewed at: <u>http://www.iol.unh.edu/consortiums/ethernet/tools/aneg/</u> Some tests required the use of specific Smart Bits cards to establish a link and send packets.



# **GROUP 1: NEXT PAGE TRANSMISSION**

Test # and Label	Part(s)	<b>Result</b> (s)
28.1.1 – Next Page Bit	а	PASS
	b	PASS
	с	PASS

Purpose: To verify that the device under test makes proper use of the Next Page bit throughout the Next Page exchange process.

- a. The DUT should keep the NP bit set until the second to last Next Page that it desires to send. The last desired Next Page and all subsequent Null Message Pages should have NP set to 0.
- b. When the DUT and the link partner desire to exchange the same number of Next Pages, the DUT should keep the NP bit set in all but the last Next Page sent.
- c. When the DUT desires to send more Next Pages than its link partner, the DUT should still keep the NP bit set in all but the last Next Page sent.

#### **Comments on Test Results**

- a. The DUT kept the Next Page bit set for the first two Next Pages Transmitted. The third page transmitted has NP=0, and the last two pages transmitted were Null Message Pages with NP=0. Thus, the DUT desired to transmit 3 Next Pages.
- b. When sent only three Next Pages (1<sup>st</sup> two with NP=1, 3<sup>rd</sup> with NP=0), the NP bit sent by the DUT was set in all but the last Next Page.
- c. When sent only Null Message Pages with NP=0, the NP bit sent by the DUT was set in all but the last Next Page.

Test # and Label	Part(s)	Result(s)
28.1.2 – Transmitted Toggle Bit Value	a	PASS
	b	PASS
Expected Results and Procedural Comments		

Purpose: To verify that the device under test properly alternates values of 0 and 1 in the Toggle bit position (bit D11) of its Next Pages, and checks to see if its received Next Pages follow this pattern.

- a. The value of the Toggle bit in the first Next Page should have the opposite value of bit D11 in the DUT's link code word.
- b. The value of the Toggle bit of the Next Page transmitted by the DUT should always take the opposite value of the Toggle bit of the previous Next Page (if the previous value was a 0, it should be a 1, and vice versa).

#### **Comments on Test Results**

- a. The value of the Toggle bit in the first Next Page transmitted by the DUT was always the opposite of bit D11 in the received Base Page.
- b. The DUT properly alternated transmission of pages with the Toggle bit set to 1 and 0.

Test # and Label	Part(s)	<b>Result</b> (s)
28.1.3 – Null Message Page Generation	a	PASS
	b	Informative
Expected Results and Procedural Comments		

Purpose: To verify that the device under test transmits proper Null Message Pages if it completes sending Message and Unformatted Pages before its link partner.

- a. When the link partner desires to send 2 more Next Pages than the DUT, the DUT should transmit validly formed Null Message Pages.
- b. INFORMATIVE: When the link partner desires to send the same number of Next Pages as the DUT, the DUT need not terminate the Next Page exchange by transmitting a Null Message Page.

#### **Comments on Test Results**

- a. Following the last desired Next Page transmission (when the DUT set NP=0), the DUT properly exchanged 2 Null Message Pages (6001 & 6801), ceasing Next Page transmission when the last page exchanged with the link partner had NP=0.
- b. The DUT did not transmit a Null Message Page when the link partner sent 2 Next Pages with NP=1, followed by 1 Next Page with NP=0.



# **GROUP 2: NEXT PAGE RECEPTION**

Test # and Label	Part(s)	<b>Result</b> (s)
28.2.1 – Next Page Wait State	a	PASS
	b	PASS
Expected Results and Procedural Comments		

Purpose: To verify that the device under test enters the NEXT PAGE WAIT state when both a device and its link partner desire a Next Page exchange to occur and mr\_next\_page\_loaded=true.

- a. While in the COMPLETE ACKNOWLEDGE state, the DUT is sent enough Next Pages with proper Toggle and ACK bit values that would normally cause the DUT the engage in a valid Next Page exchange. Since mr\_next\_page\_loaded is never set to true, the DUT should not enter NEXT PAGE WAIT and remain in the COMPLETE ACKNOWLEDGE state transmitting its initial Base Page with the Acknowledge bit set.
- b. While in the NEXT PAGE WAIT state, the DUT is sent enough Next Pages, all with proper Toggle and ACK bit values, in order to put it through the COMPLETE ACKNOWLEDGE state. The DUT should enter the NEXT PAGE WAIT state, begin transmitting its first Next Page, and complete a valid Next Page exchange.

#### **Comments on Test Results**

- a. The DUT was observed to properly remain in the COMPLETE ACKNOWLEDGE state and continue to send its initial Base Page with ACK.
- b. The DUT was observed to properly enter the NEXT PAGE WAIT state and begin transmitting its first Next Page.

Test # and Label	Part(s)	Result(s)
28.2.2 – Next Page Exchange Restart	a	PASS
Expected Results and Procedural Comments		

Purpose: To verify that the device under test enters the TRANSMIT DISABLE state from the NEXT PAGE WAIT state if flp\_receive\_idle becomes true.

a. The DUT is sent 4 FLPs without the Acknowledge bit set followed by 8 FLPs with the Acknowledge bit set, such that the DUT begins sending its first Next Page. The DUT is then sent 4 FLPs containing a Next Page. The DUT should enter the TRANSMIT DISABLE state upon expiry of the nlp\_max\_timer.

#### **Comments on Test Results**

a. The DUT was observed to properly halt transmission for approximately break\_link\_timer and restart Auto-Negotiation.

Test # and Label	Part(s)	<b>Result</b> (s)
28.2.3 – Next Page Consistency Match	а	PASS
Expected Results and Procedural Comments		

Purpose: To verify that the device under test performs a consistency match test on received Next Pages.

a. The DUT is sent a series of FLPs with the NP bit set to put it through the COMPLETE ACKNOWLEDGE state. Immediately following, the DUT is sent 3 Next Pages without the Acknowledge bit set, and 3 Next Pages containing different abilities with the Acknowledge bit set. The DUT should cease transmission of FLPs once the inconsistent FLPs have been received.

#### **Comments on Test Results**

a. The DUT properly detected consistency\_match=false and terminated transmission of FLPs, even if an FLP was being transmitted.

Test # and Label	Part(s)	Result(s)
28.2.4 – Reception of Toggle Bit	a	FAIL
Expected Results and Procedural Comments		

Purpose: To verify that the device under test properly checks to see if its received Next Pages alternate the value of the Toggle bit, and that the DUT does not accept pages that have not alternated the Toggle bit.

a. The DUT is sent a series of FLPs with the NP bit set to put it through the COMPLETE ACKNOWLEDGE state. A series of Next Pages are then sent with the Toggle bit set to the proper value followed by the same Next Pages with the Acknowledge bit set to again put the DUT through COMPLETE ACKNOWLEDGE. The second series of Next Pages (including those with the Acknowledge bit set) that are sent to the DUT have the same value of the Toggle bit as the previous page. A third series of Next Pages are then sent that have the Toggle bit set to the correct value. The DUT should not leave the NEXT PAGE WAIT state on reception of the second series of Next Pages, but should accept the last page and continue with the Next Page exchange.

## **Comments on Test Results**

a. The DUT was observed to improperly exit the NEXT PAGE WAIT state upon reception of FLPs with an incorrect Toggle bit value.

Test # and Label	Part(s)	Result(s)
28.2.5 – Reception of rx_link_code_word[NP]=0	a	PASS
Expected Results and Procedural Comments		

Purpose: To verify that the DUT will properly complete a Next Page exchange when rx\_link\_code\_word[NP]=0 while tx\_link\_code\_word[NP]=0 in the ACKNOWLEDGE DETECT state.

a. The DUT is sent a series of FLPs with the NP bit set to put it through the COMPLETE ACKNOWLEDGE state. This transmission is followed by a series of Next Pages with the Next Page bit set to one, until the DUT has no more pages to send and sets its Next Page bit to zero. There will be a time in the Next Page exchange in which the DUT is receiving pages with the NP bit set to one and is transmitting Null Message Pages with the NP bit set to zero. When the DUT has entered the COMPLETE ACKNOWLEDGE state, the NP bit is set to zero in the transmitted FLP so that the DUT receives the new page while still in the COMPLETE ACKNOWLEDGE state. The DUT should not complete the Next Page exchange, and should enter NEXT PAGE WAIT.

#### **Comments on Test Results**

a. The DUT properly entered NEXT PAGE WAIT and continued the Next Page exchange.

Test # and Label	Part(s)	Result(s)
28.2.6 – Priority Resolution Function	a	PASS
Expected Results and Procedural Comments		

Purpose: To verify that the device under test properly configures the highest common denominator (HCD) technology for the transmitted technologies in a link code word after a Next Page exchange has occurred.

a. In every case, the DUT should resolve the highest priority possible based on the priority resolution function for the technologies advertised. Note, the only test scenario conducted was advertising 10BASE-T half duplex, 10BASE-T full duplex, 100BASE-TX half duplex, 100BASE-TX full duplex, and 1000BASE-T full duplex to the DUT. Once negotiation was complete, the device was then provided a stream of frames. The device was then verified to be in full duplex mode by guaranteeing that the DUT is transmitting a frame while simultaneously receiving a frame.

## **Comments on Test Results**

a. In every case, the DUT was observed to resolve the highest priority possible based on the priority resolution function for the technologies advertised.