OpenFabrics Alliance
Interoperability Working Group (OFA-IWG)
January 2010 Logo Event Report

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Report Rev1.3
OFED Version: 1.5

Enclosed are the results from OFA Logo testing performed on the following devices under test (DUTs):

LSI XBB2 DDR SRP Target

The test suite referenced in this report is available at the OFA website. At test time release 1.30 (September 8, 2009 DRAFT) was used:

http://www.iol.unh.edu/ofatestplan/

The following table highlights the Mandatory test results required for the OpenFabrics Interoperability Logo for the DUT per the test plan referenced above and the current OpenFabrics Interoperability Logo Program (OFILP).

Mandatory Test Procedures	IWG Test Status	Result/Notes
10.1: IB Link Initialization	**Mandatory**	**PASS**
10.2: IB Fabric Initialization	**Mandatory**	**PASS**
10.5: IB SM Failover and Handover	**Mandatory**	**PASS**
10.6: IB SRP	**Mandatory**	**PASS**

For specific details regarding issues please see the corresponding test result.

Summary of all results follows on the second page of this report.

Testing Completed 02/09/2010

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Review Completed 05/11/2010

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Table 1: Result Summary

The following table summarizes all results from the event pertinent to an IB device.

Note: **Failing a beta test does not necessarily indicate a failure of the device under test.** There are several reasons that a device might fail a beta test that are beyond the control of the device manufacturer. Such reasons include unresolved problems with the OFED software, with other vendors' devices, and with the test plan procedures.

Test Procedures	IWG Test Status	Result/Notes
10.1: IB Link Initialization	**Mandatory**	**PASS**
10.2: IB Fabric Initialization	**Mandatory**	**PASS**
10.5: IB SM Failover and Handover	**Mandatory**	**PASS**
10.6: IB SRP	**Mandatory**	**PASS**

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Report Revision History

- v1.0 Initial working copy

- v1.1 Updated to include tested firmware version information.
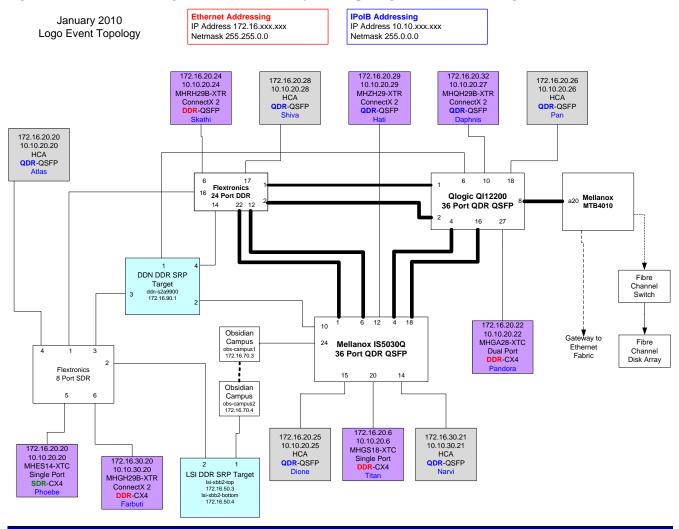
- v1.2 Updated results

- v1.3 Updated topology

Table 2: Result Key

The following table contains possible results and their meanings:

Result:	Description:
PASS	The Device Under Test (DUT) was observed to exhibit conformant behavior.
PASS with Comments	The DUT was observed to exhibit conformant behavior however an additional explanation of the situation is included, such as due to time limitations only a portion of the testing was performed.
FAIL	The DUT was observed to exhibit non-conformant behavior.
Warning	The DUT was observed to exhibit behavior that is not recommended.
Informative	Results are for informative purposes only and are not judged on a pass or fail basis.
Refer to Comments	From the observations, a valid pass or fail could not be determined. An additional explanation of the situation is included.
Not Applicable	The DUT does not support the technology required to perform this test.
Not Available	Due to testing station limitations or time limitations, the tests could not be performed.
Borderline	The observed values of the specified parameters are valid at one extreme and invalid at the other.
Not Tested	Not tested due to the time constraints of the test period.

Table 3: DUT and Test Setup Information

Figure 1: The IB fabric configuration utilized for any tests requiring a multi-switch configuration is shown below.

January 2010
Logo Event Topology

Ethernet Addressing
IP Address 172.16.xxx.xxx
Netmask 255.255.0.0

IPoIB Addressing
IP Address 10.10.xxx.xxx
Netmask 255.0.0.0

DUT #1 Details			
Manufacturer	LSI Inc.	Firmware Rev	07.62.22.00
Model	XBB2	NVSRam Rev	N7091-760834-405
Speed	DDR	Hardware Rev	NA
Additional Comments/Notes	**Firmware md5sum**		**NVSRam md5sum**
LSI XBB2 InfiniBand SRP target	6dd0b95e293fffd75508eb8623b2a28a		4d46beba75411220e27f1e398e9f4081

Mandatory Tests - IB Device Test Summary Results:

Test Number and Name	Part(s)	Summary Note(s)	Result(s)
Group 10.1: IB Link Initialize	Test #1	No issues seen	PASS
Discussion:			
Physical link initialization was verified between this device and every other device in the fabric. Link status was observed visually via status lights on the device. Port width and link speed was verified via ibdiagnet.			

Link Partner	LSI XBB2
Mellanox MHGA28-XTC DDR	Pass
Mellanox MHGS18-XTC DDR	Pass
Mellanox MHES14-XTC SDR	Pass
Mellanox MHZH29-XTR QDR	Pass
Mellanox MHRH29b-XTR DDR	Pass
Mellanox MHQH29b-XTR QDR	Pass
Mellanox MHGH29b-XTR DDR	Pass
Mellanox IS5030Q - QDR	Pass
QLogic 12200 - QDR	Pass
Flextronics X430044 - DDR	Pass
Flextronics X430066 - SDR	Pass
Obsidian Longbow	Pass
Obsidian Campus	Pass
LSI XBB2 - DDR	X
DDN S2A9900 - DDR	X
Mellanox MTB4010 - QDR	Pass

Test Number and Name	Part(s)	Summary Note(s)	Result(s)
Group 10.2: IB Fabric Initialization	Test #1:	No issues seen	PASS
Discussion:			
The fabric configuration shown in Figure 1 was used for this test. 'ibdiagnet -c 1000' showed no Port error counters increment. Only one SM is run at a time. All switches are power cycled between SM trials. All links are validated via use of 'ibdiagnet' and 'ibchecknet' was used to verify that there were no duplicate guids. Refer to the table below for SM details. SMs tested include: *OFED OpenSM (SM Only), QLogic 12200 (Managed Switch), and Mellanox IS5030Q (Managed Switch)*			

Test Number and Name	Part(s)	Summary Note(s)	Result(s)
Group 10.5: IB SM Failover and Handover	Test #1	No issues seen	PASS
Discussion:			
No issues were observed. SMs tested include: *OFED OpenSM (SM Only)*			

Test Number and Name	Part(s)	Summary Note(s)	Result(s)
Group 10.6: IB SRP	Test #1:	No issues seen	**PASS**
Discussion:			
No SRP issues were observed with this Target. SMs tested include: *OFED OpenSM (SM Only), QLogic 12200 (Managed Switch SM), and Mellanox IS5030Q (Managed Switch) SM*			