



OpenFabrics Alliance

Interoperability Logo Group (OFILG)

July 2017 Logo Report



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Date: August 29, 2017
Report Revision: 1.0
OFED Version: OFED 4.8
OS Version: Scientific Linux 7.3

Enclosed are the results from OFA Logo testing performed on the following device under test (DUT):

*Intel X722 RNIC
Intel NE020 RNIC*

The test suite referenced in this report is available at the UNH-IOL website. Release 2.05-v2 (2017-03-22) was used.

<http://www.iol.unh.edu/ofatestplan>

The following table highlights the Mandatory test results required for the OpenFabrics Interoperability Logo for the DUT per the Test Plan referenced above and the current OpenFabrics Interoperability Logo Program (OFILP).

Test Procedures	IWG Test Status	Result/Notes
<u>12.1: Ethernet Link Initialization</u>	Mandatory	Pass
<u>13.4: TI uDAPL</u>	Mandatory	Pass
<u>13.5: TI RDMA Basic Interoperability</u>	Mandatory	Pass
<u>13.6: TI RDMA Stress</u>	Mandatory	Pass
<u>13.7: TI MPI – Open MPI</u>	Mandatory	Pass

Summary of all results follows on the second page of this report.
For specific details regarding issues, please see the corresponding test result.

Testing Completed August 29, 2017

Reviewed & Issued Septe 5, 2017

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Result Summary

The Following table summarizes all results from the event pertinent to this iWARP device class.

Test Procedures	IWG Test Status	Result/Notes
11.7 TI iSER	Beta	Not Tested
12.1: Ethernet Link Initialization	Mandatory	Pass
13.2 TI NFS over RDMA	Beta	Not Tested
13.4: TI uDAPL	Mandatory	Pass
13.5: TI RDMA Basic Interoperability	Mandatory	Pass
13.6: TI RDMA Stress	Mandatory	Pass
13.7: TI MPI – Open MPI	Mandatory	Pass

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Report Revision History

- v1.0 Initial Release

Configuration Files

Description	Attachment
Scientific Linux 7.3 Configuration File	
OFED 4.8 Configuration File	

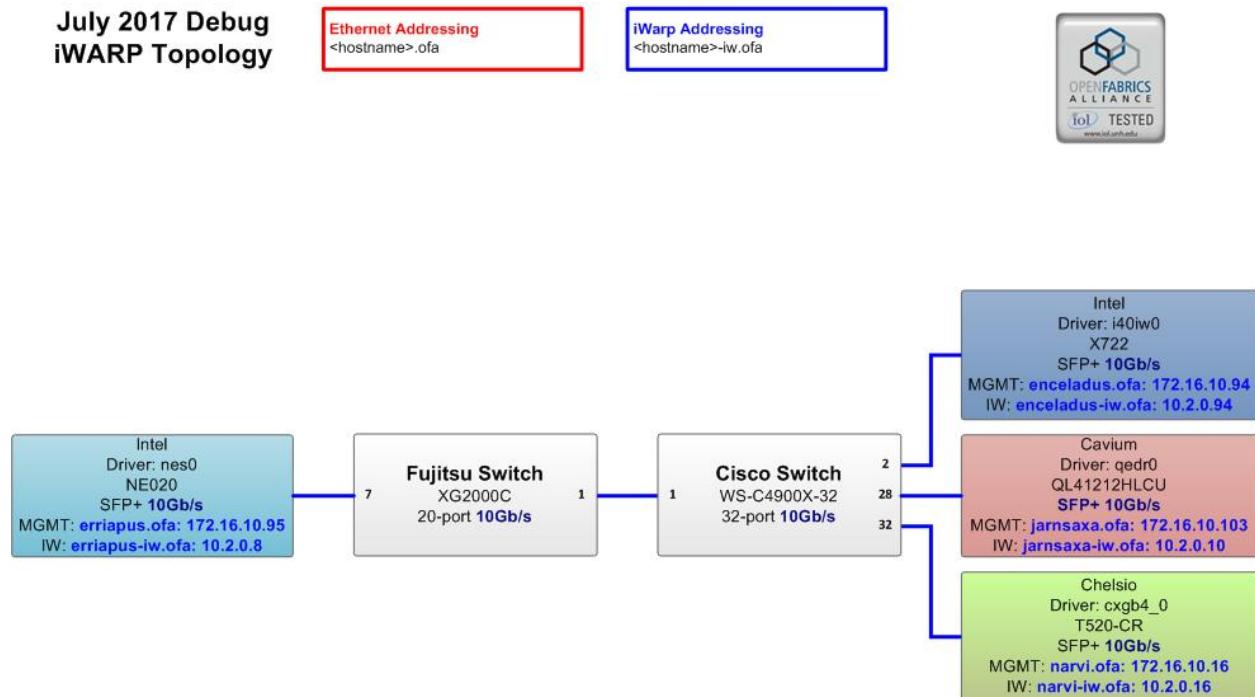
Result Key

The following table contains possible results and their meanings:

Result:	Description:
PASS	The Device Under Test (DUT) was observed to exhibit conformant behavior.
PASS with Comments	The DUT was observed to exhibit conformant behavior however an additional explanation of the situation is included.
FAIL	The DUT was observed to exhibit non-conformant behavior.
Qualified PASS	The DUT was observed to exhibit conformant behavior, with the exception of fault(s) or defect(s) which were previously known.
Warning	The DUT was observed to exhibit behavior that is not recommended.
Informative	Results are for informative purposes only and are not judged on a pass or fail basis.
Refer to Comments	From the observations, a valid pass or fail could not be determined. An additional explanation of the situation is included.
Not Supported	The DUT does not support the technology required to perform this test.
Not Available	Due to testing station limitations or time limitations, the tests could not be performed.
Borderline	The observed values of the specific parameters are valid at one extreme and invalid at the other.
Not Tested	Not tested due to the time constraints of the test period.

DUT and Test Setup Information

The iWARP fabric configuration utilized for all testing is shown below.



DUT Details			
Manufacturer:	Intel	Firmware Revision:	0.2
Model:	X722	Hardware Revision:	0x0
Speed:	10Gb/s	Located in Host:	tarvos
Additional Comments / Notes:			
Linux kernel 4.8 was used for this device.			

DUT Details			
Manufacturer:	Intel	Firmware Revision:	3.23
Model:	NE020	Hardware Revision:	0x5
Speed:	10Gb/s	Located in Host:	erriapus
Additional Comments / Notes:			

Mandatory Tests – IW Device Test Results:

12.1: Ethernet Link Initialization

Test Result	Pass
Result Discussion:	
All devices were shown to link and pass traffic to all other devices in a back-to-back configuration under nominal (unstressed) conditions.	

13.4: TI uDAPL

Test Result	Pass
Discussion:	
All devices were shown to communicate correctly using the Direct Access Programming Library, by use of the Linux dapltest tool.	

13.5: TI RDMA Basic Interoperability

Test Result	Pass
Discussion:	
All devices were shown to correctly exchange core RDMA operations across a simple network path under nominal (unstressed) conditions. Each HCA acted as both a client and a server for all tests.	

13.6: TI RDMA Stress

	Switch Load	Switch Fan In
Test Result	Pass	Pass
Discussion:		
All switches were seen to properly handle a large load as indicated by the successful completion of control communications between two RNICs while other RNICs in the fabric were used to generate traffic in order to put a high load on the switch.		

13.7: TI MPI – Open MPI

Test Result	Pass
Discussion:	
MPI Benchmarks were performed between all HCAs and were observed to exhibit the proper behavior.	