

10 GIGABIT ETHERNET

Clause 46 10Gb/s RS Test Suite
Version 1.0
Technical Document



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MODIFICATION RECORD

- October 31, 2001 Version 0.1 Released
- June 25, 2002 Version 1.0 Released
 - Minor editorial modifications and updating of test suite to D5.0

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INTRODUCTION

Overview

The University of New Hampshire's InterOperability Laboratory (IOL) is an institution designed to improve the interoperability of standards based products by providing an environment where a product can be tested against other implementations of a standard. This suite of tests has been developed to help implementers evaluate the functioning of their Clause 46 RS based products. The tests do not determine if a product conforms to the IEEE 802.3 standard, nor are they purely interoperability tests. Successful completion of all tests contained in this suite does not guarantee that the tested device will operate with other 10Gb/s capable devices. However, combined with satisfactory operation in the IOL's interoperability test bed, these tests provide a reasonable level of confidence that the Device Under Test (DUT) will function well in many 10Gb/s environments.

Organization of Tests

The tests contained in this document are organized to simplify the identification of information related to a test and to facilitate in the actual testing process. Each test contains an identification section that describes the test and provides cross-reference information. The discussion section covers background information and specifies why the test is to be performed. Tests are grouped in order to reduce setup time in the lab environment. Each test contains the following information:

Test Number

The Test Number associated with each test follows a simple grouping structure. Listed first is the Test Group Number followed by the test's number within the group. This allows for the addition of future tests to the appropriate groups of the test suite without requiring the renumbering of the subsequent tests.

Purpose

The purpose is a brief statement outlining what the test attempts to achieve. The test is written at the functional level.

References

The references section lists cross-references to the IEEE 802.3 standards and other documentation that might be helpful in understanding and evaluating the test and results.

Resource Requirements

The requirements section specifies the hardware, and test equipment that will be needed to perform the test. The items contained in this section are special test devices or other facilities, which may not be available on all devices.

Last Modification

This specifies the date of the last modification to this test.

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Discussion

The discussion covers the assumptions made in the design or implementation of the test as well as known limitations. Other items specific to the test are covered here.

Test Setup

The setup section describes the configuration of the test environment. Small changes in the configuration should be included in the test procedure.

Procedure

The procedure section of the test description contains the step-by-step instructions for carrying out the test. It provides a cookbook approach to testing, and may be interspersed with observable results.

Observable Results

The observable results section lists observables that can be examined by the tester to verify that the DUT is operating properly. When multiple values are possible for an observable, this section provides a short discussion on how to interpret them. The determination of a pass or fail for a certain test is often based on the successful (or unsuccessful) detection of a certain observable.

Possible Problems

This section contains a description of known issues with the test procedure, which may affect test results in certain situations.

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Group1: Reception

Scope: The following tests cover the reception of data at the Reconciliation Sublayer.

Purpose: These tests are designed to verify that the device under test reacts properly to receipt of data, both valid and invalid, at the Reconciliation Sublayer.

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Test 46.1.1 – Start control character creation and alignment

Purpose: To verify that upon reception of the first byte of preamble from the MAC, the RS replaces the preamble byte with a start control character and aligns it to lane 0.

References:

- IEEE Draft P802.3ae/D5.0 - subclauses 4.2.5, 46.1.7.1.4, 46.2.2, and 46.2.5

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

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Discussion: When the MAC is requested to send a new frame, it calls the procedure PhysicalSignalEncap. This procedure transmits 7 bytes of preamble and then 1 byte of SFD. The preamble pattern has historically been used to stabilize and synchronize the physical medium. In 10 Gigabit Ethernet, the MAC is required to transmit preamble and SFD but it is not necessary for stabilization and synchronization. After the successful transmission of 8 bytes of preamble, the MAC shall transmit an SFD. The entire preamble and SFD pattern is shown below:

10101010 10101010 10101010 10101010 10101010 10101010 10101010 10101011

For a 10Gb/s MAC/RS implementation, the Start control character (0xFB), which replaces the first byte of preamble, is required to be aligned to lane 0 on the XGMII. Error free transmission will not cause the SFD to be on any lane other than lane 3 in the column following the Start.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

Procedure:

1. Instruct the testing station to transmit a valid 64-byte request frame to the DUT.
2. Capture and observe the transmissions from the DUT.
3. Repeat steps 1 and 2 with frame sizes including but not limited to 65, 66, and 67 bytes in length.

Observable results:

- a. The DUT should reply to all valid frames with a Start control character aligned to lane 0, and an SFD aligned to lane 3.

Possible problems: None

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Test 46.1.2 – Terminate control character creation and alignment

Purpose: To verify that the DUT inserts a Terminate control character at the end of any frame it transmits, and that the Terminate control character can be aligned to any of the 4 lanes.

References:

- IEEE Draft P802.3ae/D5.0- subclauses 46.1.7.1.4, 46.2.4, and 46.2.5

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

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Discussion: The end of frame delimiter is denoted by the assertion of TXC with the appropriate Terminate control character on any one of the four lanes. The terminate character is represented by TXC=1 and TXD=FD on the lane containing the character across the XGMII, and by the DATA_COMPLETE PLS_DATA.request parameter from the MAC. Unlike the start control character, the terminate control character is valid on any lane. Frames may be any length from 64 bytes to 1518 bytes, and thus the last byte of the CRC can fall on any lane. When transmitting a frame, the RS inserts a terminate control character after the last byte of the CRC. The inter-packet gap begins with this character.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

Procedure:

1. Instruct the DUT to transmit a 512-byte frame.
2. Capture and observe the transmissions from the DUT.
3. Repeat steps 1 and 2, randomly increasing or decreasing the size of the frame.

Observable results:

- a. All frames received by the testing station should contain a valid Terminate control character following the last byte of the CRC.
- b. The Terminate control character should be observed on all four lanes.

Possible problems: None

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Test 46.1.3 – Deficit Idle Count

Purpose: Verify that the DUT properly implements the Deficit Idle Count.

References:

- IEEE Draft P802.3ae/D5.0 - subclause 46.3.1.4

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

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Discussion: When the MAC is ready to transmit a frame, it may be necessary for the RS to modify the length of the inter-frame gap in order to align the Start control character to lane 0. One possible way to do this is for the RS to always insert additional idle characters to align the Start control character. However, this method will reduce the effective data rate for frames with minimum inter-frame spacing. Another method allows the RS to insert and delete idle characters to align the Start to lane 0. When using this method, the RS maintains a Deficit Idle Count (DIC) that represents the cumulative count of idle characters inserted or deleted. The count has a minimum value of zero, and a maximum value of three. The DIC is incremented when idle characters are deleted, and decremented when idle characters are inserted. This means that up to three idle characters can be deleted at a time, thus shrinking the minimum inter-frame gap to 9 idle characters. It should also be noted that the average minimum inter-frame gap remains at 12 idle characters. The following table depicts the proper behavior for a device implementing DIC:

Packet Length Modulo 4	Current DIC = 0		Current DIC = 1		Current DIC = 2		Current = 3	
	IPG Length	New DIC value	IPG Length	New DIC value	IPG Length	New DIC value	IPG Length	New DIC value
n+0	12	0	12	1	12	2	12	3
n+1	11	1	11	2	11	3	15	0
n+2	10	2	10	3	14	0	14	1
n+3	9	3	13	0	13	1	13	2

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

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Procedure:

Test cases (Note that each test case is preceded by a large amount of idle.)

1. 64-byte frame, 12 bytes IPG, 64-byte frame, 12 bytes IPG, 512-byte frame
 2. 65-byte frame, 11 bytes IPG, 64-byte frame, 12 bytes IPG, 512-byte frame
 3. 66-byte frame, 10 bytes IPG, 64-byte frame, 12 bytes IPG, 512-byte frame
 4. 67-byte frame, 09 bytes IPG, 64-byte frame, 12 bytes IPG, 512-byte frame
 5. 64-byte frame, 12 bytes IPG, 65-byte frame, 11 bytes IPG, 512-byte frame
 6. 65-byte frame, 11 bytes IPG, 65-byte frame, 11 bytes IPG, 512-byte frame
 7. 66-byte frame, 10 bytes IPG, 65-byte frame, 11 bytes IPG, 512-byte frame
 8. 67-byte frame, 09 bytes IPG, 65-byte frame, 15 bytes IPG, 512-byte frame
 9. 64-byte frame, 12 bytes IPG, 66-byte frame, 10 bytes IPG, 512-byte frame
 10. 65-byte frame, 11 bytes IPG, 66-byte frame, 10 bytes IPG, 512-byte frame
 11. 66-byte frame, 10 bytes IPG, 66-byte frame, 14 bytes IPG, 512-byte frame
 12. 67-byte frame, 09 bytes IPG, 66-byte frame, 14 bytes IPG, 512-byte frame
 13. 64-byte frame, 12 bytes IPG, 67-byte frame, 09 bytes IPG, 512-byte frame
 14. 65-byte frame, 11 bytes IPG, 67-byte frame, 13 bytes IPG, 512-byte frame
 15. 66-byte frame, 10 bytes IPG, 67-byte frame, 13 bytes IPG, 512-byte frame
 16. 67-byte frame, 09 bytes IPG, 67-byte frame, 13 bytes IPG, 512-byte frame
1. The testing station is instructed to transmit three properly encapsulated, valid request frames and idle sequences as defined in test case 1. This will cause the DUT to transmit three replies to the testing station.
 2. The testing station is instructed to capture and observe all transmissions from the DUT.
 3. Parts 1 and 2 are repeated using the different test cases listed above.

Observable results:

- a. The gap between the second and third frames of test case 1 should be 12 bytes of IPG.
- b. The gap between the second and third frames of test case 2 should be 12 bytes of IPG.
- c. The gap between the second and third frames of test case 3 should be 12 bytes of IPG.
- d. The gap between the second and third frames of test case 4 should be 12 bytes of IPG.
- e. The gap between the second and third frames of test case 5 should be 11 bytes of IPG.
- f. The gap between the second and third frames of test case 6 should be 11 bytes of IPG.
- g. The gap between the second and third frames of test case 7 should be 11 bytes of IPG.
- h. The gap between the second and third frames of test case 8 should be 15 bytes of IPG.
- i. The gap between the second and third frames of test case 9 should be 10 bytes of IPG.
- j. The gap between the second and third frames of test case 10 should be 10 bytes of IPG.
- k. The gap between the second and third frames of test case 11 should be 14 bytes of IPG.
- l. The gap between the second and third frames of test case 12 should be 14 bytes of IPG.
- m. The gap between the second and third frames of test case 13 should be 9 bytes of IPG.
- n. The gap between the second and third frames of test case 14 should be 13 bytes of IPG.
- o. The gap between the second and third frames of test case 15 should be 13 bytes of IPG.
- p. The gap between the second and third frames of test case 16 should be 13 bytes of IPG.

Possible Problems: If the DUT does not support DIC, then this test cannot be done.

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Group2: Transmission

Scope: The following tests cover the transmission of data at the Reconciliation Sublayer.

Purpose: These tests are designed to verify that the device under test reacts properly to transmission of data, both valid and invalid, at the Reconciliation Sublayer.

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Test 46.2.1 – Reception of Start control character

Purpose: To verify that the DUT only accepts frames with proper Start control character alignment.

References:

- IEEE Draft P802.3ae/D5.0 - subclauses 4.2.5, 46.1.7.1.4, 46.2.2, and 46.2.5

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

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Discussion: An RS that is transmitting a frame is required to align the start of frame to lane 0. Once the RS has transmitted a frame, there is no valid mechanism to realign the start of frame to any other lane. Therefore, any frame that is received with a start character on a lane other than lane 0 indicates that an error condition is present and the frame should be discarded.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

Procedure:

1. The testing station is instructed to transmit a properly encapsulated, valid 64-byte request frame with the Start control character aligned to lane 0.
2. The testing station is instructed to transmit a properly encapsulated, valid 512-byte request frame with the Start control character aligned to lane 1.
3. The testing station is instructed to transmit a properly encapsulated, valid 64-byte request frame with the Start control character aligned to lane 0.
4. Capture and observe transmissions from the DUT.
5. Repeat steps 1 through 4 changing the Start control character alignment in step 2 to lane two, three, and four.

Observable results:

- a. The DUT should reply to all frames sent in steps 1 and 3.
- b. The DUT should not reply to any frames sent in step 2.

Possible Problems: This test cannot be done if the DUT supports a 10GBASE-R PCS, as there is in permissible encoding for an /S/ on any lane other than lane 0.

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Test 46.2.2 – Reception of Preamble and SFD

Purpose: To determine if the DUT is (in)sensitive to preamble shrinkage or growth.

References:

- IEEE Draft P802.3ae/D5.0 - subclauses 4.2.9 process BitReceiver and procedure PhysicalSignalDecap, 46.3.3.3

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

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Discussion: Preamble is not required for clock synchronization in a 10GBASE-X/R/W frame. Therefore the DUT should not be sensitive to its size. When the MAC is receiving a frame, the process BitReceiver first calls the procedure PhysicalSignalDecap. This procedure receives one bit a time from the physical medium and discards all bits until a valid SFD is detected. At this point the BitReceiver process accepts bits while the receiveDataValid signal is asserted and the frame is not finished. However, a 10 Gb/s MAC/RS implementation is not required to process a packet that has an SFD in a position other than lane 3 of the column following the column containing the Start control character. This test seeks to determine the (in)sensitivity of the MAC/RS to reception of preamble.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

Procedure:

1. The testing station is instructed to transmit a request frame with only a Start Control character and SFD present. The output from the DUT is observed.
2. Step 1 is repeated inserting additional preamble between the Start Control character and SFD until the length of the Start, preamble, and SFD is 16 octets long.

Observable results:

- a. The DUT should reply to all frames containing a properly aligned Start control character in lane 0, 6 bytes of preamble, and an SFD in lane 3.
- b. The DUT may reply to frames with preamble variations other than those listed in part (a).

Possible Problems: None

Test 46.2.3 – Reception of Terminate control character

Purpose: Verify that the DUT can receive frames containing the Terminate control character in any lane.

References:

- IEEE Draft P802.3ae/D5.0- subclauses 46.2.4, 46.2.5

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

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Discussion: A DUT is allowed to transmit any size frame from 64-1518 bytes in length. Therefore, it is possible for the frame to terminate on any of the four lanes. A receiver must therefore be insensitive to the Terminate control character being on any lane.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

Procedure:

1. The testing station is instructed to transmit a properly encapsulated, valid 512-byte request frame. This will cause the DUT to transmit a reply, indicating that the DUT is functioning properly.
2. The testing station is instructed to capture and observe all transmissions from the DUT.
3. Parts 1 and 2 are repeated, using randomly sized frames to force the Terminate control character to randomly appear on any one of the four lanes.

Observable results:

- a. The DUT should reply to frames containing the Terminate control character on any of the four lanes.

Possible Problems: None

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Test 46.2.4 – Receive IFG Tolerance

Purpose: Verify that the DUT can properly receive frames with an IFG between 5 and 12 bytes in length.

References:

- IEEE Draft P802.3ae/D5.0- subclauses 4.4.2, 46.2.1, 46.3.1.4

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

Last Modification: June 25, 2002

Discussion: It may be necessary for the RS to modify the length of the inter-frame gap in order to align the Start control character to lane 0. One method allows the RS to insert and delete idle characters to align the Start to lane 0. When using this method, the RS maintains a Deficit Idle Count (DIC) that represents the cumulative count of idle characters inserted or deleted. This means that up to three idle characters can be deleted at a time, thus shrinking the minimum inter-frame gap to 9 idle characters. However, due to variable network delays and clock tolerances, it is possible for an additional column of Idle to be deleted, which would shrink the minimum IFG to 5 bytes. Taking both DIC and clock compensation into consideration, the minimum IFG can vary from 5 to 12 bytes under normal network conditions.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

Procedure:

1. The testing station is instructed to transmit a 64-byte valid request frame followed by a total of 5 bytes of IFG.
2. The testing station is instructed to transmit a 64-byte request frame followed by a total of 12 bytes of IFG.
3. The testing station is instructed to transmit a 64-byte request frame followed by a large amount of IFG.
4. The testing station is instructed to capture and observe all transmissions from the DUT.
5. Parts 1 through 4 are repeated increasing the IFG in part 1 up to 12 bytes in length.

Observable results:

- a. For every tested value of IFG, the DUT should reply to all three frames.

Possible Problems: None

Test 46.2.5 – Assertion of DATA_VALID_STATUS

Purpose: Verify that the DUT does not accept frames that are preceded by anything but a fill column of Idle or a sequence ordered set.

References:

- IEEE Draft P802.3ae/D5.0 – subclause 46.1.7.5.3

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

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Discussion: The receiving RS sets the values of the PLS_DATA_VALID.indicate service primitive whenever the DATA_VALID_STATUS parameter changes. The parameter can take on a value of DATA_VALID or DATA_NOT_VALID, depending on the data received. When a start control character is received on lane 0, the RS sets DATA_VALID_STATUS to DATA_VALID if the previous column contained four valid Idle characters, or a Sequence ordered set. Anything else received by the RS in the column preceding the start control character will cause the RS to set DATA_VALID_STATUS to DATA_NOT_VALID.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

Procedure:

Test Cases:

1. A full column of Idle
 2. A sequence ordered set corresponding to Local Fault
 3. A sequence ordered set corresponding to Remote Fault
 4. A sequence ordered set corresponding to a reserved value
 5. A column containing a Terminate control character
 6. A column containing a Start control character
 7. A column containing an Error control character
 8. A column containing Data code groups
1. The testing station is instructed to transmit a valid 64-byte request frame followed by 12 bytes of Idle.
 2. The testing station is instructed to transmit the pattern described in Test Case 1.
 3. The testing station is instructed to transmit a valid 512-byte request frame followed by 12 bytes of Idle, followed by a 64-byte request frame.
 4. The testing station is instructed to capture and observe all transmissions from the DUT.
 5. Repeat steps 1 through 4 for all listed test cases.

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Observable results:

- a. The DUT should reply to all three frames in test case 1.
- b. The DUT should reply to all three frames in test case 2.
- c. The DUT should reply to all three frames in test case 3.
- d. The DUT should reply to all three frames in test case 4.
- e. The DUT should reply to only the two 64-byte frames in test case 5.
- f. The DUT should reply to only the two 64-byte frames in test case 6.
- g. The DUT should reply to only the two 64-byte frames in test case 7.
- h. The DUT should reply to only the two 64-byte frames in test case 8.

Possible Problems: If the DUT supports a 10GBASE-R PCS, then parts 6 through 8 cannot be tested.

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Test 46.2.6 – De-assertion of DATA_VALID_STATUS

Purpose: Verify that the DUT properly discards frames when DATA_VALID_STATUS changes from DATA_VALID to DATA_NOT_VALID before the reception of a Terminate control character when receiving a frame.

References:

- IEEE Draft P802.3ae/D5.0 – subclause 46.1.7.5.3

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

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Discussion: The receiving RS sets the values of the PLS_DATA_VALID.indicate service primitive whenever the DATA_VALID_STATUS parameter changes. This parameter can take on a value of DATA_VALID or DATA_NOT_VALID, depending on the data received. Upon reception of a valid start control character in lane 0 that is preceded by a full column of Idle or a Sequence ordered set, the RS will set DATA_VALID_STATUS to DATA_VALID. As long as no errors occur in the frame, the DUT will set DATA_VALID_STATUS to DATA_NOT_VALID upon the reception of a Terminate control character. When DATA_VALID_STATUS takes on a value of DATA_NOT_VALID for any control character other than a Terminate, the RS will make sure that the MAC detects a CRC error in the frame.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

Procedure:

1. Instruct the testing station to transmit a properly formed 64-byte request frame to the DUT followed by minimum IPG.
2. Instruct the testing station to transmit a 512-byte request frame to the DUT that replaces the terminate control character with an idle control character followed by minimum IPG.
3. Instruct the testing station to transmit a properly formed 64-byte request frame to the DUT followed by minimum IPG.
4. Observe all transmissions from the DUT.
5. Repeat steps 1 through 4 replacing the Terminate control character in part 2 with an /O/, and then with an /S/.

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Observable results:

- a. The DUT should reply to both 64-byte frames, discard the 512-byte frame in Part 2 when the /T/ is replaced with an /I/, and should increment its CRC error counter.
- b. The DUT should reply to both 64-byte frames, discard the 512-byte frame in Part 2 when the /T/ is replaced with an /O/, and should increment its CRC error counter.
- c. The DUT should reply to both 64-byte frames, discard the 512-byte frame in Part 2 when the /T/ is replaced with an /S/, and should increment its CRC error counter.

Possible Problems: If the DUT implements a 10GBASE-R PCS, then this test cannot be done, as there is no valid encoding for a frame to end without a /T/.

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Test 46.2.7 – Reception of /E/ during DATA_VALID_STATUS

Purpose: Verify that the DUT properly discards frames that are received with errors and increments its CRC error counters.

References:

- IEEE Draft P802.3ae/D5.0 – subclauses 46.1.7.5.3, 46.3.3.1

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

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Discussion: During frame reception, if a control character other than a Terminate control character is received, then the RS will make sure that the MAC indicates a CRC error for that frame. In particular, if the received frame contains one or more Error control characters in an otherwise valid frame, the RS must still cause the MAC to detect a CRC error.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

Procedure:

1. Instruct the testing station to transmit a properly formed 64-byte request frame to the DUT followed by minimum IPG.
2. Instruct the testing station to transmit a 512-byte request frame to the DUT that contains an /E/ before the /T/ followed by minimum IPG.
3. Instruct the testing station to transmit a properly formed 64-byte request frame to the DUT followed by minimum IPG.
4. Observe all transmissions from the DUT.

Observable results:

- a. The DUT should reply to both 64-byte frames, discard the 512-byte frame, and increment its CRC error counter.

Possible Problems: None

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Group3: Fault

Scope: The following tests cover the reception and transmission of fault sequences at the Reconciliation Sublayer.

Purpose: These tests are designed to verify that the device under test reacts properly to receipt and transmission of fault sequences, both valid and invalid, at the Reconciliation Sublayer.

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Test 46.3.1 – Continuous Reception of Fault Sequences

Purpose: Verify that the DUT properly reacts upon the continuous reception of either Local Fault or Remote Fault ordered_sets.

References:

- IEEE Draft P802.3ae/D5.0- subclause 46.3.4

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

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Discussion: Clause 46 clearly states “Sublayers within the PHY are capable of detecting faults that render a link unreliable for communication. Upon recognition of a fault condition a PHY sublayer indicates Local Fault status on the data path. When this Local Fault status reaches an RS, the RS stops sending MAC data, and continuously generates a Remote Fault status on the transmit data path (possibly truncating a MAC frame being transmitted). When an RS receives Remote Fault status, the RS stops sending MAC data, and continuously generates Idle control characters. When the RS no longer receives fault status messages, it returns to normal operation, sending MAC data.” The following table depicts the defined fault messages:

Lane 0	Lane 1	Lane 2	Lane 3	Description
Sequence	0x00	0x00	0x00	Reserved
Sequence	0x00	0x00	0x01	Local Fault
Sequence	0x00	0x00	0x02	Remote Fault
Sequence	≥0x00	≥0x00	≥0x03	Reserved

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

Procedure:

1. Establish a link between the testing station and the DUT.
2. Instruct the DUT to transmit frames continuously to the testing station with a minimum IFG.
3. Instruct the testing station to continuously transmit Remote Fault sequences to the DUT.
4. Observe all transmissions from the DUT.
5. Repeat steps 1 through 4 with the testing station transmitting Local Fault sequences.
6. Repeat steps 1 through 4 with the testing station transmitting Reserved sequences.

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Observable results:

- a. Upon reception of the Local Fault sequences, the DUT should cease transmission of frames and commence continuous transmission of Remote Fault sequences.
- b. Upon reception of the Remote Fault sequences, the DUT should cease transmission of frames and commence continuous transmission of Idle.
- c. Upon reception of the Reserved sequences, the DUT should not cease transmission of frames.

Possible Problems: If the DUT implements XAUI or 10GBASE-LX4, then the testing station will not receive continuous Remote Fault sequences from the DUT as described in part a.

Test 46.3.2 – Reception of Identical fault_sequences

Purpose: Determine the number of identical fault_sequences that the DUT needs to receive before acknowledging the reception of Local or Remote fault.

References:

- IEEE Draft P802.3ae/D5.0 - subclauses 46.3.4, 46.3.4.3

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

Last Modification: June 25, 2002

Discussion: The link fault signaling state machine specifies that the RS will recognize a link_fault when 4 fault_sequences containing the same value have been received without receiving any other fault_sequences in a period of 128 columns or less. Upon reception of a fault_sequence, the RS begins a count with the *col_cnt* variable. Upon reception of four local fault or four remote fault fault_sequences, the DUT sets the link_fault variable to Local Fault or Remote Fault, respectively.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

Procedure:

Case 1. DUT implements at most one XGXS.

1. The testing station is instructed to send a valid 64-byte request frame to the DUT followed by a minimum IPG.
2. The testing station is instructed to send 1 Local Fault ordered_set to the DUT.
3. The testing station is instructed to send a valid 1518-byte request frame to the DUT followed by a minimum IPG and a valid 64-byte request frame.
4. Observe all transmissions from the DUT.
5. Repeat parts 1 through 4 increasing the number of ordered_sets until the DUT stops replying to the 1518-byte frame. Let the number of ordered_sets be referred to as *n*.
6. Repeat parts 1 through 5 sending Remote Fault ordered_sets to the DUT.
7. Repeat parts 1 through 5 using *n* identical ordered_sets that are neither Local Fault nor Remote Fault.

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Case 2. DUT implements more than one XGXS.

1. The testing station is instructed to send a valid 64-byte request frame to the DUT followed by a minimum IPG.
2. The testing station is instructed to send 1 column of ||A||, followed by 1 Local Fault ordered_set, followed by 30 columns of ||R|| and/or ||K|| to the DUT.
3. Repeat part 2 zero times.
4. The testing station is instructed to send a valid 1518-byte request frame to the DUT followed by a minimum IPG.
5. The testing station is instructed to send a valid 64-byte request frame to the DUT.
6. Observe all transmissions from the DUT.
7. Repeat parts 1 through 6 increasing the number of times that part 2 is done in part 3 until the DUT does not reply to the 1518-byte frame. Let the number of ordered_sets be referred to as n .
8. Repeat parts 1 through 7 sending Remote Fault ordered_sets to the DUT.
9. Repeat parts 1 through 7 sending n identical ordered_sets that are neither Local Fault nor Remote Fault to the DUT.

Observable results:

Case 1 and Case 2.

- a. The DUT should reply to both 64-byte frames and the 1518-byte frame when the value of n is less than 4 and the ordered_sets represent Local Fault or Remote Fault fault_sequences.
- b. The DUT should reply to both 64-byte frames but not to the 1518-byte frame when the value of n is greater than or equal to 4 and the ordered_sets represent Local Fault or Remote Fault fault_sequences.
- c. The DUT should reply to both 64-byte frames and the 1518-byte frame regardless of the value of n when the ordered_sets represent neither Local Fault nor Remote Fault fault_sequences.

Possible Problems: If the DUT does not properly increment the col_cnt variable, then Case 2 cannot be done, as this test case relies on the DUT to be able to recognize reception of fault_sequences within 128 columns.

Test 46.3.3 – Reception of non-identical fault sequences

Purpose: Verify that the DUT properly resets the seq_cnt variable to 0 upon reception of non-identical ordered_sets.

References:

- IEEE Draft P802.3ae/D5.0 - subclauses 46.3.4, 46.3.4.3

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

Last Modification: June 25, 2002

Discussion: The link fault signaling state machine specifies that the RS will recognize a link_fault when 4 fault_sequences containing the same value have been received without receiving any other fault_sequences in a period of 128 columns or less. Upon reception of a fault_sequence, the RS begins a count with the col_cnt variable. Upon reception of four local fault or four remote fault fault_sequences, the DUT sets the link_fault variable to Local Fault or Remote Fault, respectively. However, when four identical fault_sequences are not received, the seq_cnt variable is reset to zero, and the DUT should not set the link_fault variable.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

Procedure:

For this test, the value of n is obtained from test 46.3.2.

Case 1. DUT implements at most one XGXS.

1. Instruct the testing station to transmit a valid 64-byte frame to the DUT followed by a minimum IPG.
2. Instruct the testing station to transmit $n-1$ Local Fault ordered_sets followed by m Remote Fault ordered_sets (where m is initially set to 1), followed by 1 Local Fault ordered_set.
3. Instruct the testing station to transmit a valid 1518-byte frame to the DUT followed by a minimum IPG.
4. Instruct the testing station to transmit a valid 64-byte frame to the DUT.
5. Observe all transmissions from the DUT.
6. Repeat steps 1 through 5 increasing the value of m in step 2 until $m = n$.
7. Repeat steps 1 through 6 transmitting $n-1$ Remote Fault ordered_sets followed by m Local Fault ordered_sets, and 1 Remote Fault ordered_set, resetting m to 1.

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8. Repeat steps 1 through 6 transmitting $n-1$ Local Fault ordered_sets followed by m Reserved ordered_sets, and 1 Local Fault ordered_set, resetting m to 1.
9. Repeat steps 1 through 6 transmitting $n-1$ Remote Fault ordered_sets followed by m Reserved ordered_sets, and 1 Remote Fault ordered_set, resetting m to 1.
10. Repeat steps 1 through 5 replacing step 2 with the following: Instruct the testing station to transmit 1 Local Fault ordered_set followed by 1 Remote Fault ordered_set. Repeat this pattern such that the total number of ordered_sets is $2*n$.
11. Repeat step 10 with 1 Local Fault ordered_set followed by 1 Reserved ordered_set.
12. Repeat step 10 with 1 Remote Fault ordered_set followed by 1 Reserved ordered_set.

Case 2. DUT implements more than one XGXS.

1. Repeat Case 1 but insert a column of ||A|| before every ordered_set, and 31 columns of ||K|| or ||R|| between all ||A||.

Observable results:

- a. The DUT should reply to both 64-byte frames and to the 1518-byte frame when the value of m is less than the value of n when the testing station is transmitting $n-1$ Local Fault ordered_sets followed by m Remote Fault ordered_sets and 1 Local Fault ordered_set, vice-versa.
- b. The DUT should not reply to the 1518-byte frame when the value of m is greater than or equal to the value of n when the testing station is transmitting $n-1$ Local Fault ordered_sets followed by m Remote Fault ordered_sets and 1 Local Fault ordered_set, or vice-versa.
- c. The DUT should reply to both 64-byte frames and to the 1518-byte frame regardless of the value of m in parts 8 and 9, and regardless of whether the Reserved ordered_sets are surrounded by Local or Remote Fault ordered_sets.
- d. The DUT should reply to both 64-byte frames and to the 1518-byte frame in part 10.
- e. The DUT should not reply to the 1518-byte frame in parts 11 and 12.

Possible Problems: None

Test 46.3.4 – Setting of col_cnt

Purpose: Verify that the DUT properly uses the col_cnt variable.

References:

- IEEE Draft P802.3ae/D5.0 - subclauses 46.3.4, 46.3.4.3

Resource Requirements:

- A testing station capable of encoding (decoding) 8 bit octets to (from) 10-bit code groups as specified in clause 48 and sending (receiving) these code groups using the signaling method described in clause 53, or a testing station capable of encoding (decoding) 64-bit words to (from) 66-bit words as specified in clause 49 and optionally clause 50, and sending (receiving) these code groups using the signaling method described in clause 52.

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Discussion: The link fault signaling state machine specifies that the RS will recognize a link_fault when 4 fault_sequences containing the same value have been received without receiving any other fault_sequences in a period of 128 columns or less. Upon reception of a fault_sequence, the RS begins a count with the col_cnt variable. Upon reception of four local fault or four remote fault fault_sequences, the DUT sets the link_fault variable to Local Fault or Remote Fault, respectively. However, when four identical fault_sequences are not received within 128, the col_cnt variable is reset to zero, and the DUT should not set the link_fault variable.

Test Setup: Connect the device under test (DUT) to the testing station (transmit to receive, receive to transmit) with the appropriate medium (i.e. multi-mode fiber or single mode fiber).

Procedure:

For this test, the value of n is taken from test 46.3.2.

Case 1. DUT implements at most one XGXS.

Part 1. The DUT has not yet set link_fault to Local Fault or Remote Fault

1. The testing station is instructed to send a valid 64-byte request frame to the DUT followed by a minimum IPG.
2. The testing station is instructed to send n Local Fault ordered_sets to the DUT, with m columns of Idle between each ordered_set, where m is initially set to less than 128.
3. The testing station is instructed to send a valid 1518-byte request frame to the DUT followed by a minimum IPG.
4. The testing station is instructed to send a valid 64-byte request frame to the DUT.
5. Observe all transmissions from the DUT.
6. Repeat parts 1 through 5 increasing the value of m until the DUT replies to the 1518-byte frame.
7. Repeat parts 1 through 6 sending Remote Fault ordered_sets to the DUT.

Part 2. The DUT has set link_fault to Local Fault or Remote Fault

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8. The testing station is instructed to send a valid 64-byte request frame to the DUT followed by a minimum IPG.
9. The testing station is instructed to send n Local Fault ordered_sets to the DUT followed by $m-1$ columns of Idle, where m is the final value obtained in step 6.
10. The testing station is instructed to send a valid 1518-byte request frame to the DUT followed by a minimum IPG.
11. The testing station is instructed to send a valid 64-byte request frame to the DUT.
12. Observe all transmissions from the DUT.
13. Repeat parts 8 through 12 sending m columns of Idle.
14. Repeat parts 8 through 13 sending Remote Fault ordered_sets.

Case 2. DUT implements more than one XGXS

Part 1. The DUT has not set link_fault to Local Fault or Remote Fault.

1. The testing station is instructed to send two valid 64-byte request frames to the DUT separated by a minimum IPG, where the third column (bytes 9-12) of the IPG contains a Local Fault ordered_set, and the second frame is followed by a minimum IPG.
2. The testing station is instructed to send k columns of Idle, where k is initially set to less than 87.
3. The testing station is instructed to send two valid 64-byte request frames to the DUT separated by a minimum IPG, where the third column (bytes 9-12) of the IPG contains a Local Fault ordered_set, and the second frame is followed by a minimum IPG.
4. The testing station is instructed to send a valid 1518-byte request frame to the DUT followed by a minimum IPG.
5. The testing station is instructed to send a valid 64-byte request frame to the DUT.
6. Observe all transmissions from the DUT.
7. Repeat steps 1 through 6 and increase the value of k until the DUT replies to the 1518-byte frame.
8. Repeat steps 1 through 6, resetting k , and sending Remote Fault ordered_sets to the DUT.

Part 2. The DUT has set link_fault to Local Fault or Remote Fault

9. The testing station is instructed to send a valid 64-byte request frame to the DUT followed by a minimum IPG.
10. The testing station is instructed to send n Local Fault ordered_sets to the DUT spaced 31 columns apart. followed by $k+40$ columns of Idle, where k is the final value obtained in step 7.
11. The testing station is instructed to send a valid 1518-byte request frame to the DUT followed by a minimum IPG.
12. The testing station is instructed to send a valid 64-byte request frame to the DUT.
13. Observe all transmissions from the DUT.
14. Repeat parts 9 through 13 sending $k+41$ columns of Idle.
15. Repeat parts 9 through 14 sending Remote Fault ordered_sets.

Observable results:

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Case 1.

- a. In Part 1, when the value of m is below 128, the DUT should not reply to the 1518-byte frame.
- b. In Part 1, when the value of m is greater than or equal to 128, the DUT should reply to the 1518-byte frame.
- c. In Part 2, when $m-1$ columns of Idle are being sent to the DUT, the DUT should not reply to the 1518-byte frame.
- d. In Part 2, when m columns of Idle are being sent to the DUT, the DUT should reply to the 1518-byte frame.

Case 2.

- a. In Part 1, when the value of k is below 87, the DUT should not reply to the 1518-byte frame.
- b. In Part 1, when the value of k is greater than or equal to 87, the DUT should reply to the 1518-byte frame.
- c. In Part 2, when $k+40$ columns of Idle are being sent to the DUT, the DUT should not reply to the 1518-byte frame.
- d. In Part 2, when $k+41$ columns of Idle are being sent to the DUT, the DUT should reply to the 1518-byte frame.

Possible Problems: When testing Case 2, it may be necessary to send a single additional frame before the test is started to assure that the PHY XGXS of the DUT will transmit a column of $\|K\|$ to the DTE XGXS following the first frame of the test.