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ID-337I2 TEST REPORT

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DEVICE AND TEST PLAN INFORMATION	
Device Under Test (DUT)	Acme Co. Super CPE
Test Specification/Suite	ID-337i2, Version Test Script 1.0,
UNH-IOL Test ID	1234
This testing pertains to a set of standard requirements, put forth in Issue 2 of the Broadband Forum ID-337.	

CONTACT INFORMATION		
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Report Created by	Lincoln Lavoie	lylavoie@iol.unh.edu
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UNH-IOL
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SUMMARY OF RESULTS

The following table contains a summary of results other than PASS. The definition of result types can be found in the Result Key.

TEST NUMBER & LABEL	RESULTS
No non-passing results were uncovered during testing.	

TESTING NOTES

NOTES
None

REVISION HISTORY

The following table contains a revision history for this report.

REVISION	DATE	AUTHOR	EXPLANATION
1.0	2021-01-08 09:03:03	Lincoln Lavoie	Initial Report.

DEVICE UNDER TEST AND INITIALIZATION INFORMATION

The following table contains the state of the DUT during testing.

COMPONENT	DESCRIPTION
NAME	Acme Co. Super CPE
IOL ID	12345
MODEL	Super CPE
MAKE	Acme Co.
SOFTWARE VERSION	1.0.0
SERIAL NUMBER	abcd12345
CHIPSET MAKE	chip_maker_1
CHIPSET FIRMWARE VERSION	firmware_1
CHIPSET MODEL	model_1

TEST TOOL AND ENVIRONMENT INFORMATION

The following table contains the test tool and test suite versions used during testing.

TOOL	Version
Test Tool	Test Sentinel-REL:2.1.0.8193 Build:8c54c101c IR

TESTBED DEVICES

The following tables contain devices from the UNH-IOL testbed used during testing.

Acme Co. Super DPU	
USED FOR	CO
IOL ID	123456
MODEL	Acme Co. Super DPU
MAKE	Acme Co.
CHIPSET MAKE	chip_maker_2
SERIAL NUMBER	abcd123456
CHIPSET FIRMWARE VERSION	firmware_2
SOFTWARE VERSION	1.0.0
DSLAM LINECARD PORT	1,2,3,4,5,6,7,8
CHIPSET MODEL	chip_model_2

Model 501 Gfast Digital Analyzer	
USED FOR	Spectrum_Analyzer
IOL ID	24354
MODEL	Model 501 Gfast Digital Analyzer
MAKE	Telebyte, Inc.
SERIAL NUMBER	TB7302

SPT-9000A	
USED FOR	Traffic_Generator
IOL ID	14421
MODEL	SPT-9000A
MAKE	Spirent Communications
SERIAL NUMBER	E10040489

Model 4902 Gfast Universal Noise Generator	
USED FOR	AWG
IOL ID	24348
MODEL	Model 4902 Gfast Universal Noise Generator
MAKE	Telebyte, Inc.
SERIAL NUMBER	TB7304
IMPAIRMENT AWG DEVICE IDENTIFIER	TB7963
IMPAIRMENT AWG NOISE IDENTIFIER	2.20.1
IMPAIRMENT AWG COUPLING CIRCUIT	Telebyte 4902-2-300-TPD

Model CFA-24 Gfast Transparent Cable Farm Automation Switch	
USED FOR	XTLK
IOL ID	24962
MODEL	Model CFA-24 Gfast Transparent Cable Farm Automation Switch
MAKE	Telebyte, Inc.
SERIAL NUMBER	TB7357

RESULTS

The following table contains all results from testing. Detailed test results including observed behaviors can be found in the Detailed Test Results.

TEST NUMBER & LABEL	RESULTS
6.1 Inventory Data Test	PASS
6.2.1 PSD Limit Mask Test	PASS
6.2.2 Sub-carrier Masking Test	PASS
6.2.3 PSD Shaping Test	PASS
6.2.4 RFI Notching Test	PASS
6.2.5 UPBO Test	PASS
6.2.6 TIGA Test	PASS
6.3.1 TDD Inter-frame Gap Test	PASS
6.3.2 DS and US Ratio Configuration (Mds) Test	PASS
6.4 Accelerated MTBE Test	PASS
6.6 Bit Swap Test	PASS
6.7 SRA Downshift Test	PASS
6.8 SRA Upshift Test	PASS
6.9 FRA & SRA Upshift Test	PASS
6.10 RPA Test	PASS
6.11 RMC Bit Loading Configuration Test	PASS
6.12.1 Re-Initialization Policy Short disconnect Test	PASS
6.12.2 Re-Initialization Policy Long disconnect Test with LOS failure	PASS
6.12.3 Re-Initialization Policy Long disconnect Test with LOR failure	PASS
6.13 Dying Gasp Test	PASS
6.14 Increased Bit Loading Test	PASS
6.15 Test Parameters Test	PASS
7.1 SHINE Stability Test	PASS

7.2 REIN Stability Test	**PASS**
7.3 Fluctuating Broadband RFI Noise Present at Initialization Test	**PASS**
7.4 Stationary Broadband RFI Noise Present at Initialization Test	**PASS**
7.5 Fluctuating Broadband RFI Noise Present at Showtime Test	**PASS**
7.6 Stationary Broadband RFI Noise Present at Showtime Test	**PASS**
8.1.1 Single-line Basic Throughput Test	**PASS**
8.1.1 Single-line Basic Throughput Test (HNBT Loop)	**PASS**
8.2.1 Multi-line Basic Throughput Test	**PASS**
8.2.2 Multi-line Disorderly Shutdown Test	**PASS**

DETAILED TEST RESULTS

6.1 Inventory Data Test	PARTS	RESULTS
		PASS
PURPOSE		
The purpose of this test is to verify that the DUT inventory information retrieved from the DPU-MIB corresponds with the DUT vendor declared inventory information.		

Results - Inventory	
DPU Inventory Information	
FTU-O_GHS_VENDOR	tQBCRENNTVQ
DPU_System_VERSION	MjAuMS4zLjAgVkVfMTNfMF8xMwAAAAAAAAAAAAAAAAA
x_SYSTEM_VENDOR	DwBOT0tCAAA
x_SYSTEM_SERIALNR	QUEyMDI3RlMySEItMDE
CPE Inventory Information	
FTU-R_GHS_VENDOR	tQBCRENNANQ
CPE System_VERSION	MDQ2dCBBMnB2ZmJLAAAAAA
x_SYSTEM_VENDOR	Tk9LQgAAAAA
x_SYSTEM_SERIALNR	TTExMjAzOEEwMDAwMDAzNSBGLTAxMEctRiBBQUFCMTI

Summary		
The retrieved inventory data SHALL be identical to the vendor declared inventory data.	Reported Values Match	pass

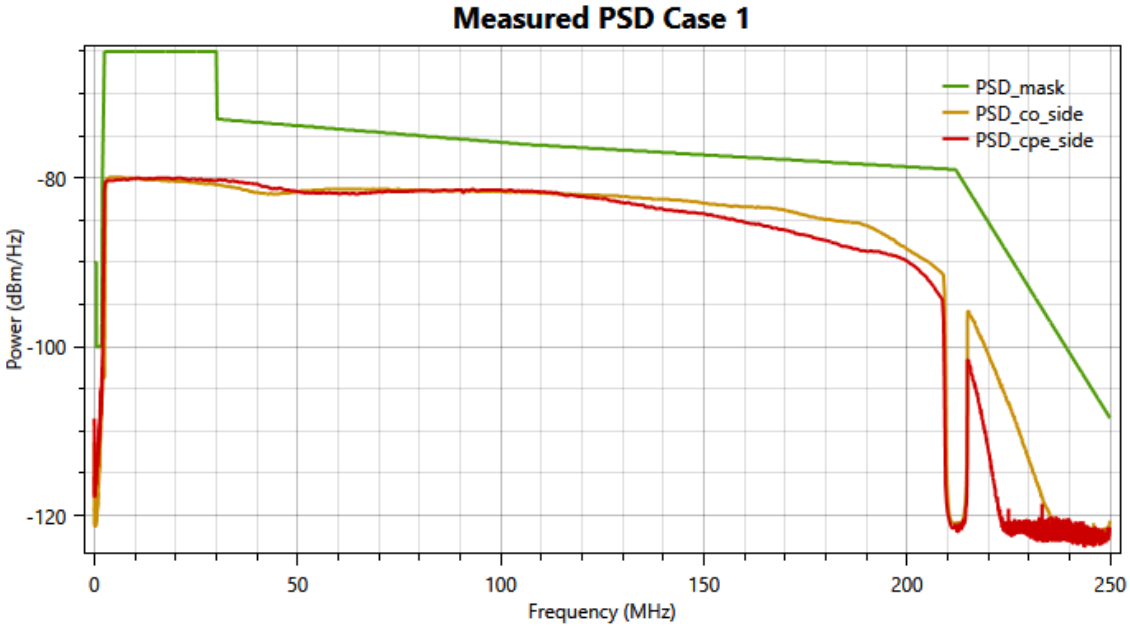
6.2.1 PSD Limit Mask Test	PARTS	RESULTS
		PASS

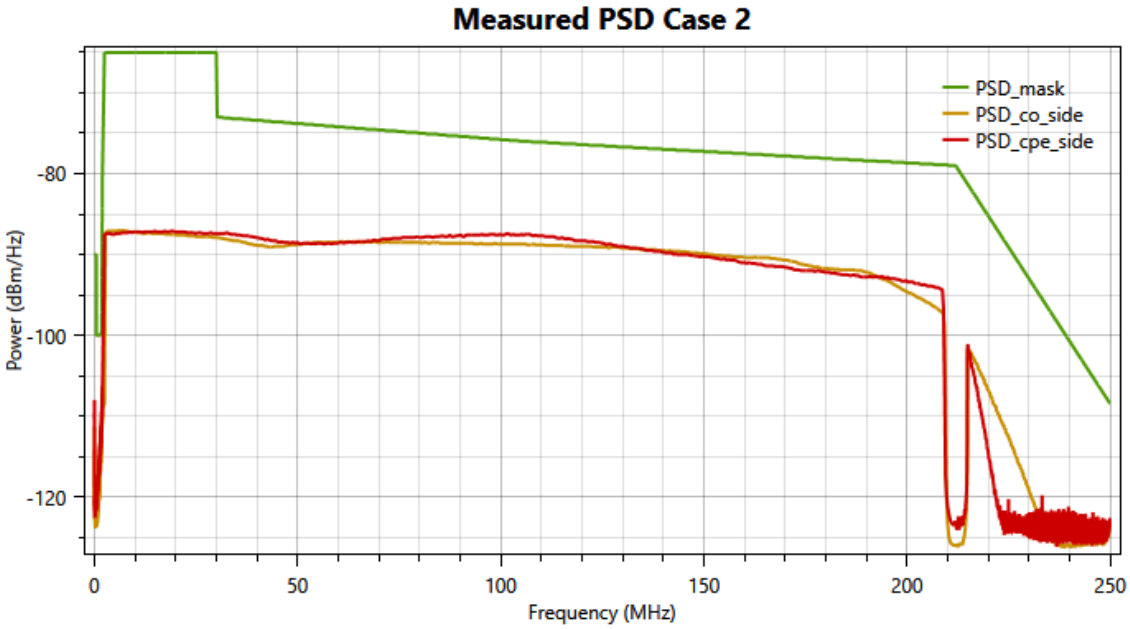
PURPOSE

The purpose of this test is to verify that the signals transmitted by the DUT do not exceed the limit PSD mask (LPM) and maximum aggregate transmit power (MAXATP), when no additional spectral controls or restrictions have been configured.

Results - PSD Limit Mask Case 1	
NDRds (kbit/s)	1499053
NDRus (kbit/s)	393273
Measured PSD Plot (dBm/Hz)	(Refer to Figure)
ATPds	0.99
ATPus	0.69

Results - PSD Limit Mask Case 2	
NDRds (kbit/s)	1456901
NDRus (kbit/s)	353473
Measured PSD Plot (dBm/Hz)	(Refer to Figure)
ATPds	-6.06
ATPus	-5.72


Measured PSD Case 1
PSD_mask
PSD_co_side
PSD_cpe_side
-80
-100
-120
Power (dBm/Hz)
0
50
100
150
200
250
Frequency (MHz)


Measured PSD Case 2
PSD_mask
PSD_co_side
PSD_cpe_side
-80
-100
-120
Power (dBm/Hz)
0
50
100
150
200
250
Frequency (MHz)

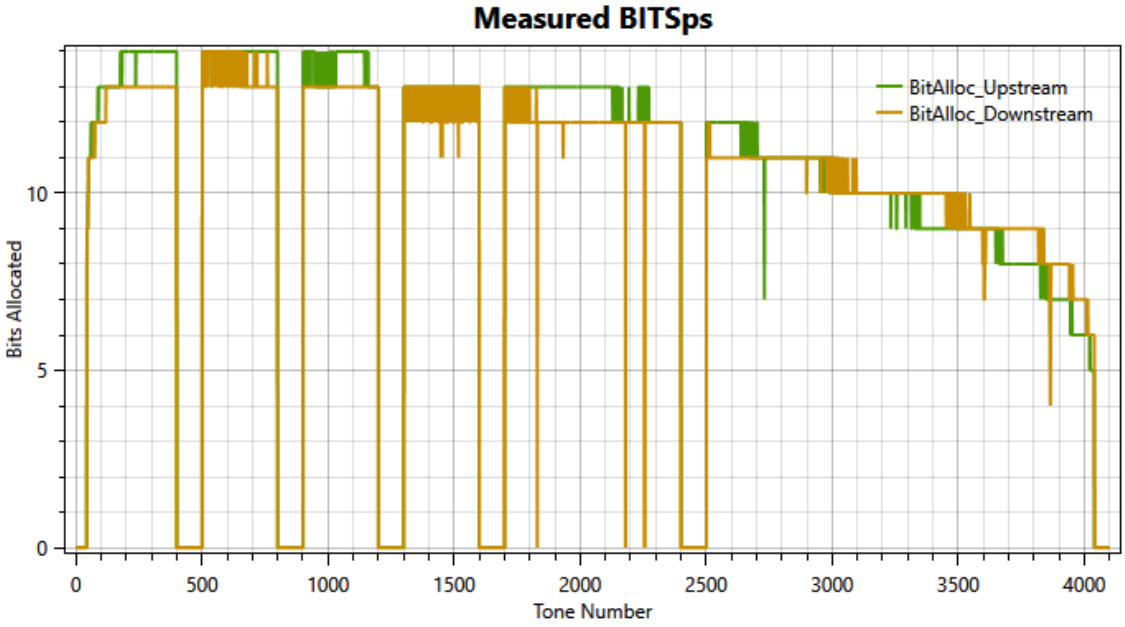
Summary		
The measured CPE PSD SHALL be equal to or lower than the Limit PSD Mask (PSD Limit Mask Case 1 Test).	Measured PSD Within Mask	pass
The measured DPU PSD SHALL be equal to or lower than the Limit PSD Mask (PSD Limit Mask Case 1 Test).	Measured PSD Within Mask	pass
The measured CPE transmit power SHALL be equal to or lower than the MAXATPus (PSD Limit Mask Case 1 Test).	Measured ATP CPE side Within Bounds: 0.69	pass
The measured DPU transmit power SHALL be equal to or lower than the MAXATPds (PSD Limit Mask Case 1 Test).	Measured ATP CO side Within Bounds: 0.99	pass
The measured CPE PSD SHALL be equal to or lower than the Limit PSD Mask (PSD Limit Mask Case 2 Test).	Measured PSD Within Mask	pass
The measured DPU PSD SHALL be equal to or lower than the Limit PSD Mask (PSD Limit Mask Case 2 Test).	Measured PSD Within Mask	pass
The measured CPE transmit power SHALL be equal to or lower than the MAXATPus (PSD Limit Mask Case 2 Test).	Measured ATP CPE side Within Bounds: -5.72	pass
The measured DPU transmit power SHALL be equal to or lower than the MAXATPds (PSD Limit Mask Case 2 Test).	Measured ATP CO side Within Bounds: -6.06	pass

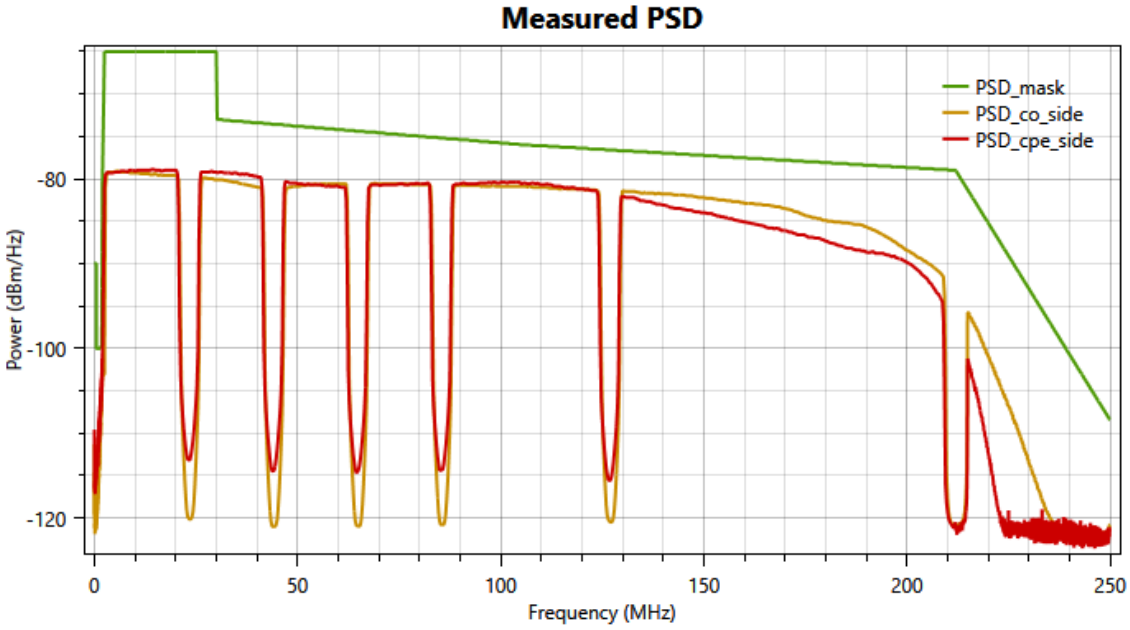
6.2.2 Sub-carrier Masking Test	PARTS	RESULTS
		PASS

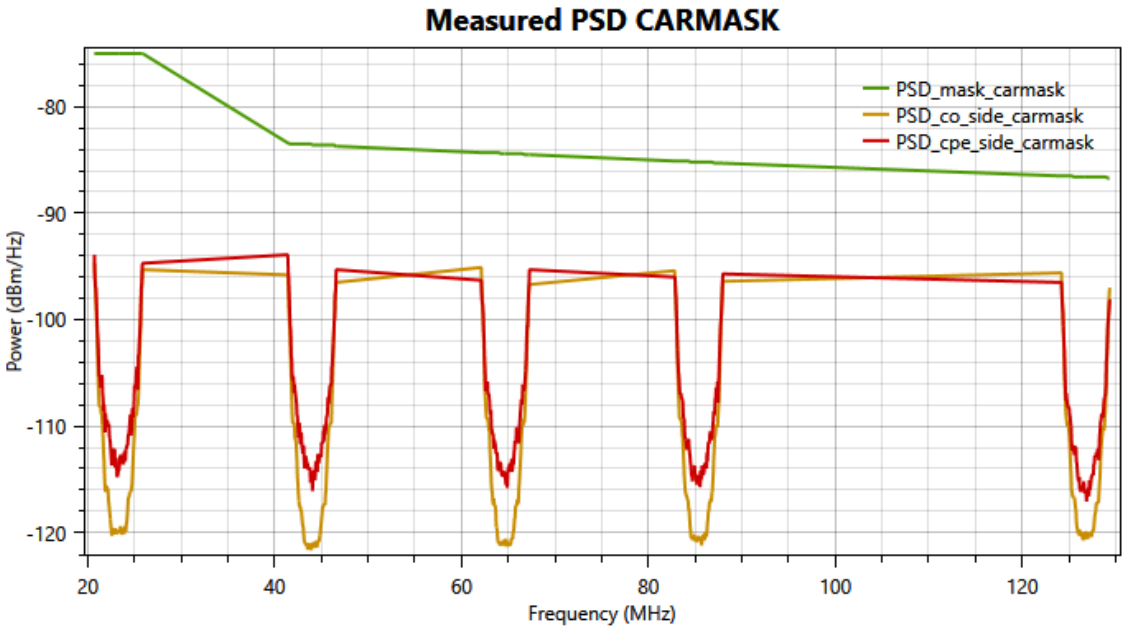
PURPOSE

The purpose of this test is to verify that the DUT is able to apply an arbitrary masking of sub-carriers to force no bit loading and reduced transmitted power over the masked carriers.

Results - Sub-Carrier Masking	
NDRds (kbit/s)	1359105
NDRus (kbit/s)	341091
BITSpsds	(Refer to Figure)
BITSpsus	(Refer to Figure)
Measured PSD Plot (dBm/Hz)	(Refer to Figure)
ATPds (dBm)	0.94
ATPus (dBm)	0.78
Measured Sub-Carrier PSD(dBm/Hz)	(Refer to Figure)


Measured BITSps
BitAlloc_Upstream
BitAlloc_Downstream
Bits Allocated
0
5
10
0
500
1000
1500
2000
2500
3000
3500
4000
Tone Number


Measured PSD
PSD_mask
PSD_co_side
PSD_cpe_side
Power (dBm/Hz)
-80
-100
-120
0
50
100
150
200
250
Frequency (MHz)



Summary		
The measured CPE PSD SHALL be equal to or lower than the Limit PSD Mask.	Measured PSD Within Mask	pass
The measured DPU PSD SHALL be equal to or lower than the Limit PSD Mask.	Measured PSD Within Mask	pass
The measured CPE transmit power SHALL be equal to or lower than 4dBm.	Measured ATP CPE side Within Bounds: 0.78	pass
The measured DPU transmit power SHALL be equal to or lower than 4dBm.	Measured ATP CO side Within Bounds: 0.94	pass
For the sub-carriers inside the CARMASK, the measured sub-carrier CPE PSD SHALL be at least 10 dB lower than the Limit PSD Mask.	Measured PSD Within Mask	pass
For the sub-carriers inside the CARMASK, the measured sub-carrier DPU PSD SHALL be at least 10 dB lower than the Limit PSD Mask.	Measured PSD Within Mask	pass

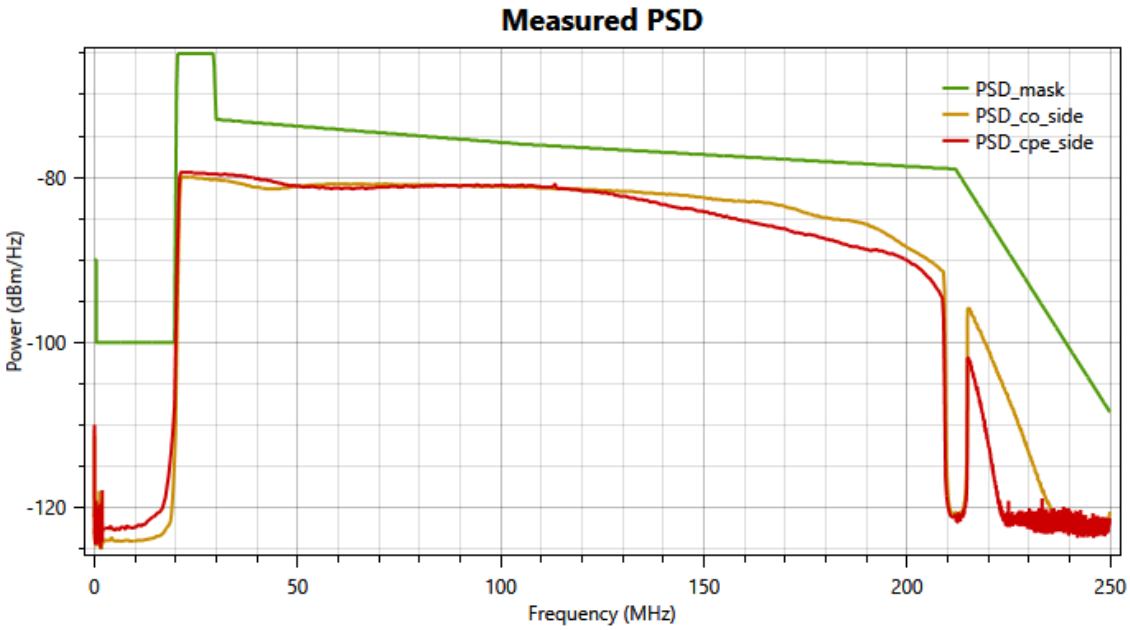
The sub-carriers with index from 0 to 42 inclusive, and for sub-carriers inside the CARMASK, the BITSpsus SHALL be equal to 0.	No Bits Loaded in Masked Carriers on CPE: 0	pass
The sub-carriers with index from 0 to 42 inclusive, and the sub-carriers inside the CARMASK , the BITSpsds SHALL be equal to 0.	No Bits Loaded in Masked Carriers on CO: 0	pass

6.2.3 PSD Shaping Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT is able to apply an arbitrary PSD shaping. Within this test, this feature is also used to verify the configuration excluding the VDSL2 17MHz band, through the specific configuration values used for the PSD mask.

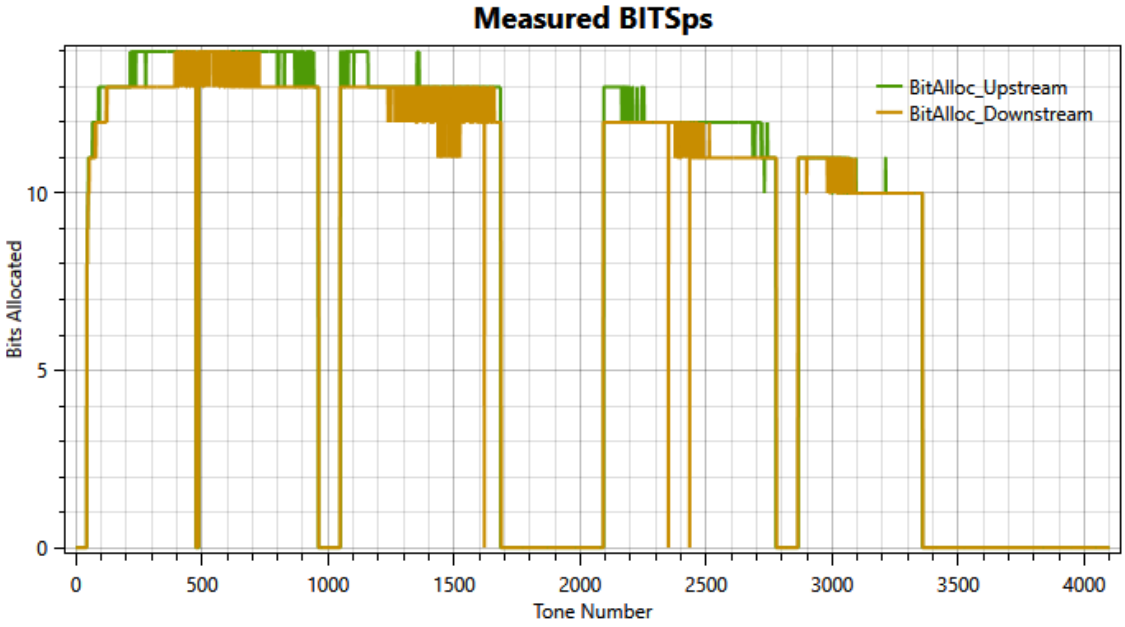
Results - PSD Shaping	
NDRds (kbit/s)	1416780
NDRus (kbit/s)	352665
Measured PSD Plot (dBm/Hz)	(Refer to Figure)
ATPds (dBm)	0.78
ATPus (dBm)	0.44

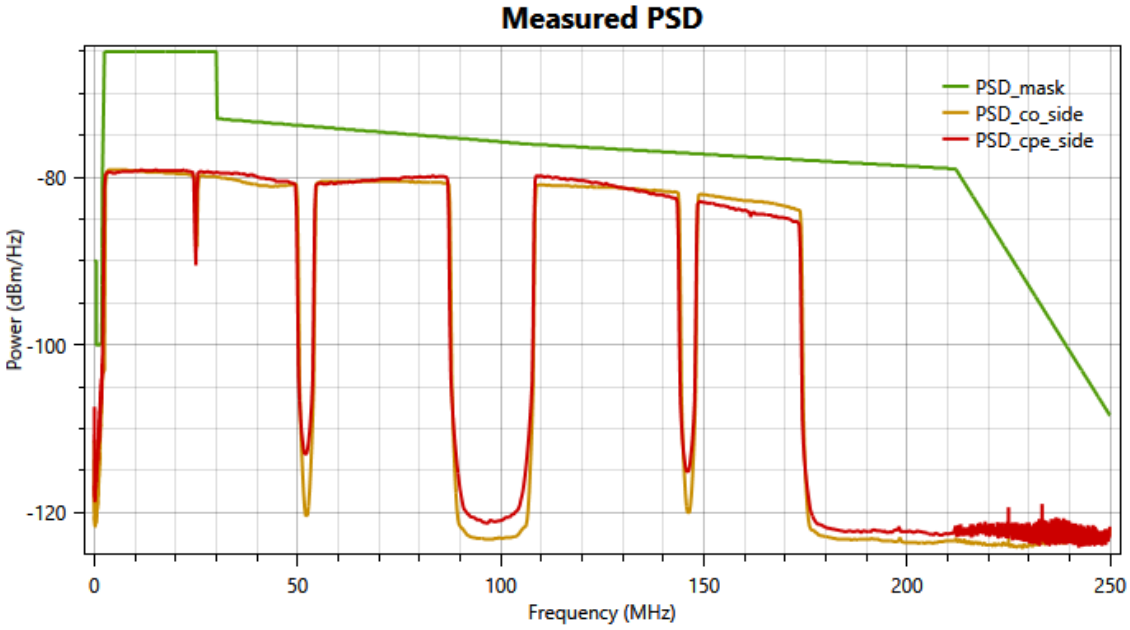


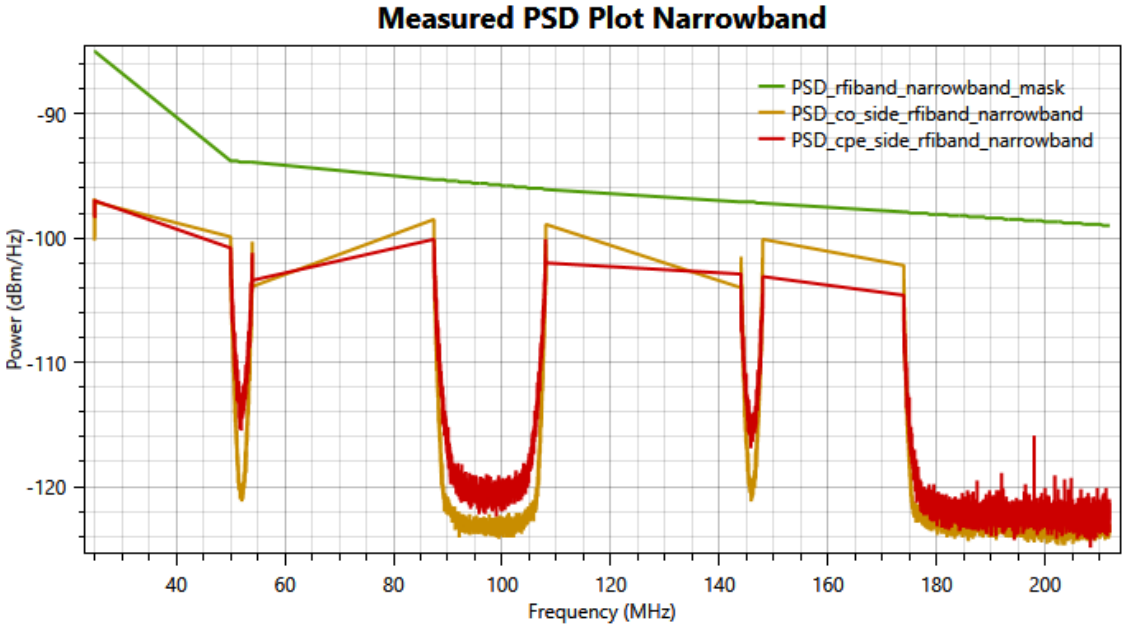
Summary		
The measured CPE PSD SHALL be lower than the MREFPSD-MASKus.	Measured PSD Within Mask	pass
The measured DPU PSD SHALL be lower than the MREFPSD-MASKds.	Measured PSD Within Mask	pass
The measured CPE transmit power SHALL be equal to or lower than 4dBm.	Measured ATP CPE side Within Bounds: 0.44	pass
The measured DPU transmit power SHALL be equal to or lower than 4dBm.	Measured ATP CO side Within Bounds: 0.78	pass

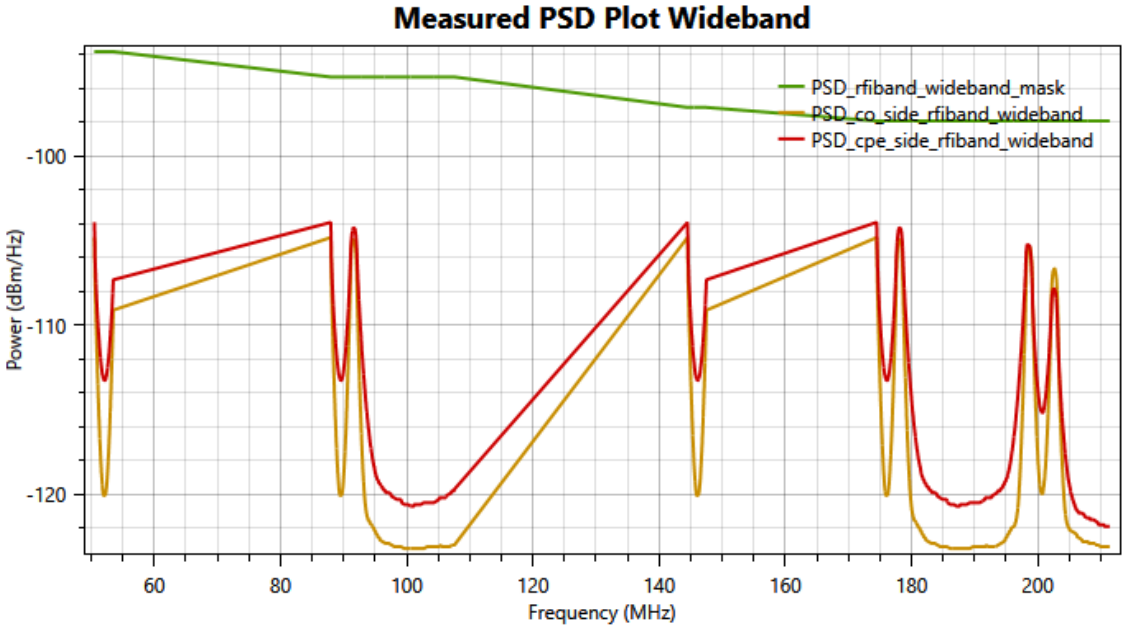
6.2.4 RFI Notching Test	PARTS	RESULTS
		PASS
PURPOSE		
The purpose of this test is to verify that the DUT is able to apply a set of RFI notches.		

Results - RFI Notching	
NDRds (kbit/s)	1133193
NDRus (kbit/s)	290429
BITSpsds	(Refer to Figure)
BITSpsus	(Refer to Figure)
Measured PSD Plot (dBm/Hz)	(Refer to Figure)
Measured Narrowband PSD Plots over Notch 1 Frequencies (dBm/Hz)	(Refer to Figure)
Measured Narrowband PSD Plots over Notch 2 Frequencies (dBm/Hz)	(Refer to Figure)
Measured Narrowband PSD Plots over Notch 3 Frequencies (dBm/Hz)	(Refer to Figure)
Calculated Wideband PSD plots over Notch 2 Frequencies (dBm/Hz)	(Refer to Figure)
Calculated Wideband PSD plots over Notch 3 Frequencies (dBm/Hz)	(Refer to Figure)


Measured BITSps
BitAlloc_Upstream
BitAlloc_Downstream
Bits Allocated
0
5
10
0
500
1000
1500
2000
2500
3000
3500
4000
Tone Number


Measured PSD
PSD_mask
PSD_co_side
PSD_cpe_side
Power (dBm/Hz)
-80
-100
-120
0
50
100
150
200
250
Frequency (MHz)


Measured PSD Plot Narrowband
PSD_rfiband_narrowband_mask
PSD_co_side_rfiband_narrowband
PSD_cpe_side_rfiband_narrowband
Power (dBm/Hz)
-90
-100
-110
-120
40
60
80
100
120
140
160
180
200
Frequency (MHz)


Measured PSD Plot Wideband
PSD_rfiband_wideband_mask
PSD_co_side_rfiband_wideband
PSD_cpe_side_rfiband_wideband
Power (dBm/Hz)
-100
-110
-120
60
80
100
120
140
160
180
200
Frequency (MHz)

Summary		
The measured CPE PSD SHALL be lower than the TXPSDM_N PSD Mask for the narrowband measurement of Notch 1.	Measured PSD Within Mask	pass
The measured DPU PSD SHALL be lower than the TXPSDM_N PSD Mask for the narrowband measurement of Notch 1.	Measured PSD Within Mask	pass
The measured CPE PSD SHALL be lower than the TXPSDM_N PSD Mask for the narrowband measurement of Notch 2.	Measured PSD Within Mask	pass
The measured DPU PSD SHALL be lower than the TXPSDM_N PSD Mask for the narrowband measurement of Notch 2.	Measured PSD Within Mask	pass
The calculated CPE PSD SHALL be lower than the TXPSDM_W PSD Mask for the wideband calculation of Notch 2.	Measured PSD Within Mask	pass
The calculated DPU PSD SHALL be lower than the TXPSDM_W PSD Mask for the wideband calculation of Notch 2.	Measured PSD Within Mask	pass
The measured CPE PSD SHALL be lower than the TXPSDM_N PSD Mask for the narrowband measurement of Notch 3.	Measured PSD Within Mask	pass
The measured DPU PSD SHALL be lower than the TXPSDM_N PSD Mask for the narrowband measurement of Notch 3.	Measured PSD Within Mask	pass
The calculated CPE PSD SHALL be lower than the TXPSDM_W PSD Mask for the wideband calculation of Notch 3.	Measured PSD Within Mask	pass
The calculated DPU PSD SHALL be lower than the TXPSDM_W PSD Mask for the wideband calculation of Notch 3.	Measured PSD Within Mask	pass
The measured CPE PSD SHALL be lower than the TXPSDM_N PSD Mask for the narrowband measurement of Notch 4.	Measured PSD Within Mask	pass

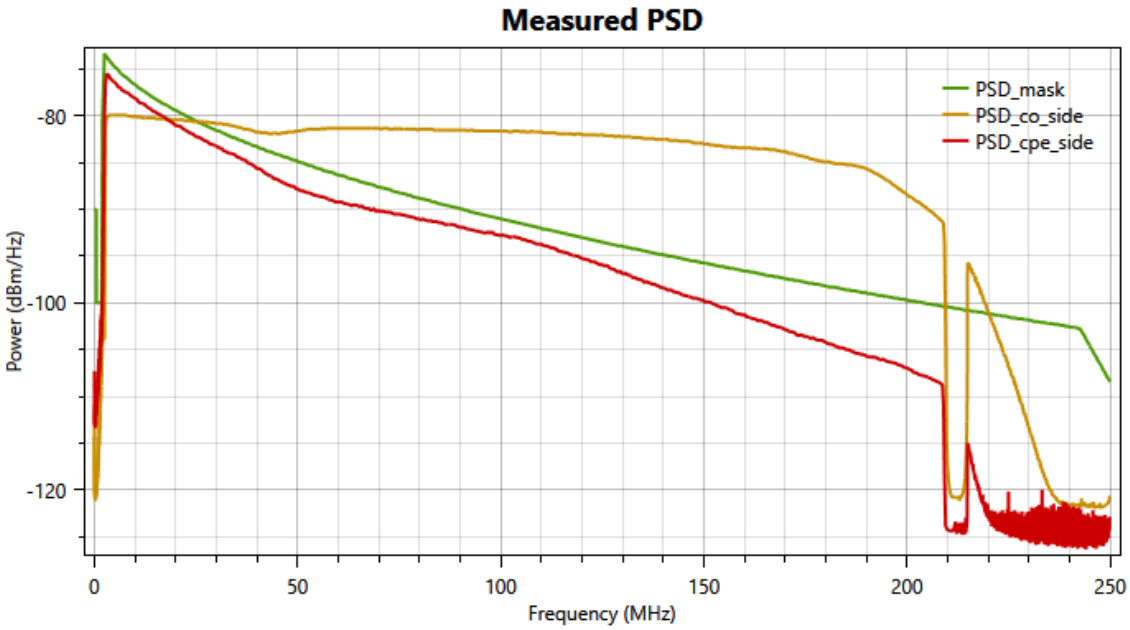
The measured DPU PSD SHALL be lower than the TXPSDM_N PSD Mask for the narrowband measurement of Notch 4.	Measured PSD Within Mask	pass
The calculated CPE PSD SHALL be lower than the TXPSDM_W PSD Mask for the wideband calculation of Notch 4.	Measured PSD Within Mask	pass
The calculated DPU PSD SHALL be lower than the TXPSDM_W PSD Mask for the wideband calculation of Notch 4.	Measured PSD Within Mask	pass
The measured CPE PSD SHALL be lower than the TXPSDM_N PSD Mask for the narrowband measurement of Notch 5.	Measured PSD Within Mask	pass
The measured DPU PSD SHALL be lower than the TXPSDM_N PSD Mask for the narrowband measurement of Notch 5.	Measured PSD Within Mask	pass
The calculated CPE PSD SHALL be lower than the TXPSDM_W PSD Mask for the wideband calculation of Notch 5.	Measured PSD Within Mask	pass
The calculated DPU PSD SHALL be lower than the TXPSDM_W PSD Mask for the wideband calculation of Notch 5.	Measured PSD Within Mask	pass
The sub-carriers inside the RFIBANDs and IARBANDs defined in Table 38, the BITSpsus SHALL be equal to 0.	CPE Reported 0 bits	pass
The sub-carriers inside the RFIBANDs and IARBANDs defined in Table 38, the BITSpsds SHALL be equal to 0.	CO Reported 0 bits	pass

6.2.5 UPBO Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the signals transmitted by the DUT do not exceed the calculated upstream Transmit PSD Mask (PSDMASK), which includes the transmit PSD reductions due to UPBO. The UPBO configuration parameters are communicated to the FTU R by the FTU O during initialization and are required for the correct calculation of the Transmit PSD Mask. Additionally, the FTU R needs to accurately estimate the electrical length (kl0) of the channel to calculate the appropriate Transmit PSD Mask.

Results - UPBO PSD Mask	
PSD_ATTN (f)	20dB
Kl0 Calculated	3.4
NDRus (kbit/s)	292140
NDRds (kbit/s)	292140
UPBOKLE	3.4
UPBOKLE-R	3.4
Measured PSD Plot (dBm/Hz)	(Refer to Figure)
ATPds (dBm)	0.99
ATPus (dBm)	-3.45



Summary		
The measured CPE PSD SHALL be lower than the MREFPSD-MASKus.	Measured PSD Within Mask	pass
The measured CPE transmit power SHALL be equal to or lower than 4dBm.	Reported No Increase CPE side: -3.45	pass
The measured DPU transmit power SHALL be equal to or lower than 4dBm.	Reported No Increase CO side: 0.99	pass
The UPBOKLE-R value and the UPBOKLE value SHALL be within 2 dB of the kl0_reference value.	Measured Value In Bounds: UPBOKLE: 3.4 UPBOKLE_R: 3.4	pass

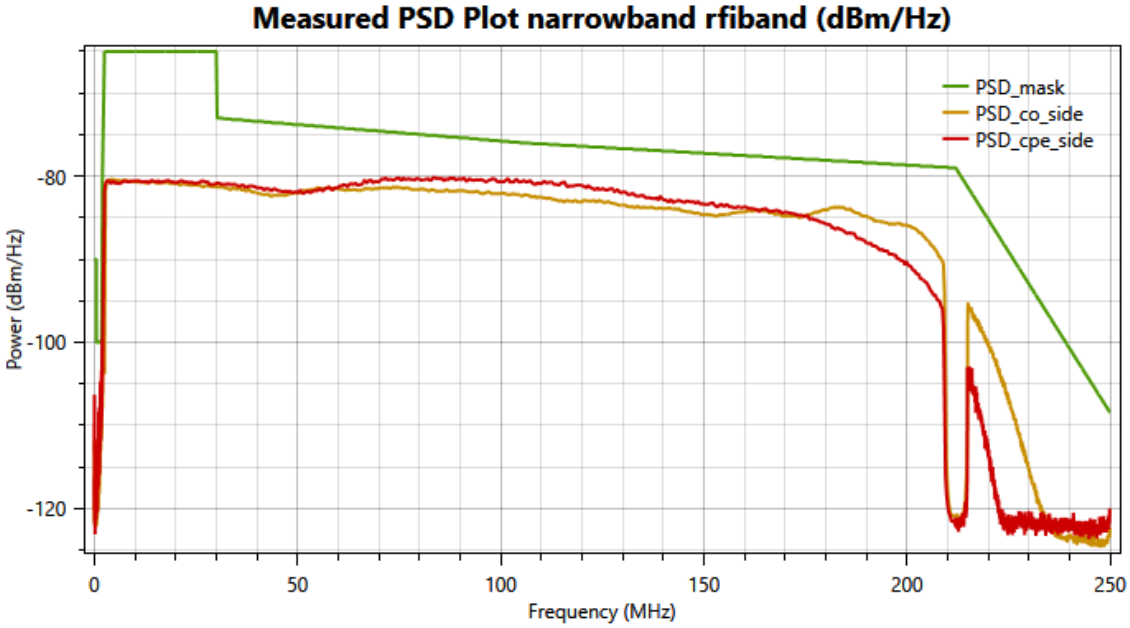
6.2.6 TIGA Test	PARTS	RESULTS
		PASS

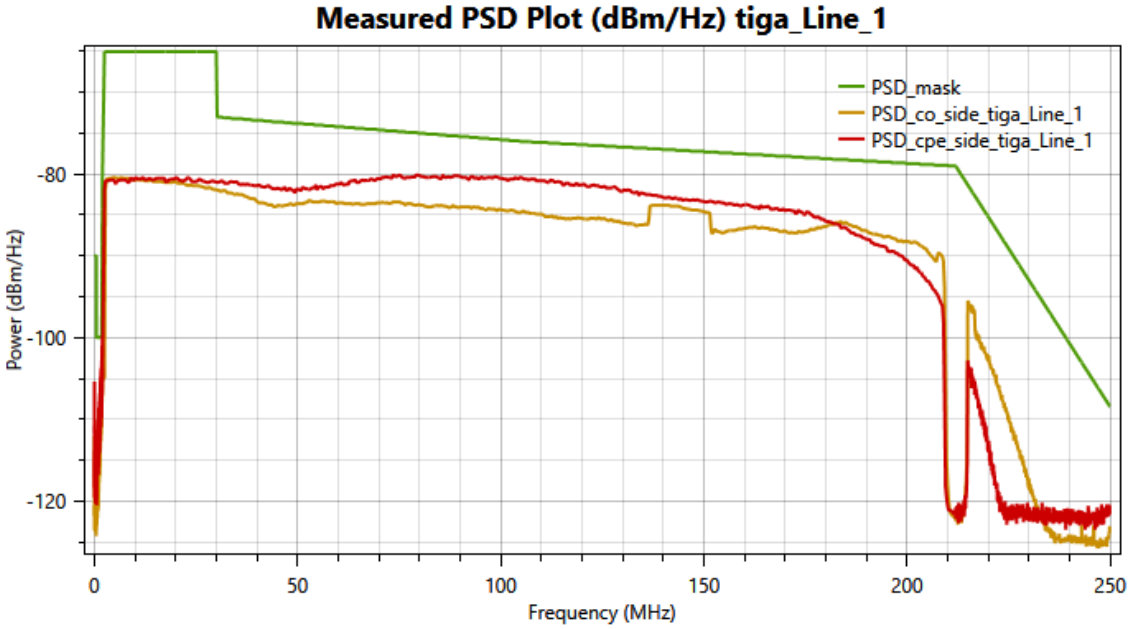
PURPOSE

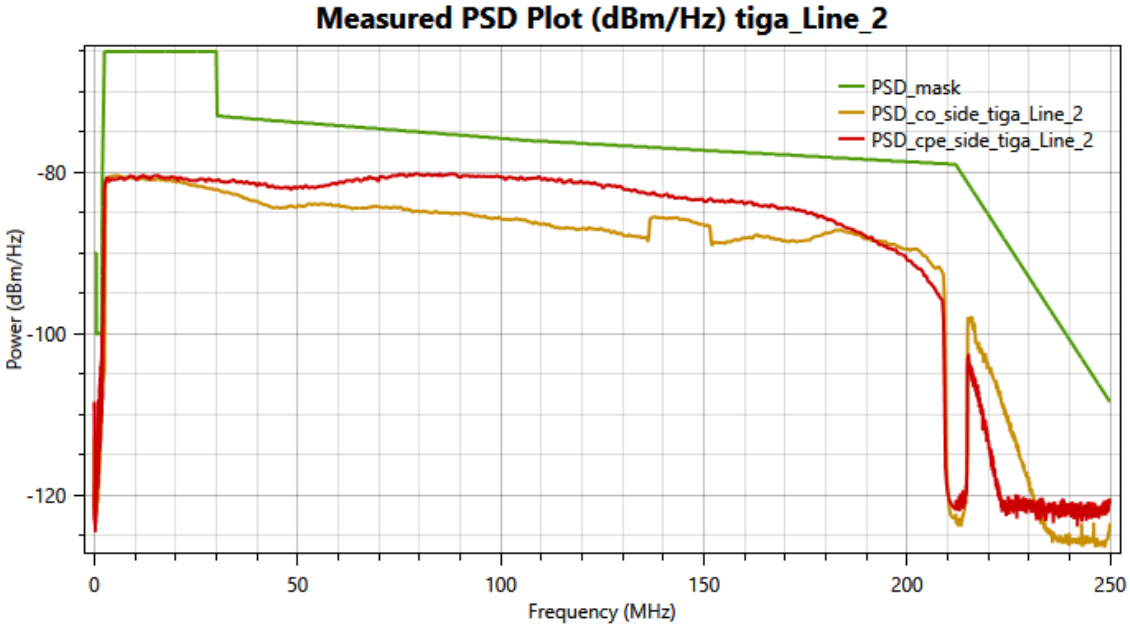
The purpose of this test is to verify that the signals transmitted by the DUT do not exceed the limit PSD mask (LPM), when high-crosstalk lines join the pre-coded group. This test applies only if the DPU supports multiple ports.

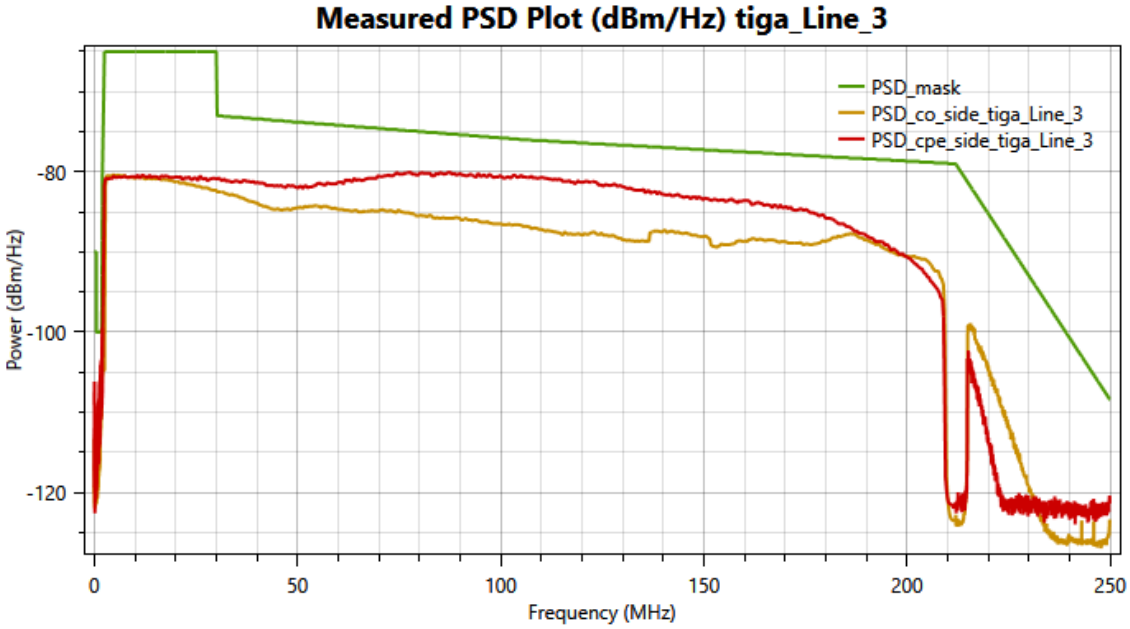
Results - TIGA	
NDRds (kbit/s)	278464
NDRus (kbit/s)	278464
Results with G.fast Connection on Line #1, Lines #2 #3 #4 Remain in L3 State	
CURR_24_FULL_INITS	1
CURR_24_FAST_INITS	0
CURR_24_TIGA	0
CURR_NE_24_ES	0
CURR_FE_24_ES	0
CURR_NE_24_SES	0
CURR_FE_24_SES	0
CURR_NE_24_UAS	38
CURR_FE_24_UAS	38
Measured PSD Plot (dBm/Hz)	(Refer to Figure)
ATPus measurement 1 (dBm)	1.14
ATPds measurement 1 (dBm)	1.14
Results with G.fast Connection on Line #1, Lines #2 #3 #4 Remain in L3 State	
Measured PSD Plot (dBm/Hz)	(Refer to Figure)
ATPus measurement 2 (dBm)	1.14
ATPds measurement 2 (dBm)	-0.93

Results with G.fast Connection on Lines #1 #2 #3 , Lines #4 Remain in L3 State	
Measured PSD Plot (dBm/Hz)	(Refer to Figure)
ATPus measurement 3 (dBm)	1.12
ATPds measurement 3 (dBm)	-1.7
Results with G.fast Connection on Lines #1 #2 #3 #4	
Measured PSD Plot (dBm/Hz)	(Refer to Figure)
ATPus measurement 4 (dBm)	1.14
ATPds measurement 4 (dBm)	-2.16
CURR_24_FULL_INITS	1
CURR_24_FAST_INITS	0
CURR_24_TIGA	20
CURR_NE_24_ES	0
CURR_FE_24_ES	0
CURR_NE_24_SES	0
CURR_FE_24_SES	0
CURR_NE_24_UAS	38
CURR_FE_24_UAS	38


Measured PSD Plot narrowband rfiband (dBm/Hz)
PSD_mask
PSD_co_side
PSD_cpe_side
-80
-100
-120
Power (dBm/Hz)
0
50
100
150
200
250
Frequency (MHz)


Measured PSD Plot (dBm/Hz) tiga_Line_1
PSD_mask
PSD_co_side_tiga_Line_1
PSD_cpe_side_tiga_Line_1
-80
-100
-120
Power (dBm/Hz)
0
50
100
150
200
250
Frequency (MHz)





Summary		
The CURR_24_FULL_INITS counter increase SHALL be 0.	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0.	Reported No Fast Inits Increase: 0	pass
The CURR_24_TIGA counter increase SHALL be >= 1.	Reported Tiga Increase: 20	pass
The CURR_NE/FE_24_ES counter increase SHALL be 0.	Reported No ES Increase: FE: 0 NE: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be 0.	Reported No SES Increase: FE: 0 NE: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0.	Reported No UAS Increase: FE: 0 NE: 0	pass
The measured CPE PSD SHALL be lower than the Limit PSD Mask (LPM) plus 1dB.	Measured PSD Within Limit Mask	pass
The measured DPU PSD SHALL be lower than the Limit PSD Mask (LPM) plus 1dB.	Measured PSD Within Limit Mask	pass
The measured transmit power values SHALL be lower than 4dBm + 1dB.	Measured Value Within Limit	pass

6.3.1 TDD Inter-frame Gap Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT maintains a proper gap time between the start and stop of each TDD transmission, as observed at its interface. The test performed over a number of different loop lengths to assure the introduced delay does not negatively impact the gap time. The loop may be simulated/modeled or may be real cable, but it SHALL introduce a propagation delay related to its length.

Results - 20m		**Results - 200m**	
Tg1 Gap Times	10.8	**Tg1 Gap Time**	10.8
Tg1' Gap Time	9.2	**Tg1' Gap Time**	7.2
Tg2 Gap Times	9.5	**Tg2 Gap Times**	9.7
Tg2' Gap Time	10.2	**Tg2' Gap Time**	12.3

Summary		
The average measured gap time Tg1' SHALL fall in the range of 6.5-0.75us to 11.2us for 20m.	Reported Tg1' Value In Range: 9.2	pass
The average measured gap time Tg1 SHALL be greater than or equal to 6.5-0.75us for 20m.	Reported Tg1 Value In Range: 10.8	pass
The average measured gap time Tg1' SHALL fall in the range of 6.5-0.75us to 11.2us for 200m.	Reported Tg1' Value In Range: 7.2	pass
The average measured gap time Tg1 SHALL be greater than or equal to 6.5-0.75us for 200m.	Reported Tg1 Value In Range: 10.8	pass

6.3.2 DS and US Ratio Configuration (Mds) Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT adheres to the split of downstream/upstream transmission time/rate as configured in the DPU-MIB.

Results - Case 1		**Results - Case 2**		**Results - Case 3**	
Mds measured	32	**Mds measured**	28	**Mds measured**	16
Mus measured	3	**Mus measured**	7	**Mus measured**	19
NDRds	1225229	**NDRds**	1077136	**NDRds**	609461
NDRus	110036	**NDRus**	271755	**NDRus**	747012

Summary		
The Mds_measured SHALL be equal to Mds (Case 1).	Measured Mds Value Matches: 32	pass
The Mus_measured SHALL be equal to Mus (Case 1).	Measured Mus Value Matches: 3	pass
The Mds_measured SHALL be equal to Mds (Case 2).	Measured Mds Value Matches: 28	pass
The Mus_measured SHALL be equal to Mus (Case 2).	Measured Mus Value Matches: 7	pass
The Mds_measured SHALL be equal to Mds (Case 3).	Measured Mds Value Matches: 16	pass
The Mus_measured SHALL be equal to Mus (Case 3).	Measured Mus Value Matches: 19	pass

6.4 Accelerated MTBE Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify through the accelerated MTBE test whether the DUT is able to sustain the minimum MTBE defined in G.9701. The RTX_TESTMODE (no retransmission), TPS_TESTMODE (maximum (dummy) data rate), and DRA_TESTMODE (TTRds=Mds; TTRus=Mus) defined in G.9701 are used for accelerating the test time.

Results - Downstream		Results - Upstream	
Initial SNRMds	6	**Initial SNRMds**	6
Final SNRMds	1.1	**Final SNRMds**	5.6
Initial SNRMus	6.1	**Initial SNRMus**	6.1
Final SNRMus	5.6	**Final SNRMus**	1.2
Initial NDRds (kbit/s)	314000	**Initial NDRds (kbit/s)**	313369
Final NDRds (kbit/s)	313969	**Final NDRds (kbit/s)**	313399
Initial NDRus (kbit/s)	82597	**Initial NDRus (kbit/s)**	82597
Final NDRus (kbit/s)	82667	**Final NDRus (kbit/s)**	82588
fDMT (Hz)	48000	**fDMT (Hz)**	48000
LDds	9129	**LDds**	9105
LDus)	9917	**LDus**	9913
T_DTU_in_DMTds	1.71078001752848	**T_DTU_in_DMTds**	1.72934973637961
T_DTU_in_DMTus	1.71428571428571	**T_DTU_in_DMTus**	1.71428571428571
max_NE_RTXUC	418.330013267038	**max_NE_RTXUC**	418.330013267038
max_FE_RTXUC	418.758410730312	**max_FE_RTXUC**	416.504033301117
MTBE	14400	**MTBE**	14400
Result Counters From Before Measurement Period			
CURR_24_FULL_INITS	1	**CURR_24_FULL_INITS**	2
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_RTXUC	0	**CURR_NE_24_RTXUC**	312

CURR_FE_24_RTXUC	0	**CURR_FE_24_RTXUC**	20900
EFB-C24Hds	89344	**EFB-C24Hds**	1787544
EFB-C24Hus	78398	**EFB-C24Hus**	581145
Result Counters From After Measurement Period			
CURR_24_FULL_INITS	1	**CURR_24_FULL_INITS**	2
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_RTXUC	0	**CURR_NE_24_RTXUC**	312
CURR_FE_24_RTXUC	2	**CURR_FE_24_RTXUC**	20900
EFB-C24Hds	1564610	**EFB-C24Hds**	3260185
EFB-C24Hus	470235	**EFB-C24Hus**	972477

Summary		
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Downstream Test).	Reported No Fast Inits Increase: 0	pass
Over the measurement period, 0.25 <= T_DTU_in_DMTds/us <=4 (Downstream Test).	T_DTU In Bounds: DS: 1.71078001752848 US: 1.71428571428571	pass
Over the measurement period, (65536 x EFBds/us)/measurement_time > 95% of NDRds/us (Downstream Test).	EFB Above 95% of NDR: EFB: 1475266 NDR: 313969	pass
Over the measurement period, NE_RTX_TX = 0 (Downstream Test).	NE RTXTX Counter Increased: 0	pass
Over the measurement period, FE_RTX_TX = 0 (Downstream Test).	FE RTXTX Counter Increased: 0	pass
Over the measurement period, FE_RTXUC <= max_FE_RTXUC (Downstream Test).	FE_RTXUC: 2, max_FE_RTXUC: 418.758410730312	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Upstream Test).	Reported No Fast Inits Increase: 0	pass
Over the measurement period, 0.25 <= T_DTU_in_DMTds/us <=4 (Upstream Test).	T_DTU In Bounds: DS: 1.72934973637961 US: 1.71428571428571	pass

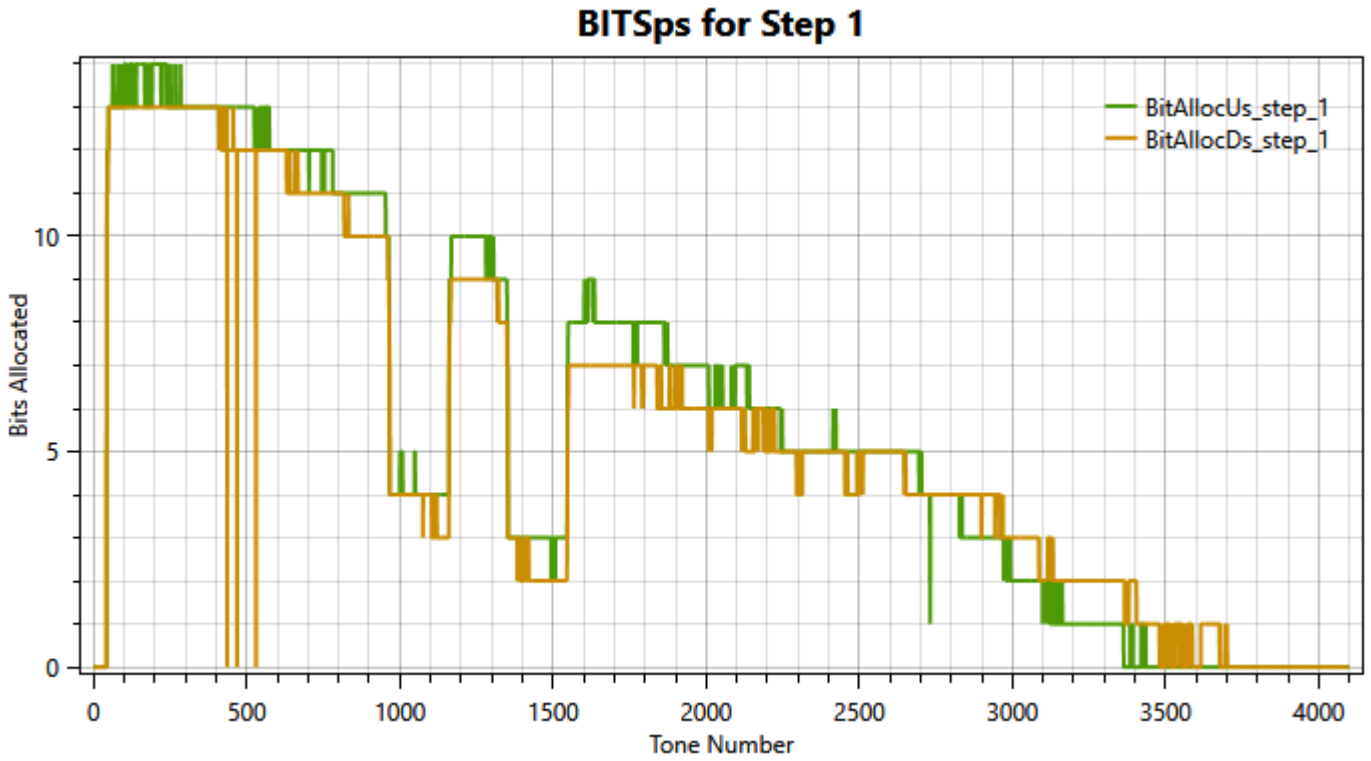
Over the measurement period, (65536 x EFBds/us)/measurement_time > 95% of NDRds/us (Upstream Test).	EFB Above 95% of NDR: EFB: 391332 NDR: 82588	pass
Over the measurement period, NE_RTX_TX = 0 (Upstream Test).	NE RTXTX Counter Increased: 0	pass
Over the measurement period, FE_RTX_TX = 0 (Upstream Test).	FE RTXTX Counter Increased: 0	pass
Over the measurement period, NE_RTXUC <= max_NE_RTXUC (Upstream Test).	NE_RTXUC: 0, max_NE_RTXUC: 418.330013267038	pass

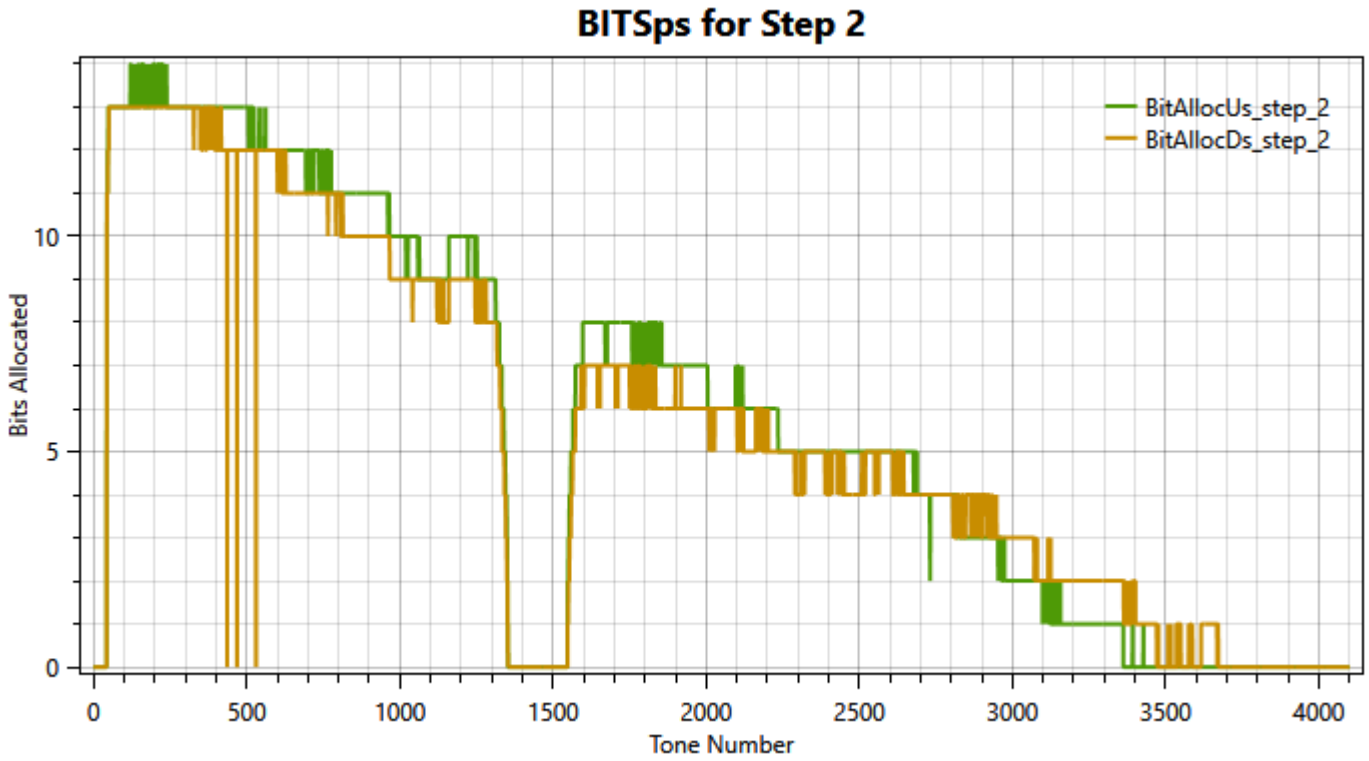
6.6 Bit Swap Test	PARTS	RESULTS
		PASS

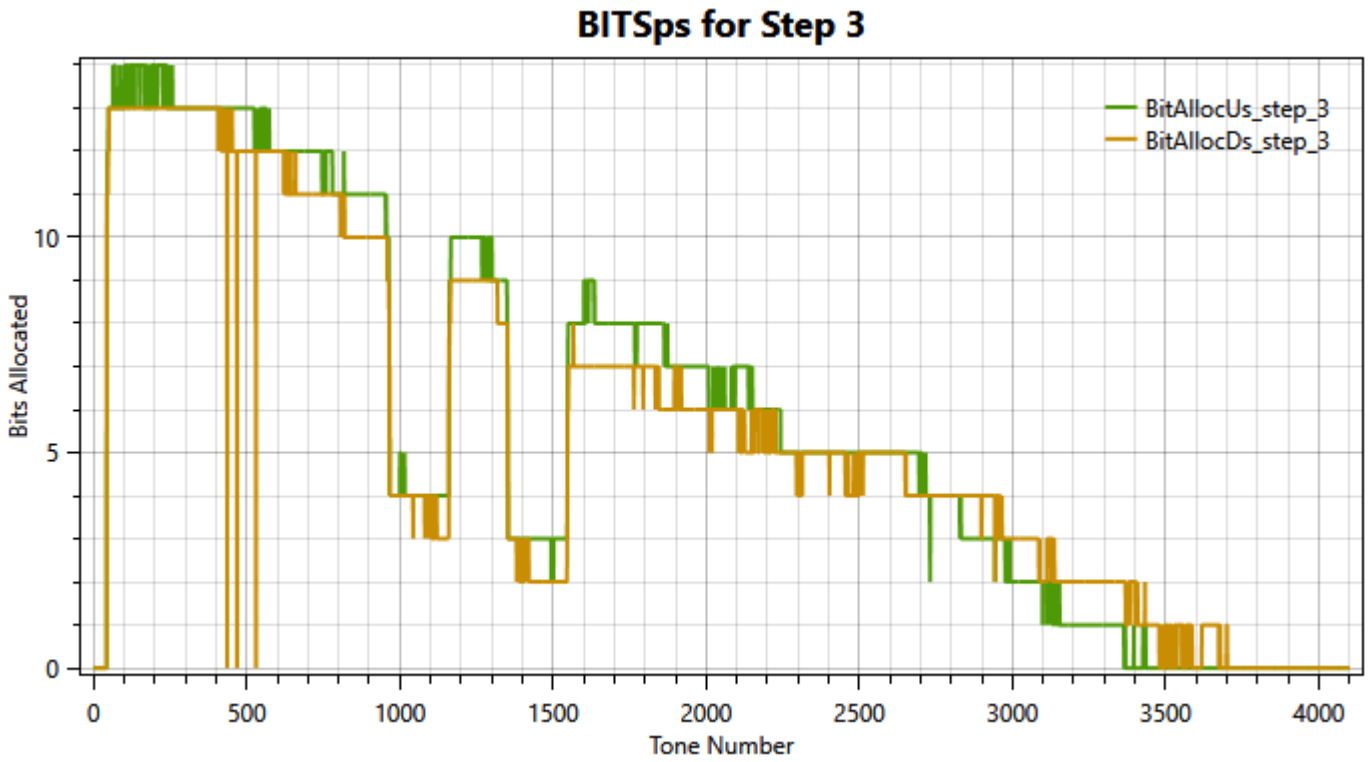
PURPOSE

The purpose of this test is to verify that the DUT supports the bit swap OLR type.

Step 1 - Noise of Type 1		Step 2 - Noise of Type 2		Step 3 - Noise of Type 4	
BITSpsds	(Refer to Figure)	**BITSpsds**	(Refer to Figure)	**BITSpsds**	(Refer to Figure)
BITSpsus	(Refer to Figure)	**BITSpsus**	(Refer to Figure)	**BITSpsus**	(Refer to Figure)
LDds	20873	**LDds**	20877	**LDds**	20873
LDus	21938	**LDus**	21937	**LDus**	21936
BDds	2609	**BDds**	2609	**BDds**	2609
BDus	2742	**BDus**	2742	**BDus**	2742
BITSds	721	**BITSds**	1707	**BITSds**	722
BITSus	779	**BITSus**	1828	**BITSus**	776
CURR_24_FULL_INITS	3	**CURR_24_FULL_INITS**	3	**CURR_24_FULL_INITS**	3
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_BSW	147	**CURR_NE_24_BSW**	181	**CURR_NE_24_BSW**	218
CURR_FE_24_BSW	8	**CURR_FE_24_BSW**	20	**CURR_FE_24_BSW**	36
CURR_NE_24_SES	3	**CURR_NE_24_SES**	3	**CURR_NE_24_SES**	3
CURR_FE_24_SES	1	**CURR_FE_24_SES**	1	**CURR_FE_24_SES**	1
CURR_NE_24_UAS	84	**CURR_NE_24_UAS**	84	**CURR_NE_24_UAS**	84
CURR_FE_24_UAS	84	**CURR_FE_24_UAS**	84	**CURR_FE_24_UAS**	84
BITSds_Band_BLN1	721	**BITSds_Band_BLN1**	1707	**BITSds_Band_BLN1**	722
BITSus_Band_BLN1	779	**BITSus_Band_BLN1**	1828	**BITSus_Band_BLN1**	776
BITSds_Band_BLN2	433	**BITSds_Band_BLN2**	0	**BITSds_Band_BLN2**	432
BITSus_Band_BLN2	574	**BITSus_Band_BLN2**	0	**BITSus_Band_BLN2**	574







Summary		
The CURR_24_FULL_INITS counter increase SHALL be 0.	Reported No Full Inits Increase: 0 step 3: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0.	Reported No Full Inits Increase: 0 step 3: 0	pass
The total increase of the CURR_NE_24_BSW and CURR_NE_24_SRA counters SHALL be >= 1. The total increase Of the CURR_FE_24_BSW And CURR_FE_24_SRA counters SHALL be >= 1.	Reported BSW or SRA Increase: BSW FE: Step 1 to Step 2:12 Step 2 to Step 3: 16Reported BSW or SRA Increase: BSW NE: Step 1 to Step 2:34 Step 2 to Step 3: 37	pass
The CURR_NE/FE_24_SES counter increase SHALL be <= 1.	Reported SES Increases <= 1: FE: before:0 after: 0 NE: before: 0 after: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0.	Reported No UAS Increase: FE: before:0 after: 0 NE: before: 0 after: 0	pass
The BITSds/us in the frequency band where the noise is removed SHALL increase.	Reported BITS Increases: DS: before: 986 after: 432 US: before: 1049 after: 574	pass
The BITSds/us in the frequency band where the noise is removed SHALL increase.	Reported BITS Decrease: DS: before: -433 after: -985 US: before: -574 after: -1052	pass
The MONSCds/us in the frequency range of noise band 2 after the noise is added SHALL increase.	Reported MONSC Increase: DS: before: 0 after: 193 US: before: 0 after: 193	pass
The MONSCds/us in the frequency range of noise band 2 after the noise is removed SHALL decrease.	Reported MONSC decrease: DS: before: 193 after: 0 US: before: 193 after: 0	pass
The value of BDds/us SHALL not drop by more than 5%.	Reported BDds Step 1: 2609, Step 3: 2609, BDus Step 1: 2742, Step 3: 2742	pass

6.7 SRA Downshift Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT is able to decrease the net data rate (NDR), based on the external noise on the line increasing slowly over time. The NDR downshift is triggered with an increase of the noise present on the line.

Before Noise Increase		After Noise Increase	
NDRds (kbit/s)	313723	**NDRds (kbit/s)**	194944
NDRus (kbit/s)	82335	**NDRus (kbit/s)**	52799
SNRMds	6	**SNRMds**	6.1
SNRMus	6.2	**SNRMus**	5.8
CURR_24_FULL_INITS	4	**CURR_24_FULL_INITS**	4
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_SRA	9	**CURR_NE_24_SRA**	15
CURR_FE_24_SRA	0	**CURR_FE_24_SRA**	6
CURR_NE_24_CV	0	**CURR_NE_24_CV**	0
CURR_FE_24_CV	18	**CURR_FE_24_CV**	18
CURR_NE_24_ES	3	**CURR_NE_24_ES**	3
CURR_FE_24_ES	4	**CURR_FE_24_ES**	4
CURR_NE_24_SES	3	**CURR_NE_24_SES**	3
CURR_FE_24_SES	1	**CURR_FE_24_SES**	1
CURR_NE_24_UAS	107	**CURR_NE_24_UAS**	107
CURR_FE_24_UAS	107	**CURR_FE_24_UAS**	107

Summary		
The CURR_24_FULL_INITS counter increase SHALL be 0.	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0.	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_SRA counter increase SHALL be >= 1.	Reported SRA Increase >= 1: FE: 6 NE: 6	pass
The CURR_NE/FE_24_CV counter increase SHALL be 0.	Reported No CV Increase: FE: 0 NE: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be 0.	Reported No ES Increase: FE: 0 NE: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be 0.	Reported No SES Increase: FE: 0 NE: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0.	Reported No UAS Increase: FE: 0 NE: 0	pass
The NDRds/us SHALL decrease.	NDR Changed: DS: -118779 US: -29536	pass
The SNRMds/us SHALL be within the bounds of the configured SRA margins.	SNRM In Bounds: DS: before: 6 after: 6.1 US: before: 6.2 after: 5.8	pass

6.8 SRA Upshift Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT is able to increase the net data rate (NDR), based on the external noise on the line decreasing slowly over time. The NDR upshift is triggered with a decrease of the noise present on the line.

Before Noise Decrease		After Noise Decrease	
NDRds (kbit/s)	315020	**NDRds (kbit/s)**	450475
NDRus (kbit/s)	82607	**NDRus (kbit/s)**	120401
SNRMds	5.9	**SNRMds**	6.2
SNRMus	6.1	**SNRMus**	6.2
CURR_24_FULL_INITS	4	**CURR_24_FULL_INITS**	4
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_SRA	9	**CURR_NE_24_SRA**	18
CURR_FE_24_SRA	0	**CURR_FE_24_SRA**	5
CURR_NE_24_CV	0	**CURR_NE_24_CV**	0
CURR_FE_24_CV	18	**CURR_FE_24_CV**	18
CURR_NE_24_ES	3	**CURR_NE_24_ES**	3
CURR_FE_24_ES	4	**CURR_FE_24_ES**	4
CURR_NE_24_SES	3	**CURR_NE_24_SES**	3
CURR_FE_24_SES	1	**CURR_FE_24_SES**	1
CURR_NE_24_UAS	107	**CURR_NE_24_UAS**	107
CURR_FE_24_UAS	107	**CURR_FE_24_UAS**	107

Summary		
The CURR_24_FULL_INITS counter increase SHALL be 0.	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0.	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_SRA counter increase SHALL be >= 1.	Reported SRA Increase >= 1: FE: 5 NE: 9	pass
The CURR_NE/FE_24_CV counter increase SHALL be 0.	Reported No CV Increase: FE: 0 NE: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be 0.	Reported No ES Increase: FE: 0 NE: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be 0.	Reported No SES Increase: FE: 0 NE: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0.	Reported No UAS Increase: FE: 0 NE: 0	pass
The NDRds/us SHALL increase.	NDR Changed: DS: 135455 US: 37794	pass
The SNRMds/us SHALL be within the bounds of the configured SRA margins.	SNRM In Bounds: DS: before: 5.9 after: 6.2 US: before: 6.1 after: 6.2	pass

6.9 FRA & SRA Upshift Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT is capable of rapidly performing bit loading reduction in a specified portion of frequency spectrum in both the upstream and downstream directions, based on sudden noise increase.'

Stage 1: Results with High Noise Injected on Both Sides		Stage 2: Results with Low Noise Injected on Both Sides		Stage 3: Results with High Noise Injected on Both Sides	
NDRds (kbit/s)	317717	**NDRds (kbit/s)**	655639	**NDRds (kbit/s)**	309119
NDRus (kbit/s)	95992	**NDRus (kbit/s)**	212239	**NDRus (kbit/s)**	97065
SNRMds	5.9	**SNRMds**	6.1	**SNRMds**	6.2
SNRMus	6.3	**SNRMus**	6.1	**SNRMus**	6.2
CURR_24_FULL_INITS	5	**CURR_24_FULL_INITS**	5	**CURR_24_FULL_INITS**	5
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_FRA	0	**CURR_NE_24_FRA**	0	**CURR_NE_24_FRA**	1
CURR_FE_24_FRA	0	**CURR_FE_24_FRA**	0	**CURR_FE_24_FRA**	2
CURR_NE_24_SRA	16	**CURR_NE_24_SRA**	18	**CURR_NE_24_SRA**	19
CURR_FE_24_SRA	4	**CURR_FE_24_SRA**	8	**CURR_FE_24_SRA**	10
CURR_NE_24_CV	0	**CURR_NE_24_CV**	0	**CURR_NE_24_CV**	1
CURR_FE_24_CV	18	**CURR_FE_24_CV**	18	**CURR_FE_24_CV**	20
CURR_NE_24_ES	3	**CURR_NE_24_ES**	3	**CURR_NE_24_ES**	4
CURR_FE_24_ES	4	**CURR_FE_24_ES**	4	**CURR_FE_24_ES**	5
CURR_NE_24_SES	3	**CURR_NE_24_SES**	3	**CURR_NE_24_SES**	3
CURR_FE_24_SES	1	**CURR_FE_24_SES**	1	**CURR_FE_24_SES**	1
CURR_NE_24_UAS	132	**CURR_NE_24_UAS**	132	**CURR_NE_24_UAS**	132
CURR_FE_24_UAS	132	**CURR_FE_24_UAS**	132	**CURR_FE_24_UAS**	132

Summary		
At each stage, the SNRMds/us SHALL be within the configured SRA bounds.	SNRM In Bounds: DS: stage 1: 5.9 stage 2 6.1 stage 3 6.2 US: stage 1: 6.3 stage 2 6.1 stage 3 6.2	pass
From stage 1 to stage 2, the CURR_24_FULL_INITS counter increase SHALL be 0.	Reported No Full Inits Increase: 0	pass
From stage 1 to stage 2, the CURR_24_FAST_INITS counter increase SHALL be 0.	Reported No Fast Inits Increase: 0	pass
From stage 1 to stage 2, the CURR_NE/FE_24_FRA counter increase SHALL be 0.	Reported No FRA Increase: FE: 0 NE: 0	pass
From stage 1 to stage 2, the CURR_NE/FE_24_SRA counter increase SHALL be >= 1.	Reported No SRA Increase: FE: 4 NE: 2	pass
From stage 1 to stage 2, the CURR_NE/FE_24_CV counter increase SHALL be 0.	Reported No CV Increase: FE: 0 NE: 0	pass
From stage 1 to stage 2, the CURR_NE/FE_24_ES counter increase SHALL be 0.	Reported No ES Increase: FE: 0 NE: 0	pass
From stage 1 to stage 2, the CURR_NE/FE_24_SES counter increase SHALL be 0.	Reported No SES Increase: FE: 0 NE: 0	pass
From stage 1 to stage 2, the CURR_NE/FE_24_UAS counter increase SHALL be 0.	Reported No UAS Increase: FE: 0 NE: 0	pass
From stage 1 to stage 2, the NDRds/us SHALL increase.	Reported NDR Increase: DS: 337922 US: 116247	pass
From stage 2 to stage 3, the CURR_24_FULL_INITS counter increase SHALL be 0.	Reported No Full Inits Increase: 0	pass
From stage 2 to stage 3, the CURR_24_FAST_INITS counter increase SHALL be 0.	Reported No Fast Inits Increase: 0	pass

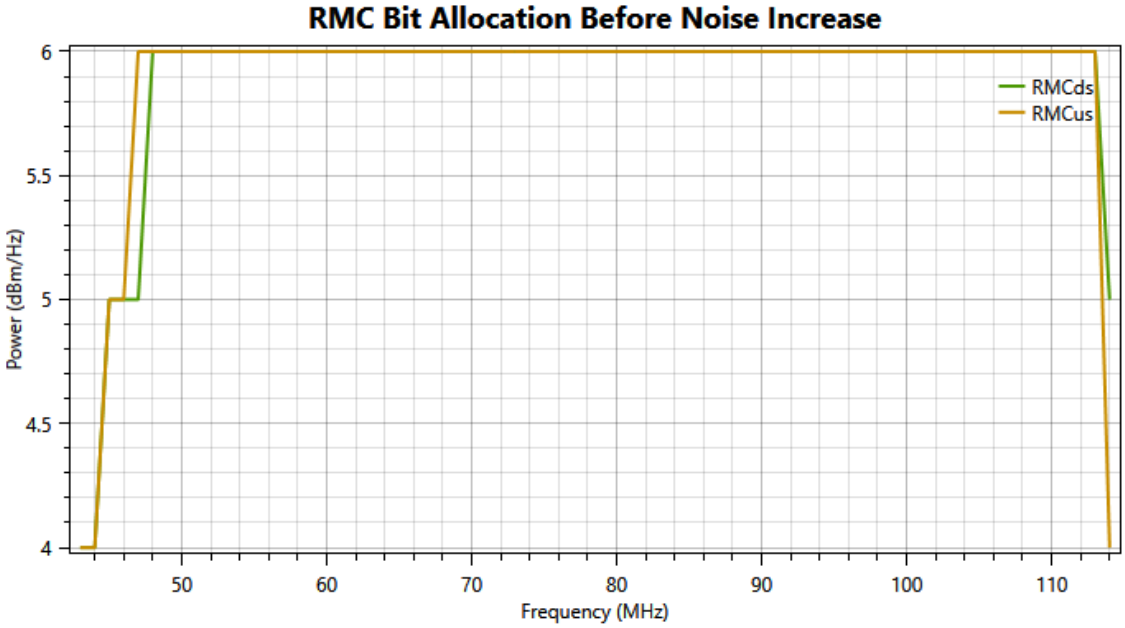
From stage 2 to stage 3, the CURR_NE/FE_24_FRA counter increase SHALL be >= 1.	Reported FRA Increase: FE: 2 NE: 1	pass
From stage 2 to stage 3, the CURR_NE/FE_24_SRA counter increase SHALL be >= 1.	Reported SRA Increase: FE: 2 NE: 1	pass
From stage 2 to stage 3, the CURR_NE/FE_24_CV counter increase SHALL be <= 5.	Reported CV Increase of 3 or Less: FE: 2 NE: 1	pass
From stage 2 to stage 3, the CURR_NE/FE_24_ES counter increase SHALL be <= 2.	Reported ES Increas 2 or Less: FE: 1 NE: 1	pass
From stage 2 to stage 3, the CURR_NE/FE_24_SES counter increase SHALL be 0.	Reported No SES Increase: FE: 0 NE: 0	pass
From stage 2 to stage 3, the CURR_NE/FE_24_UAS counter increase SHALL be 0.	Reported No UAS Increase: FE: 0 NE: 0	pass
From stage 2 to stage 3, the NDRds/us SHALL decrease.	Reported NDR Decrease: DS: -346520 US: -115174	pass

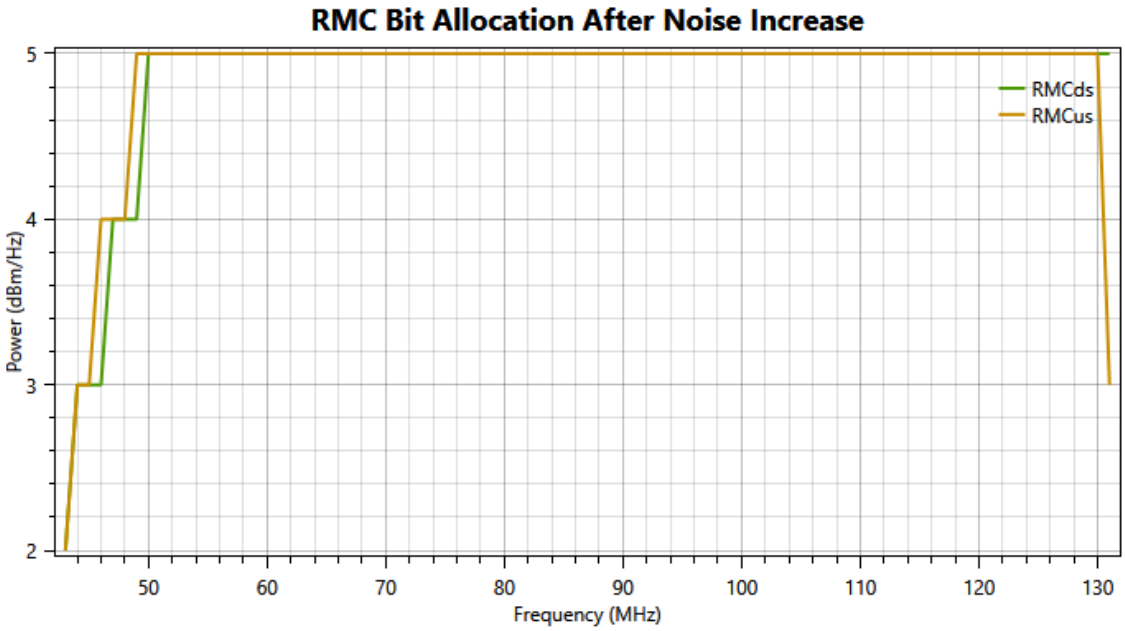
6.10 RPA Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT is capable of adjusting RMC parameters if the external noise changes over time. The RMC parameter adjustment (RPA) is triggered when the SNR margin of the RMC (either SNRM-RMCds or SNRM-RMCus) is lower than the minimum SNR margin of the RMC (MINSNRM-RMCds or MINSNRM-RMCus).

Results - RMC RPA	
Results from Before Noise Increase	
SNRM-RMCds	16.5
SNRM-RMCus	16.9
BITSRMCpsds	(Refer to Figure)
BITSRMCpsus	(Refer to Figure)
CURR_24_FULL_INITS	5
CURR_24_FAST_INITS	0
CURR_NE_24_RPA	0
CURR_FE_24_RPA	0
Results from After Noise Increase	
SNRM-RMCds	10.3
SNRM-RMCus	10
BITSRMCpsds	(Refer to Figure)
BITSRMCpsus	(Refer to Figure)
CURR_24_FULL_INITS	5
CURR_24_FAST_INITS	0
CURR_NE_24_RPA	1
CURR_FE_24_RPA	1


RMC Bit Allocation Before Noise Increase
Power (dBm/Hz)
Frequency (MHz)
RMCds
RMCus
6
5.5
5
4.5
4
50
60
70
80
90
100
110


RMC Bit Allocation After Noise Increase
Power (dBm/Hz)
Frequency (MHz)
RMCds
RMCus
5
4
3
2
50
60
70
80
90
100
110
120
130

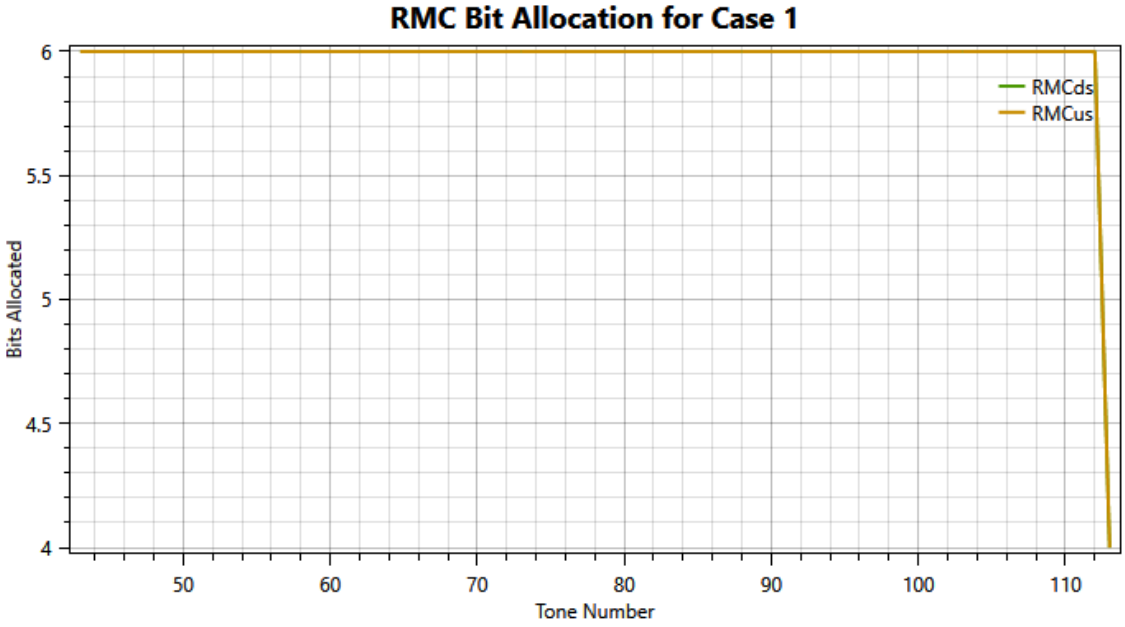
Summary		
The CURR_24_FULL_INITS counter increase SHALL be 0.	Reported No Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0.	Reported No Increase: 0	pass
The CURR_NE/FE_24_RPA increase SHALL be >= 1.	Reported Increase: FE:1NE: 1	pass
The RMC bit loading (BITSRMCpsds) SHALL change	Reported Change	pass
The RMC bit loading (BITSRMCpsus) SHALL change	Reported Change	pass

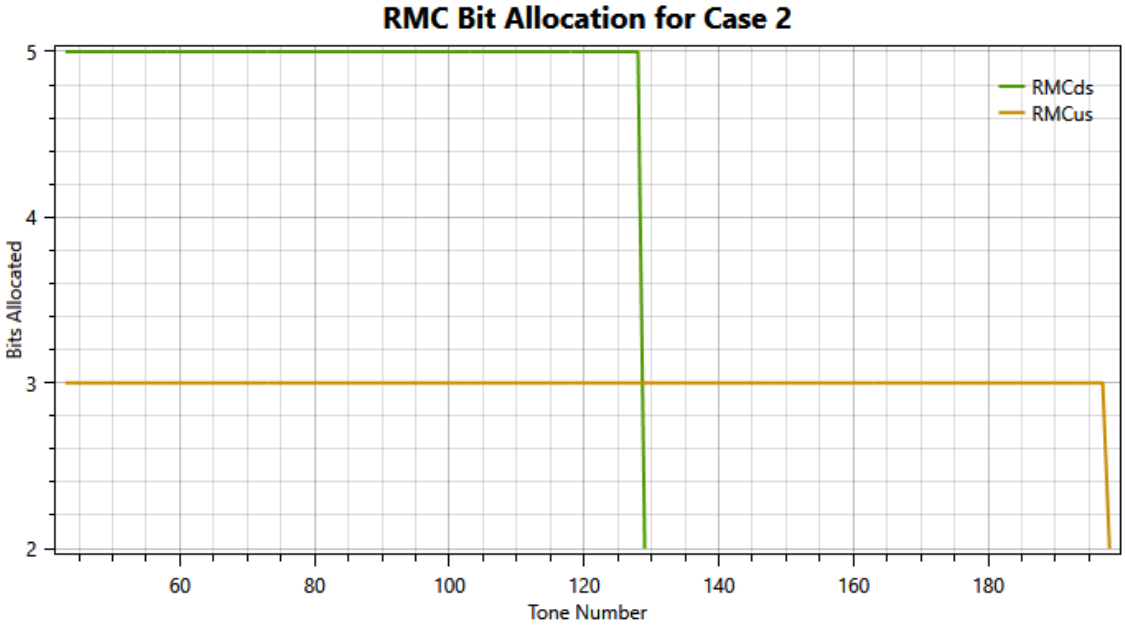
6.11 RMC Bit Loading Configuration Test	PARTS	RESULTS
		PASS

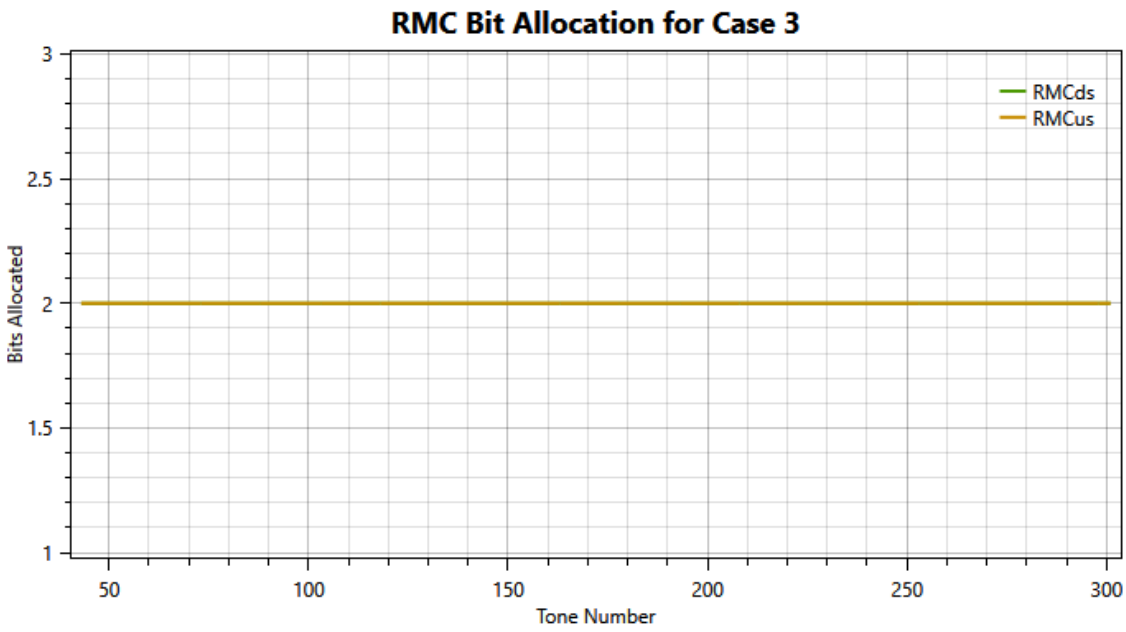
PURPOSE

The purpose of this test is to verify that the DUT is capable to perform accurately the RMC bit loading configuration.

Results - Case 1: 6/6-bits (100m)		**Results - Case 2: 5/3-bits (100m)**		**Results - Case 3: 2/2-bits (100m)**	
SNRM-RMCds	27.6	**SNRM-RMCds**	31.3	**SNRM-RMCds**	42.5
SNRM-RMCus	28.7	**SNRM-RMCus**	39.2	**SNRM-RMCus**	43.4
BITSRMCpsds	(Refer to Figure)	**BITSRMCpsds**	(Refer to Figure)	**BITSRMCpsds**	(Refer to Figure)
BITSRMCpsus	(Refer to Figure)	**BITSRMCpsus**	(Refer to Figure)	**BITSRMCpsus**	(Refer to Figure)


RMC Bit Allocation for Case 1
RMCds
RMCus
Bits Allocated
Tone Number
6
5.5
5
4.5
4
50
60
70
80
90
100
110


RMC Bit Allocation for Case 2
RMCds
RMCus
Bits Allocated
Tone Number
5
4
3
2
60
80
100
120
140
160
180



Summary		
Test Case 1: 6/6-bits (100m): The BITSRMCpsds/us SHALL meet the bounds of the MAXBL-RMCds/us specified in Table 46.	Bit Loading Within Range	pass
Test Case 1: 6/6-bits (100m): The SNRM-RMCds/us SHALL be >= TARSNRM-RMCds/us - 1 dB.	Measured SNRM-RMC In Bounds: DS: 27.6 US: 28.7	pass
Test Case 2: 5/3-bits (100m): The BITSRMCpsds/us SHALL meet the bounds of the MAXBL-RMCds/us specified in Table 46.	Bit Loading Within Range	pass
Test Case 2: 5/3-bits (100m): The SNRM-RMCds/us SHALL be >= TARSNRM-RMCds/us - 1 dB.	Measured SNRM-RMC In Bounds: DS: 31.3 US: 39.2	pass
Test Case 3: 2/2-bits (100m): The BITSRMCpsds/us SHALL meet the bounds of the MAXBL-RMCds/us specified in Table 46.	Bit Loading Within Range	pass
Test Case 3: 2/2-bits (100m): The SNRM-RMCds/us SHALL be >= TARSNRM-RMCds/us - 1 dB.	Measured SNRM-RMC In Bounds: DS: 42.5 US: 43.4	pass

6.12.1 Re-Initialization Policy Short disconnect Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT supports the re-initialization policy for a loop disconnect that is shorter than the los and lor defect persistency and hence does not cause a re-initialization.

Results - Downstream (100m) Test 1			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	5	**CURR_24_FULL_INITS**	5
CURR_NE_24_LOS	0	**CURR_NE_24_LOS**	0
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	3	**CURR_NE_24_ES**	4
CURR_FE_24_ES	4	**CURR_FE_24_ES**	5
CURR_NE_24_SES	3	**CURR_NE_24_SES**	4
CURR_FE_24_SES	1	**CURR_FE_24_SES**	2
CURR_NE_24_UAS	131	**CURR_NE_24_UAS**	131
CURR_FE_24_UAS	131	**CURR_FE_24_UAS**	131
CURR_NE_24_LOSS	0	**CURR_NE_24_LOSS**	1
CURR_FE_24_LOSS	0	**CURR_FE_24_LOSS**	1

CURR_NE_24_LORS	0	**CURR_NE_24_LORS**	1
CURR_FE_24_LORS	0	**CURR_FE_24_LORS**	1

Results - Downstream (100m) Test 2			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	6	**CURR_24_FULL_INITS**	6
CURR_NE_24_LOS	0	**CURR_NE_24_LOS**	0
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	4	**CURR_NE_24_ES**	6
CURR_FE_24_ES	5	**CURR_FE_24_ES**	7
CURR_NE_24_SES	4	**CURR_NE_24_SES**	6

CURR_FE_24_SES	2	**CURR_FE_24_SES**	3
CURR_NE_24_UAS	155	**CURR_NE_24_UAS**	155
CURR_FE_24_UAS	155	**CURR_FE_24_UAS**	155
CURR_NE_24_LOSS	1	**CURR_NE_24_LOSS**	2
CURR_FE_24_LOSS	1	**CURR_FE_24_LOSS**	2
CURR_NE_24_LORS	1	**CURR_NE_24_LORS**	3
CURR_FE_24_LORS	1	**CURR_FE_24_LORS**	3

Results - Downstream (100m) Test 3			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	7	**CURR_24_FULL_INITS**	7
CURR_NE_24_LOS	0	**CURR_NE_24_LOS**	0
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0

CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	6	**CURR_NE_24_ES**	7
CURR_FE_24_ES	7	**CURR_FE_24_ES**	8
CURR_NE_24_SES	6	**CURR_NE_24_SES**	7
CURR_FE_24_SES	3	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	178	**CURR_NE_24_UAS**	178
CURR_FE_24_UAS	178	**CURR_FE_24_UAS**	178
CURR_NE_24_LOSS	2	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	2	**CURR_FE_24_LOSS**	3
CURR_NE_24_LORS	3	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	3	**CURR_FE_24_LORS**	4

Results - Downstream (100m) Test 4			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	8	**CURR_24_FULL_INITS**	8
CURR_NE_24_LOS	0	**CURR_NE_24_LOS**	0
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	7	**CURR_NE_24_ES**	9
CURR_FE_24_ES	8	**CURR_FE_24_ES**	10
CURR_NE_24_SES	7	**CURR_NE_24_SES**	9
CURR_FE_24_SES	4	**CURR_FE_24_SES**	6
CURR_NE_24_UAS	202	**CURR_NE_24_UAS**	202
CURR_FE_24_UAS	202	**CURR_FE_24_UAS**	202
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	5
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	5

CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	6
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	7

Results - Downstream (100m) Test 5			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	9	**CURR_24_FULL_INITS**	9
CURR_NE_24_LOS	0	**CURR_NE_24_LOS**	0
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	9	**CURR_NE_24_ES**	10
CURR_FE_24_ES	10	**CURR_FE_24_ES**	11
CURR_NE_24_SES	9	**CURR_NE_24_SES**	10

CURR_FE_24_SES	6	**CURR_FE_24_SES**	7
CURR_NE_24_UAS	226	**CURR_NE_24_UAS**	226
CURR_FE_24_UAS	226	**CURR_FE_24_UAS**	226
CURR_NE_24_LOSS	5	**CURR_NE_24_LOSS**	6
CURR_FE_24_LOSS	5	**CURR_FE_24_LOSS**	6
CURR_NE_24_LORS	6	**CURR_NE_24_LORS**	7
CURR_FE_24_LORS	7	**CURR_FE_24_LORS**	8

Results - Upstream (100m) Test 1			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	10	**CURR_24_FULL_INITS**	10
CURR_NE_24_LOS	0	**CURR_NE_24_LOS**	0
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0

CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	10	**CURR_NE_24_ES**	11
CURR_FE_24_ES	11	**CURR_FE_24_ES**	12
CURR_NE_24_SES	10	**CURR_NE_24_SES**	11
CURR_FE_24_SES	7	**CURR_FE_24_SES**	8
CURR_NE_24_UAS	250	**CURR_NE_24_UAS**	250
CURR_FE_24_UAS	250	**CURR_FE_24_UAS**	250
CURR_NE_24_LOSS	6	**CURR_NE_24_LOSS**	7
CURR_FE_24_LOSS	6	**CURR_FE_24_LOSS**	7
CURR_NE_24_LORS	7	**CURR_NE_24_LORS**	8
CURR_FE_24_LORS	8	**CURR_FE_24_LORS**	9

Results - Upstream (100m) Test 2			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	11	**CURR_24_FULL_INITS**	11
CURR_NE_24_LOS	0	**CURR_NE_24_LOS**	0
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	11	**CURR_NE_24_ES**	12
CURR_FE_24_ES	12	**CURR_FE_24_ES**	13
CURR_NE_24_SES	11	**CURR_NE_24_SES**	12
CURR_FE_24_SES	8	**CURR_FE_24_SES**	9
CURR_NE_24_UAS	273	**CURR_NE_24_UAS**	273
CURR_FE_24_UAS	273	**CURR_FE_24_UAS**	273
CURR_NE_24_LOSS	7	**CURR_NE_24_LOSS**	8
CURR_FE_24_LOSS	7	**CURR_FE_24_LOSS**	8

CURR_NE_24_LORS	8	**CURR_NE_24_LORS**	9
CURR_FE_24_LORS	9	**CURR_FE_24_LORS**	10

Results - Upstream (100m) Test 3			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	12	**CURR_24_FULL_INITS**	12
CURR_NE_24_LOS	0	**CURR_NE_24_LOS**	0
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	12	**CURR_NE_24_ES**	13
CURR_FE_24_ES	13	**CURR_FE_24_ES**	14
CURR_NE_24_SES	12	**CURR_NE_24_SES**	13

CURR_FE_24_SES	9	**CURR_FE_24_SES**	10
CURR_NE_24_UAS	297	**CURR_NE_24_UAS**	297
CURR_FE_24_UAS	297	**CURR_FE_24_UAS**	297
CURR_NE_24_LOSS	8	**CURR_NE_24_LOSS**	9
CURR_FE_24_LOSS	8	**CURR_FE_24_LOSS**	9
CURR_NE_24_LORS	9	**CURR_NE_24_LORS**	10
CURR_FE_24_LORS	10	**CURR_FE_24_LORS**	11

Results - Upstream (100m) Test 4			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	13	**CURR_24_FULL_INITS**	13
CURR_NE_24_LOS	0	**CURR_NE_24_LOS**	0
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0

CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	13	**CURR_NE_24_ES**	15
CURR_FE_24_ES	14	**CURR_FE_24_ES**	16
CURR_NE_24_SES	13	**CURR_NE_24_SES**	15
CURR_FE_24_SES	10	**CURR_FE_24_SES**	11
CURR_NE_24_UAS	321	**CURR_NE_24_UAS**	321
CURR_FE_24_UAS	321	**CURR_FE_24_UAS**	321
CURR_NE_24_LOSS	9	**CURR_NE_24_LOSS**	10
CURR_FE_24_LOSS	9	**CURR_FE_24_LOSS**	10
CURR_NE_24_LORS	10	**CURR_NE_24_LORS**	12
CURR_FE_24_LORS	11	**CURR_FE_24_LORS**	13

Results - Upstream (100m) Test 5			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	14	**CURR_24_FULL_INITS**	14
CURR_NE_24_LOS	0	**CURR_NE_24_LOS**	0
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	15	**CURR_NE_24_ES**	16
CURR_FE_24_ES	16	**CURR_FE_24_ES**	17
CURR_NE_24_SES	15	**CURR_NE_24_SES**	16
CURR_FE_24_SES	11	**CURR_FE_24_SES**	12
CURR_NE_24_UAS	345	**CURR_NE_24_UAS**	345
CURR_FE_24_UAS	345	**CURR_FE_24_UAS**	345
CURR_NE_24_LOSS	10	**CURR_NE_24_LOSS**	11
CURR_FE_24_LOSS	10	**CURR_FE_24_LOSS**	11

CURR_NE_24_LORS	12	**CURR_NE_24_LORS**	13
CURR_FE_24_LORS	13	**CURR_FE_24_LORS**	14

Summary		
The CURR_24_FAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FE_LOSS counter increase SHALL be >= 1 (Downstream (100m) Test 1).	Reported Value In Range: 1	pass
The CURR_24_FE_LORS counter increase SHALL be >= 1 (Downstream (100m) Test 1).	Reported Value In Range: 1	pass
The CURR_24_FE_ES counter increase SHALL be >= 1 (Downstream (100m) Test 1).	Reported Value In Range: 1	pass
The CURR_24_FE_SES counter increase SHALL be >= 1 (Downstream (100m) Test 1).	Reported Value In Range: 1	pass

The CURR_24_FE_LOS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FE_LOR counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FE_LOM counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_FE_24_CV counter increase SHALL be <= 4 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_FE_24_RTXUC counter increase SHALL be >= 13 (Downstream (100m) Test 1).	Reported No Increase: 3230	pass
The CURR_FE_24_RTXTX counter increase SHALL be >= 80 (Downstream (100m) Test 1).	Reported No Increase: 348	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FE_LOSS counter increase SHALL be >= 1 (Downstream (100m) Test 2).	Reported Value In Range: 1	pass

The CURR_24_FE_LORS counter increase SHALL be >= 1 (Downstream (100m) Test 2).	Reported Value In Range: 2	pass
The CURR_24_FE_ES counter increase SHALL be >= 1 (Downstream (100m) Test 2).	Reported Value In Range: 2	pass
The CURR_24_FE_SES counter increase SHALL be >= 1 (Downstream (100m) Test 2).	Reported Value In Range: 1	pass
The CURR_24_FE_LOS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FE_LOR counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FE_LOM counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_FE_24_CV counter increase SHALL be <= 4 (Downstream (100m) Test 2).	Reported No Increase: 2	pass
The CURR_FE_24_RTXUC counter increase SHALL be >= 13 (Downstream (100m) Test 2).	Reported No Increase: 3155	pass
The CURR_FE_24_RTXTX counter increase SHALL be >= 80 (Downstream (100m) Test 2).	Reported No Increase: 325	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass

The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FE_LOSS counter increase SHALL be >= 1 (Downstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_FE_LORS counter increase SHALL be >= 1 (Downstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_FE_ES counter increase SHALL be >= 1 (Downstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_FE_SES counter increase SHALL be >= 1 (Downstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_FE_LOS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FE_LOR counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FE_LOM counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_FE_24_CV counter increase SHALL be <= 4 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_FE_24_RTXUC counter increase SHALL be >= 13 (Downstream (100m) Test 3).	Reported No Increase: 3222	pass
The CURR_FE_24_RTXTX counter increase SHALL be >= 80 (Downstream (100m) Test 3).	Reported No Increase: 316	pass

The CURR_24_FAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FE_LOSS counter increase SHALL be >= 1 (Downstream (100m) Test 4).	Reported Value In Range: 2	pass
The CURR_24_FE_LORS counter increase SHALL be >= 1 (Downstream (100m) Test 4).	Reported Value In Range: 3	pass
The CURR_24_FE_ES counter increase SHALL be >= 1 (Downstream (100m) Test 4).	Reported Value In Range: 2	pass
The CURR_24_FE_SES counter increase SHALL be >= 1 (Downstream (100m) Test 4).	Reported Value In Range: 2	pass
The CURR_24_FE_LOS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FE_LOR counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FE_LOM counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass

The CURR_FE_24_CV counter increase SHALL be <= 4 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_FE_24_RTXUC counter increase SHALL be >= 13 (Downstream (100m) Test 4).	Reported No Increase: 3211	pass
The CURR_FE_24_RTXTX counter increase SHALL be >= 80 (Downstream (100m) Test 4).	Reported No Increase: 325	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FE_LOSS counter increase SHALL be >= 1 (Downstream (100m) Test 5).	Reported Value In Range: 1	pass
The CURR_24_FE_LORS counter increase SHALL be >= 1 (Downstream (100m) Test 5).	Reported Value In Range: 1	pass
The CURR_24_FE_ES counter increase SHALL be >= 1 (Downstream (100m) Test 5).	Reported Value In Range: 1	pass
The CURR_24_FE_SES counter increase SHALL be >= 1 (Downstream (100m) Test 5).	Reported Value In Range: 1	pass

The CURR_24_FE_LOS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FE_LOR counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FE_LOM counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_FE_24_CV counter increase SHALL be <= 4 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_FE_24_RTXUC counter increase SHALL be >= 13 (Downstream (100m) Test 5).	Reported No Increase: 3372	pass
The CURR_FE_24_RTXTX counter increase SHALL be >= 80 (Downstream (100m) Test 5).	Reported No Increase: 334	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_LOSS counter increase SHALL be >= 1 (Upstream (100m) Test 1).	Reported Value In Range: 1	pass

The CURR_24_NE_LORS counter increase SHALL be >= 1 (Upstream (100m) Test 1).	Reported Value In Range: 1	pass
The CURR_24_NE_ES counter increase SHALL be >= 1 (Upstream (100m) Test 1).	Reported Value In Range: 1	pass
The CURR_24_NE_SES counter increase SHALL be >= 1 (Upstream (100m) Test 1).	Reported Value In Range: 1	pass
The CURR_24_NE_LOS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_LOR counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_NE_24_CV counter increase SHALL be <= 4 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_NE_24_RTXUC counter increase SHALL be >= 13 (Upstream (100m) Test 1).	Reported No Increase: 34	pass
The CURR_NE_24_RTXTX counter increase SHALL be >= 80 (Upstream (100m) Test 1).	Reported No Increase: 111	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass

The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_LOSS counter increase SHALL be >= 1 (Upstream (100m) Test 2).	Reported Value In Range: 1	pass
The CURR_24_NE_LORS counter increase SHALL be >= 1 (Upstream (100m) Test 2).	Reported Value In Range: 1	pass
The CURR_24_NE_ES counter increase SHALL be >= 1 (Upstream (100m) Test 2).	Reported Value In Range: 1	pass
The CURR_24_NE_SES counter increase SHALL be >= 1 (Upstream (100m) Test 2).	Reported Value In Range: 1	pass
The CURR_24_NE_LOS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_LOR counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_NE_24_CV counter increase SHALL be <= 4 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_NE_24_RTXUC counter increase SHALL be >= 13 (Upstream (100m) Test 2).	Reported No Increase: 35	pass
The CURR_NE_24_RTXTX counter increase SHALL be >= 80 (Upstream (100m) Test 2).	Reported No Increase: 110	pass

The CURR_24_FAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_LOSS counter increase SHALL be >= 1 (Upstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_NE_LORS counter increase SHALL be >= 1 (Upstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_NE_ES counter increase SHALL be >= 1 (Upstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_NE_SES counter increase SHALL be >= 1 (Upstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_NE_LOS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_LOR counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass

The CURR_NE_24_CV counter increase SHALL be <= 4 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_NE_24_RTXUC counter increase SHALL be >= 13 (Upstream (100m) Test 3).	Reported No Increase: 33	pass
The CURR_NE_24_RTXTX counter increase SHALL be >= 80 (Upstream (100m) Test 3).	Reported No Increase: 105	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_LOSS counter increase SHALL be >= 1 (Upstream (100m) Test 4).	Reported Value In Range: 1	pass
The CURR_24_NE_LORS counter increase SHALL be >= 1 (Upstream (100m) Test 4).	Reported Value In Range: 2	pass
The CURR_24_NE_ES counter increase SHALL be >= 1 (Upstream (100m) Test 4).	Reported Value In Range: 2	pass
The CURR_24_NE_SES counter increase SHALL be >= 1 (Upstream (100m) Test 4).	Reported Value In Range: 2	pass

The CURR_24_NE_LOS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_LOR counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_NE_24_CV counter increase SHALL be <= 4 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_NE_24_RTXUC counter increase SHALL be >= 13 (Upstream (100m) Test 4).	Reported No Increase: 36	pass
The CURR_NE_24_RTXTX counter increase SHALL be >= 80 (Upstream (100m) Test 4).	Reported No Increase: 104	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_LOSS counter increase SHALL be >= 1 (Upstream (100m) Test 5).	Reported Value In Range: 1	pass

The CURR_24_NE_LORS counter increase SHALL be >= 1 (Upstream (100m) Test 5).	Reported Value In Range: 1	pass
The CURR_24_NE_ES counter increase SHALL be >= 1 (Upstream (100m) Test 5).	Reported Value In Range: 1	pass
The CURR_24_NE_SES counter increase SHALL be >= 1 (Upstream (100m) Test 5).	Reported Value In Range: 1	pass
The CURR_24_NE_LOS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_LOR counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_NE_24_CV counter increase SHALL be <= 4 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_NE_24_RTXUC counter increase SHALL be >= 13 (Upstream (100m) Test 5).	Reported No Increase: 30	pass
The CURR_NE_24_RTXTX counter increase SHALL be >= 80 (Upstream (100m) Test 5).	Reported No Increase: 104	pass

6.12.2 Re-Initialization Policy Long disconnect Test with LOS failure	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT supports the re-initialization policy for a loop disconnect that is longer than the los defect persistency and shorter than the lor defect persistency, and hence causes a reinitialization triggered by a LOS failure.

Results - Downstream (100m) Test 1			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	1
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	8	**CURR_24_FULL_INITS**	8
CURR_NE_24_LOS	0	**CURR_NE_24_LOS**	1
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	7	**CURR_NE_24_ES**	7
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	202	**CURR_NE_24_UAS**	209
CURR_FE_24_UAS	202	**CURR_FE_24_UAS**	208
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3

CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Downstream (100m) Test 2			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	1	**CURR_24_FAST_INITS**	2
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	9	**CURR_24_FULL_INITS**	9
CURR_NE_24_LOS	1	**CURR_NE_24_LOS**	2
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	7	**CURR_NE_24_ES**	8
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7

CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	233	**CURR_NE_24_UAS**	239
CURR_FE_24_UAS	232	**CURR_FE_24_UAS**	238
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3
CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Downstream (100m) Test 3			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	2	**CURR_24_FAST_INITS**	3
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	10	**CURR_24_FULL_INITS**	10
CURR_NE_24_LOS	2	**CURR_NE_24_LOS**	3
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0

CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	8	**CURR_NE_24_ES**	8
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	263	**CURR_NE_24_UAS**	270
CURR_FE_24_UAS	262	**CURR_FE_24_UAS**	269
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3
CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Downstream (100m) Test 4			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	3	**CURR_24_FAST_INITS**	4
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	11	**CURR_24_FULL_INITS**	11
CURR_NE_24_LOS	3	**CURR_NE_24_LOS**	4
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	8	**CURR_NE_24_ES**	8
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	294	**CURR_NE_24_UAS**	301
CURR_FE_24_UAS	293	**CURR_FE_24_UAS**	300
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3

CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Downstream (100m) Test 5			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	4	**CURR_24_FAST_INITS**	5
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	12	**CURR_24_FULL_INITS**	12
CURR_NE_24_LOS	4	**CURR_NE_24_LOS**	5
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	8	**CURR_NE_24_ES**	8
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7

CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	325	**CURR_NE_24_UAS**	332
CURR_FE_24_UAS	324	**CURR_FE_24_UAS**	331
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3
CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Upstream (100m) Test 1			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	5	**CURR_24_FAST_INITS**	6
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	13	**CURR_24_FULL_INITS**	13
CURR_NE_24_LOS	5	**CURR_NE_24_LOS**	6
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0

CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	8	**CURR_NE_24_ES**	8
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	356	**CURR_NE_24_UAS**	363
CURR_FE_24_UAS	355	**CURR_FE_24_UAS**	361
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3
CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Upstream (100m) Test 2			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	6	**CURR_24_FAST_INITS**	7
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	14	**CURR_24_FULL_INITS**	14
CURR_NE_24_LOS	6	**CURR_NE_24_LOS**	7
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	8	**CURR_NE_24_ES**	8
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	387	**CURR_NE_24_UAS**	394
CURR_FE_24_UAS	385	**CURR_FE_24_UAS**	391
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3

CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Upstream (100m) Test 3			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	7	**CURR_24_FAST_INITS**	8
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	15	**CURR_24_FULL_INITS**	15
CURR_NE_24_LOS	7	**CURR_NE_24_LOS**	8
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	8	**CURR_NE_24_ES**	8
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7

CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	418	**CURR_NE_24_UAS**	425
CURR_FE_24_UAS	415	**CURR_FE_24_UAS**	422
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3
CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Upstream (100m) Test 4			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	8	**CURR_24_FAST_INITS**	9
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	16	**CURR_24_FULL_INITS**	16
CURR_NE_24_LOS	8	**CURR_NE_24_LOS**	9
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0

CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	8	**CURR_NE_24_ES**	8
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	449	**CURR_NE_24_UAS**	456
CURR_FE_24_UAS	446	**CURR_FE_24_UAS**	453
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3
CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Upstream (100m) Test 5			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	9	**CURR_24_FAST_INITS**	10
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	17	**CURR_24_FULL_INITS**	17
CURR_NE_24_LOS	9	**CURR_NE_24_LOS**	10
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	0
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	8	**CURR_NE_24_ES**	8
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	480	**CURR_NE_24_UAS**	488
CURR_FE_24_UAS	477	**CURR_FE_24_UAS**	484
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3

CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Summary		
The CURR_24_FAST_INITS counter increase SHALL be 1 (Downstream (100m) Test 1).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be >= 1 and <= 15 (Downstream (100m) Test 1).	Reported Value In Range: 6	pass
The CURR_24_FE_LOSS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FE_LORS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FE_ES counter increase SHALL be <= 1 (Downstream (100m) Test 1).	Reported Value In Range: 0	pass
The CURR_24_FE_SES counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 1 (Downstream (100m) Test 2).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be >= 1 and <= 15 (Downstream (100m) Test 2).	Reported Value In Range: 6	pass
The CURR_24_FE_LOSS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FE_LORS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FE_ES counter increase SHALL be <= 1 (Downstream (100m) Test 2).	Reported Value In Range: 0	pass
The CURR_24_FE_SES counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 1 (Downstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass

The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be >= 1 and <= 15 (Downstream (100m) Test 3).	Reported Value In Range: 7	pass
The CURR_24_FE_LOSS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FE_LORS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FE_ES counter increase SHALL be <= 1 (Downstream (100m) Test 3).	Reported Value In Range: 0	pass
The CURR_24_FE_SES counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 1 (Downstream (100m) Test 4).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be >= 1 and <= 15 (Downstream (100m) Test 4).	Reported Value In Range: 7	pass
The CURR_24_FE_LOSS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass

The CURR_24_FE_LORS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FE_ES counter increase SHALL be <= 1 (Downstream (100m) Test 4).	Reported Value In Range: 0	pass
The CURR_24_FE_SES counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 1 (Downstream (100m) Test 5).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be >= 1 and <= 15 (Downstream (100m) Test 5).	Reported Value In Range: 7	pass
The CURR_24_FE_LOSS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FE_LORS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FE_ES counter increase SHALL be <= 1 (Downstream (100m) Test 5).	Reported Value In Range: 0	pass
The CURR_24_FE_SES counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 1 (Upstream (100m) Test 1).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be >= 1 and <= 15 (Upstream (100m) Test 1).	Reported Value In Range: 7	pass
The CURR_24_NE_LOSS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_LORS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_ES counter increase SHALL be <= 1 (Upstream (100m) Test 1).	Reported Value In Range: 0	pass
The CURR_24_NE_SES counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_LOS counter increase SHALL be 1 (Upstream (100m) Test 1).	Reported Value In Range: 1	pass
The CURR_24_NE_LOR counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 1 (Upstream (100m) Test 2).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be >= 1 and <= 15 (Upstream (100m) Test 2).	Reported Value In Range: 7	pass
The CURR_24_NE_LOSS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_LORS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_ES counter increase SHALL be <= 1 (Upstream (100m) Test 2).	Reported Value In Range: 0	pass
The CURR_24_NE_SES counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_LOS counter increase SHALL be 1 (Upstream (100m) Test 2).	Reported Value In Range: 1	pass
The CURR_24_NE_LOR counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 1 (Upstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be >= 1 and <= 15 (Upstream (100m) Test 3).	Reported Value In Range: 7	pass
The CURR_24_NE_LOSS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_LORS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_ES counter increase SHALL be <= 1 (Upstream (100m) Test 3).	Reported Value In Range: 0	pass
The CURR_24_NE_SES counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_LOS counter increase SHALL be 1 (Upstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_NE_LOR counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 1 (Upstream (100m) Test 4).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be >= 1 and <= 15 (Upstream (100m) Test 4).	Reported Value In Range: 7	pass
The CURR_24_NE_LOSS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_LORS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_ES counter increase SHALL be <= 1 (Upstream (100m) Test 4).	Reported Value In Range: 0	pass
The CURR_24_NE_SES counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_LOS counter increase SHALL be 1 (Upstream (100m) Test 4).	Reported Value In Range: 1	pass
The CURR_24_NE_LOR counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 1 (Upstream (100m) Test 5).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be >= 1 and <= 15 (Upstream (100m) Test 5).	Reported Value In Range: 8	pass
The CURR_24_NE_LOSS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_LORS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_ES counter increase SHALL be <= 1 (Upstream (100m) Test 5).	Reported Value In Range: 0	pass
The CURR_24_NE_SES counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_LOS counter increase SHALL be 1 (Upstream (100m) Test 5).	Reported Value In Range: 1	pass
The CURR_24_NE_LOR counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass

6.12.3 Re-Initialization Policy Long disconnect Test with LOR failure	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT supports the re-initialization policy for a loop disconnect that is shorter than the los defect persistency and longer than the lor defect persistency, and hence causes a reinitialization triggered by a LOR failure.

Results - Downstream (100m) Test 1			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	7	**CURR_24_FAST_INITS**	8
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	15	**CURR_24_FULL_INITS**	15
CURR_NE_24_LOS	7	**CURR_NE_24_LOS**	7
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	0	**CURR_NE_24_LOR**	1
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	8	**CURR_NE_24_ES**	8
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	418	**CURR_NE_24_UAS**	425
CURR_FE_24_UAS	415	**CURR_FE_24_UAS**	422
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3

CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Downstream (100m) Test 2			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	8	**CURR_24_FAST_INITS**	9
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	16	**CURR_24_FULL_INITS**	16
CURR_NE_24_LOS	7	**CURR_NE_24_LOS**	7
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	1	**CURR_NE_24_LOR**	2
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	8	**CURR_NE_24_ES**	8
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7

CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	449	**CURR_NE_24_UAS**	456
CURR_FE_24_UAS	446	**CURR_FE_24_UAS**	453
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3
CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Downstream (100m) Test 3			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	9	**CURR_24_FAST_INITS**	10
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	17	**CURR_24_FULL_INITS**	17
CURR_NE_24_LOS	7	**CURR_NE_24_LOS**	7
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	2	**CURR_NE_24_LOR**	3
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0

CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	8	**CURR_NE_24_ES**	9
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	480	**CURR_NE_24_UAS**	487
CURR_FE_24_UAS	477	**CURR_FE_24_UAS**	484
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3
CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Downstream (100m) Test 4			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	10	**CURR_24_FAST_INITS**	11
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	18	**CURR_24_FULL_INITS**	18
CURR_NE_24_LOS	7	**CURR_NE_24_LOS**	7
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	3	**CURR_NE_24_LOR**	4
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	9	**CURR_NE_24_ES**	9
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	511	**CURR_NE_24_UAS**	519
CURR_FE_24_UAS	508	**CURR_FE_24_UAS**	515
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3

CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Downstream (100m) Test 5			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	11	**CURR_24_FAST_INITS**	12
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	19	**CURR_24_FULL_INITS**	19
CURR_NE_24_LOS	7	**CURR_NE_24_LOS**	7
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	4	**CURR_NE_24_LOR**	5
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	9	**CURR_NE_24_ES**	9
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7

CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	543	**CURR_NE_24_UAS**	550
CURR_FE_24_UAS	539	**CURR_FE_24_UAS**	546
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3
CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Upstream (100m) Test 1			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	12	**CURR_24_FAST_INITS**	13
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	20	**CURR_24_FULL_INITS**	20
CURR_NE_24_LOS	7	**CURR_NE_24_LOS**	7
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	5	**CURR_NE_24_LOR**	6
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0

CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	9	**CURR_NE_24_ES**	9
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	574	**CURR_NE_24_UAS**	581
CURR_FE_24_UAS	570	**CURR_FE_24_UAS**	577
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3
CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Upstream (100m) Test 2			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	13	**CURR_24_FAST_INITS**	14
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	21	**CURR_24_FULL_INITS**	21
CURR_NE_24_LOS	7	**CURR_NE_24_LOS**	7
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	6	**CURR_NE_24_LOR**	7
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	9	**CURR_NE_24_ES**	9
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	605	**CURR_NE_24_UAS**	612
CURR_FE_24_UAS	601	**CURR_FE_24_UAS**	608
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3

CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Upstream (100m) Test 3			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	14	**CURR_24_FAST_INITS**	15
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	22	**CURR_24_FULL_INITS**	22
CURR_NE_24_LOS	7	**CURR_NE_24_LOS**	7
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	7	**CURR_NE_24_LOR**	8
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	9	**CURR_NE_24_ES**	10
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7

CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	636	**CURR_NE_24_UAS**	642
CURR_FE_24_UAS	632	**CURR_FE_24_UAS**	638
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3
CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Upstream (100m) Test 4			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	15	**CURR_24_FAST_INITS**	16
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	23	**CURR_24_FULL_INITS**	23
CURR_NE_24_LOS	7	**CURR_NE_24_LOS**	7
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	8	**CURR_NE_24_LOR**	9
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0

CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	10	**CURR_NE_24_ES**	10
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	666	**CURR_NE_24_UAS**	672
CURR_FE_24_UAS	662	**CURR_FE_24_UAS**	668
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3
CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Results - Upstream (100m) Test 5			
Results Before Disconnect		**Results After Disconnect**	
CURR_24_FAILEDFAST_INITS	0	**CURR_24_FAILEDFAST_INITS**	0
CURR_24_FAST_INITS	16	**CURR_24_FAST_INITS**	20
CURR_24_FAILEDFULL_INITS	0	**CURR_24_FAILEDFULL_INITS**	0
CURR_24_FULL_INITS	24	**CURR_24_FULL_INITS**	27
CURR_NE_24_LOS	7	**CURR_NE_24_LOS**	7
CURR_FE_24_LOS	0	**CURR_FE_24_LOS**	0
CURR_NE_24_LOR	9	**CURR_NE_24_LOR**	13
CURR_FE_24_LOR	0	**CURR_FE_24_LOR**	0
CURR_NE_24_LOM	0	**CURR_NE_24_LOM**	0
CURR_FE_24_LOM	0	**CURR_FE_24_LOM**	0
CURR_NE_24_ES	10	**CURR_NE_24_ES**	11
CURR_FE_24_ES	8	**CURR_FE_24_ES**	8
CURR_NE_24_SES	7	**CURR_NE_24_SES**	7
CURR_FE_24_SES	4	**CURR_FE_24_SES**	4
CURR_NE_24_UAS	696	**CURR_NE_24_UAS**	796
CURR_FE_24_UAS	692	**CURR_FE_24_UAS**	792
CURR_NE_24_LOSS	3	**CURR_NE_24_LOSS**	3
CURR_FE_24_LOSS	3	**CURR_FE_24_LOSS**	3

CURR_NE_24_LORS	4	**CURR_NE_24_LORS**	4
CURR_FE_24_LORS	4	**CURR_FE_24_LORS**	4

Summary		
The CURR_24_FAST_INITS counter increase SHALL be 1 (Downstream (100m) Test 1).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be >= 1 and <= 15 (Downstream (100m) Test 1).	Reported Value In Range: 7	pass
The CURR_24_FE_LOSS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FE_LORS counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FE_ES counter increase SHALL be <= 1 (Downstream (100m) Test 1).	Reported Value In Range: 0	pass
The CURR_24_FE_SES counter increase SHALL be 0 (Downstream (100m) Test 1).	Reported No Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 1 (Downstream (100m) Test 2).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be >= 1 and <= 15 (Downstream (100m) Test 2).	Reported Value In Range: 7	pass
The CURR_24_FE_LOSS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FE_LORS counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FE_ES counter increase SHALL be <= 1 (Downstream (100m) Test 2).	Reported Value In Range: 0	pass
The CURR_24_FE_SES counter increase SHALL be 0 (Downstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 1 (Downstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass

The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be >= 1 and <= 15 (Downstream (100m) Test 3).	Reported Value In Range: 7	pass
The CURR_24_FE_LOSS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FE_LORS counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FE_ES counter increase SHALL be <= 1 (Downstream (100m) Test 3).	Reported Value In Range: 0	pass
The CURR_24_FE_SES counter increase SHALL be 0 (Downstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 1 (Downstream (100m) Test 4).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be >= 1 and <= 15 (Downstream (100m) Test 4).	Reported Value In Range: 7	pass
The CURR_24_FE_LOSS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass

The CURR_24_FE_LORS counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FE_ES counter increase SHALL be <= 1 (Downstream (100m) Test 4).	Reported Value In Range: 0	pass
The CURR_24_FE_SES counter increase SHALL be 0 (Downstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 1 (Downstream (100m) Test 5).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FE_UAS counter increase SHALL be >= 1 and <= 15 (Downstream (100m) Test 5).	Reported Value In Range: 7	pass
The CURR_24_FE_LOSS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FE_LORS counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FE_ES counter increase SHALL be <= 1 (Downstream (100m) Test 5).	Reported Value In Range: 0	pass
The CURR_24_FE_SES counter increase SHALL be 0 (Downstream (100m) Test 5).	Reported No Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 1 (Upstream (100m) Test 1).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be >= 1 and <= 15 (Upstream (100m) Test 1).	Reported Value In Range: 7	pass
The CURR_24_NE_LOSS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_LORS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_ES counter increase SHALL be <= 1 (Upstream (100m) Test 1).	Reported Value In Range: 0	pass
The CURR_24_NE_SES counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_LOS counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass
The CURR_24_NE_LOR counter increase SHALL be 1 (Upstream (100m) Test 1).	Reported Value In Range: 1	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 1).	Reported No Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 1 (Upstream (100m) Test 2).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be >= 1 and <= 15 (Upstream (100m) Test 2).	Reported Value In Range: 7	pass
The CURR_24_NE_LOSS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_LORS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_ES counter increase SHALL be <= 1 (Upstream (100m) Test 2).	Reported Value In Range: 0	pass
The CURR_24_NE_SES counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_LOS counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass
The CURR_24_NE_LOR counter increase SHALL be 1 (Upstream (100m) Test 2).	Reported Value In Range: 1	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 2).	Reported No Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 1 (Upstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be >= 1 and <= 15 (Upstream (100m) Test 3).	Reported Value In Range: 6	pass
The CURR_24_NE_LOSS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_LORS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_ES counter increase SHALL be <= 1 (Upstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_NE_SES counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_LOS counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass
The CURR_24_NE_LOR counter increase SHALL be 1 (Upstream (100m) Test 3).	Reported Value In Range: 1	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 3).	Reported No Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 1 (Upstream (100m) Test 4).	Reported Value In Range: 1	pass
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be >= 1 and <= 15 (Upstream (100m) Test 4).	Reported Value In Range: 6	pass
The CURR_24_NE_LOSS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_LORS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_ES counter increase SHALL be <= 1 (Upstream (100m) Test 4).	Reported Value In Range: 0	pass
The CURR_24_NE_SES counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_LOS counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass
The CURR_24_NE_LOR counter increase SHALL be 1 (Upstream (100m) Test 4).	Reported Value In Range: 1	pass
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 4).	Reported No Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 1 (Upstream (100m) Test 5).	Reported Value Outside Range: 4	fail
The CURR_24_FAILEDFAST_INITS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported Increase: 3	fail
The CURR_24_FAILEDFULL_INITS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_UAS counter increase SHALL be >= 1 and <= 15 (Upstream (100m) Test 5).	Reported Value Outside Range: 100	fail
The CURR_24_NE_LOSS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_LORS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_ES counter increase SHALL be <= 1 (Upstream (100m) Test 5).	Reported Value In Range: 1	pass
The CURR_24_NE_SES counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_LOS counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass
The CURR_24_NE_LOR counter increase SHALL be 1 (Upstream (100m) Test 5).	Reported Value Outside Range: 4	fail
The CURR_24_NE_LOM counter increase SHALL be 0 (Upstream (100m) Test 5).	Reported No Increase: 0	pass

6.13 Dying Gasp Test	PARTS	RESULTS
		PASS

PURPOSE

If the DUT Is a CPE, then the purpose of this test Is to verify that the DUT supports sending the dying gasp to the DPU. If the DUT Is a DPU, then the purpose of this test Is to verify that the DUT supports receiving the dying gasp from the CPE And supports conveying the dying gasp over the DPU Northbound management protocol.

Results - Dying Gasp	
Results Before AC Mains Power Disconnect	
LOS-FE	False
LPR-FE	False
Results After AC Mains Power Disconnect	
LOS-FE	True
LPR-FE	True
Results After AC Mains Power Connect	
LOS-FE	False
LPR-FE	False
Results After Simultaneous Disconnection of Both Wires	
LOS-FE	True
LPR-FE	False
Results After Simultaneous Connection of Both Wires	
LOS-FE	False
LPR-FE	False

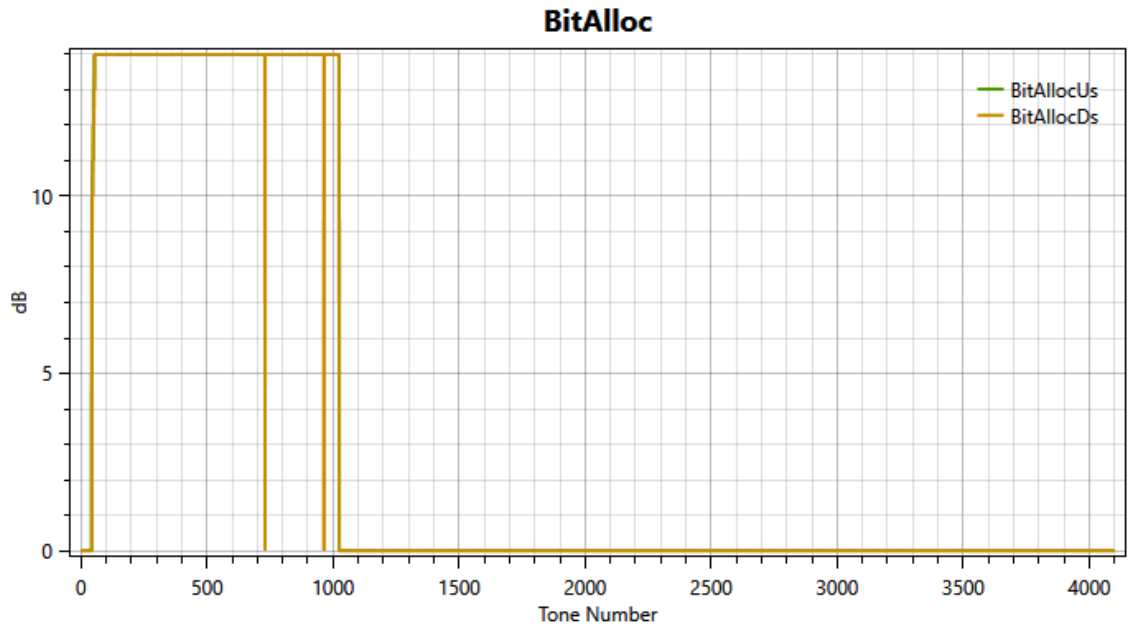
Summary		
Before disconnecting AC mains power: LOS = FALSE, LPR-FE = FALSE.	Reported Correct Status	pass
After disconnecting AC mains power: LOS = TRUE, LPR-FE = TRUE.	Reported Correct Status	pass
After reconnecting AC mains power: LOS = FALSE, LPR-FE = FALSE.	Reported Correct Status	pass
After disconnecting the loop: LOS = TRUE, LPR-FE = FALSE.	Reported Correct Status	pass
After reconnecting the loop: LOS = FALSE, LPR-FE = FALSE.	Reported Correct Status	pass

6.14 Increased Bit Loading Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT is capable to allocate an increased bit loading of up to 14 bits to a subcarrier.

Measurements			
BITSpsus	(Refer to Figure)	**BITSpsds**	(Refer to Figure)



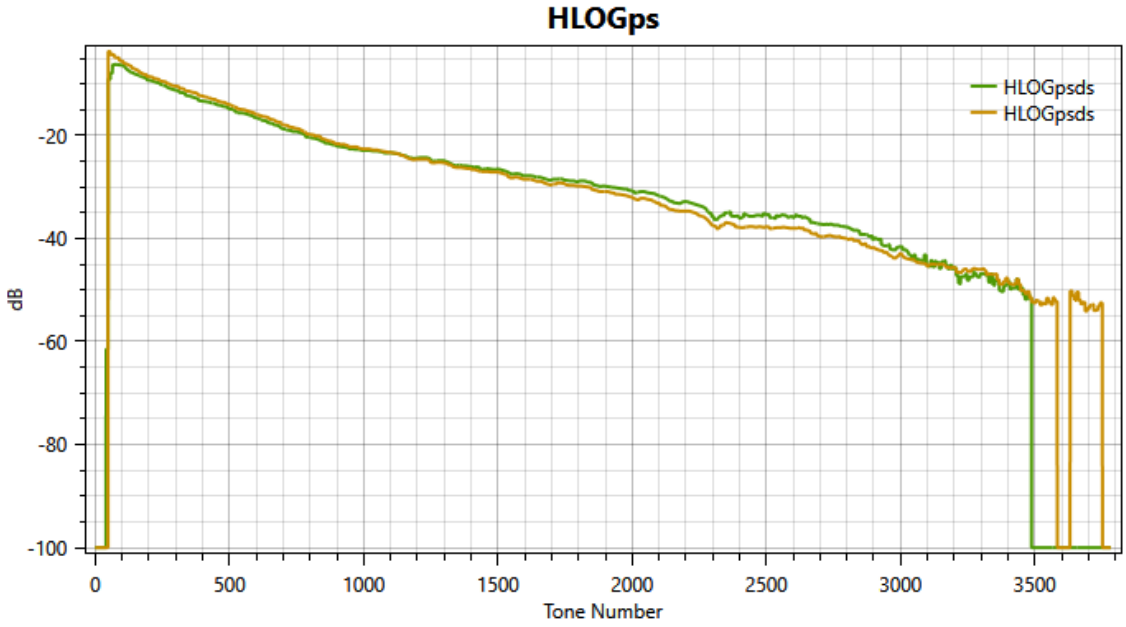
Summary		
For each subcarrier, the BITSpsds/us SHALL be from 0 to 14, inclusive.	Reported all BITSpsDs/Us to be from 0 to 14	pass
At least 1 subcarrier shall have BITSpsds/us equal to 14.	Reported BITSpsds/us to have a subcarrier equal to 14.	pass

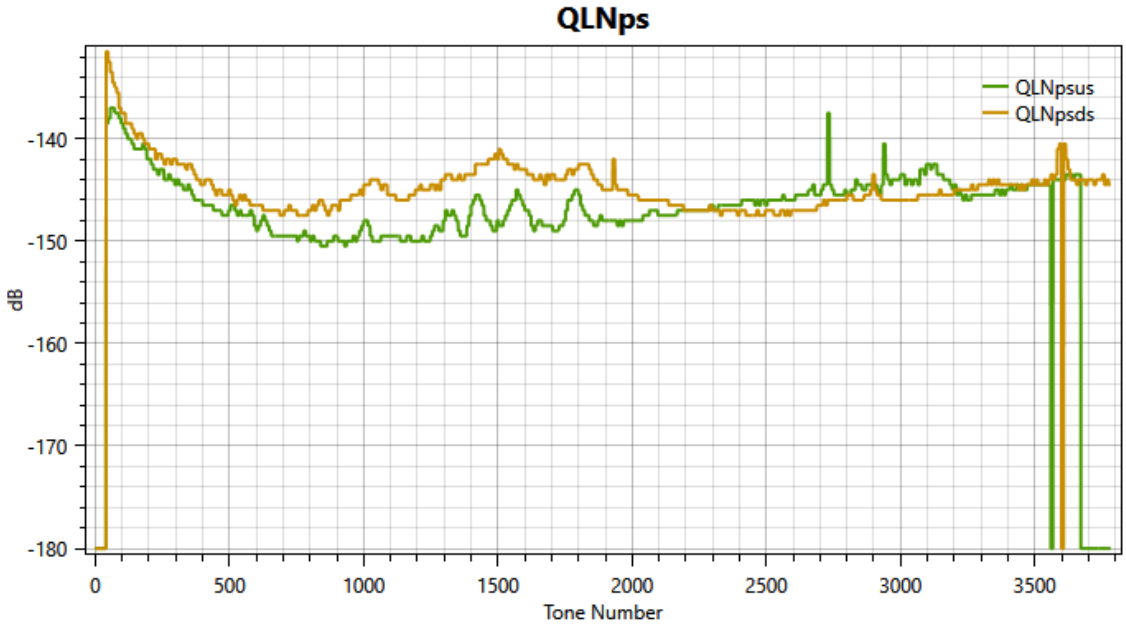
6.15 Test Parameters Test	PARTS	RESULTS
		PASS

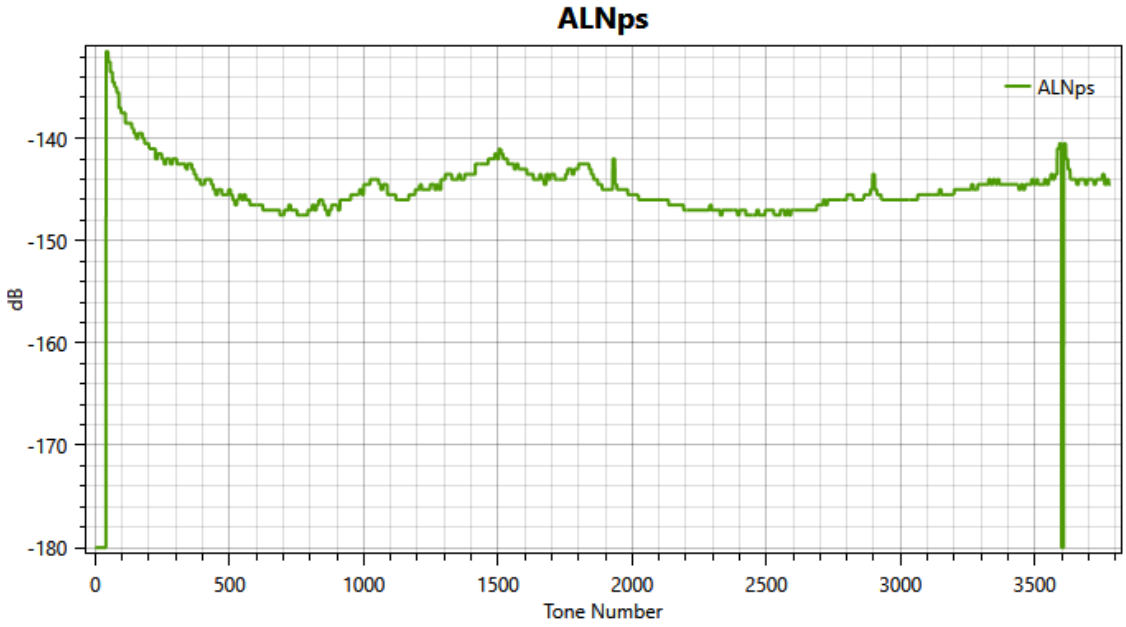
PURPOSE

The purpose of this test is to verify that the DUT supports measurement and reporting of the test parameters quiet line noise (QLN), active line noise (ALN), insertion loss (HLOG), and signal attenuation (SATN).

Insertion loss per sub-carrier group			
HLOGMTds	160	**HLOGMTus**	160
HLOGGds	8	**HLOGGus**	8
HLOGpsds	(Refer to Figure)	**HLOGpsus**	(Refer to Figure)
Quiet line noise per sub-carrier group			
QLNMTds	200	**QLNMTus**	200
QLNGds	8	**QLNGus**	8
QLNpsds	(Refer to Figure)	**QLNpsus**	(Refer to Figure)
Active line noise per sub-carrier group		**Signal attenuation**	
QLNMTds	256	**QLNMTus**	17
QLNGds	8	**QLNGus**	17.3
QLNpsds	(Refer to Figure)		


HLOGps
HLOGpsds
HLOGpsds
-20
-40
-60
-80
-100
dB
0
500
1000
1500
2000
2500
3000
3500
Tone Number


QLNps
QLNpsus
QLNpsds
-140
-150
-160
-170
-180
dB
0
500
1000
1500
2000
2500
3000
3500
Tone Number


ALNps
ALNps
-140
-150
-160
-170
-180
dB
0
500
1000
1500
2000
2500
3000
3500
Tone Number

Summary		
HLOGMTds, HLOGGds and HLOGpsds values SHALL comply within the valid values as specified in clauses 7.10.7.1, 7.10.7.2 and 7.10.7.3 of G.997.2 respectively.	HLOGMTds was within the valid values. HLOGGds was within the valid values. HLOGpsds was within the valid values.	pass
HLOGMTus, HLOGGus and HLOGpsus values SHALL comply within the valid values as specified in clauses 7.10.7.4, 7.10.7.5 and 7.10.7.6 of G.997.2 respectively.	HLOGMTus was within the valid values. HLOGGus was within the valid values. HLOGpsus was within the valid values.	pass
QLNMTds, QLNGds and QLNpsds values SHALL comply within the valid values as specified in clauses 7.10.9.1, 7.10.9.2 and 7.10.9.3 of G.997.2 respectively.	QLNMTds was within the valid values. QLNGds was within the valid values. QLNpsds was within the valid values.	pass
QLNMTus, QLNGus and QLNpsus values SHALL comply within the valid values as specified in clauses 7.10.9.4, 7.10.9.5 and 7.10.9.6 of G.997.2 respectively.	QLNMTus was within the valid values. QLNGus was within the valid values. QLNpsus was within the valid values.	pass
ALNMT, ALNG and ALNps values SHALL comply within the valid values as specified in clauses 7.10.10.1, 7.10.10.2 and 7.10.10.3 of G.997.2 respectively.	ALNMT was within the valid values. ALNG was within the valid values. ALNps was within the valid values.	pass
SATNds and SATNus values SHALL comply within the valid values as specified in clauses 7.10.15.1 and 7.10.15.2 of G.997.2 respectively.	SATNds was within the valid values. SATNus was within the valid values.	pass

7.1 SHINE Stability Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT is able to maintain physical layer and traffic stability under noise burst conditions.

Results - Downstream SHINE Type 1 (100m)		Results - Upstream SHINE Type 1 (100m)	
Total Transmitted Frames us	704550	**Total Transmitted Frames us**	582727
Total Transmitted Frames ds	2278670	**Total Transmitted Frames ds**	2891834
Total Received Frames us	704550	**Total Received Frames us**	582727
Total Received Frames ds	2278670	**Total Received Frames ds**	2891834
FLRds	0	**FLRds**	0
FLRus	0	**FLRus**	0
Results before SHINE noise			
CURR_24_FULL_INITS	1	**CURR_24_FULL_INITS**	2
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_CV	0	**CURR_NE_24_CV**	0
CURR_FE_24_CV	0	**CURR_FE_24_CV**	0
CURR_NE_24_ES	0	**CURR_NE_24_ES**	0
CURR_FE_24_ES	0	**CURR_FE_24_ES**	0
CURR_NE_24_SES	0	**CURR_NE_24_SES**	0
CURR_FE_24_SES	0	**CURR_FE_24_SES**	0
CURR_NE_24_UAS	24	**CURR_NE_24_UAS**	48
CURR_FE_24_UAS	24	**CURR_FE_24_UAS**	48
Results after SHINE noise			
CURR_24_FULL_INITS	1	**CURR_24_FULL_INITS**	2
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_CV	0	**CURR_NE_24_CV**	0

CURR_FE_24_CV	0	**CURR_FE_24_CV**	0
CURR_NE_24_ES	0	**CURR_NE_24_ES**	0
CURR_FE_24_ES	0	**CURR_FE_24_ES**	0
CURR_NE_24_SES	0	**CURR_NE_24_SES**	0
CURR_FE_24_SES	0	**CURR_FE_24_SES**	0
CURR_NE_24_UAS	24	**CURR_NE_24_UAS**	48
CURR_FE_24_UAS	24	**CURR_FE_24_UAS**	48

Results - Downstream SHINE Type 2 (100m)		**Results - Upstream SHINE Type 2 (100m)**	
Total Transmitted Frames us	1389402	**Total Transmitted Frames us**	1149569
Total Transmitted Frames ds	4496431	**Total Transmitted Frames ds**	5735482
Total Received Frames us	1387894	**Total Received Frames us**	1149310
Total Received Frames ds	4462329	**Total Received Frames ds**	5655271
FLRds	0.00758423736514582	**FLRds**	0.0139850495564279
FLRus	0.00108535902496182	**FLRus**	0.000225301830512131
Results before SHINE noise			
CURR_24_FULL_INITS	3	**CURR_24_FULL_INITS**	4
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_CV	0	**CURR_NE_24_CV**	0
CURR_FE_24_CV	0	**CURR_FE_24_CV**	0
CURR_NE_24_ES	0	**CURR_NE_24_ES**	12
CURR_FE_24_ES	0	**CURR_FE_24_ES**	12
CURR_NE_24_SES	0	**CURR_NE_24_SES**	12
CURR_FE_24_SES	0	**CURR_FE_24_SES**	12
CURR_NE_24_UAS	72	**CURR_NE_24_UAS**	96
CURR_FE_24_UAS	72	**CURR_FE_24_UAS**	96
Results after SHINE noise			
CURR_24_FULL_INITS	3	**CURR_24_FULL_INITS**	4
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0

CURR_NE_24_CV	0	**CURR_NE_24_CV**	0
CURR_FE_24_CV	0	**CURR_FE_24_CV**	0
CURR_NE_24_ES	12	**CURR_NE_24_ES**	24
CURR_FE_24_ES	12	**CURR_FE_24_ES**	24
CURR_NE_24_SES	12	**CURR_NE_24_SES**	24
CURR_FE_24_SES	12	**CURR_FE_24_SES**	24
CURR_NE_24_UAS	72	**CURR_NE_24_UAS**	96
CURR_FE_24_UAS	72	**CURR_FE_24_UAS**	96

Results - Downstream SHINE Type 3 (100m)		**Results - Upstream SHINE Type 3 (100m)**	
Total Transmitted Frames us	2759059	**Total Transmitted Frames us**	2302272
Total Transmitted Frames ds	8930062	**Total Transmitted Frames ds**	11334154
Total Received Frames us	2636169	**Total Received Frames us**	2227104
Total Received Frames ds	8512416	**Total Received Frames ds**	10896731
FLRds	0.046768544272145	**FLRds**	0.038593352446067
FLRus	0.0445405480636695	**FLRus**	0.0326494871153365
Results before SHINE noise			
CURR_24_FULL_INITS	5	**CURR_24_FULL_INITS**	6
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_CV	0	**CURR_NE_24_CV**	0
CURR_FE_24_CV	0	**CURR_FE_24_CV**	0
CURR_NE_24_ES	24	**CURR_NE_24_ES**	48
CURR_FE_24_ES	24	**CURR_FE_24_ES**	47
CURR_NE_24_SES	24	**CURR_NE_24_SES**	47
CURR_FE_24_SES	24	**CURR_FE_24_SES**	47
CURR_NE_24_UAS	120	**CURR_NE_24_UAS**	144
CURR_FE_24_UAS	120	**CURR_FE_24_UAS**	144
Results after SHINE noise			

CURR_24_FULL_INITS	5	**CURR_24_FULL_INITS**	6
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_CV	0	**CURR_NE_24_CV**	0
CURR_FE_24_CV	0	**CURR_FE_24_CV**	62
CURR_NE_24_ES	48	**CURR_NE_24_ES**	72
CURR_FE_24_ES	47	**CURR_FE_24_ES**	71
CURR_NE_24_SES	47	**CURR_NE_24_SES**	71
CURR_FE_24_SES	47	**CURR_FE_24_SES**	67
CURR_NE_24_UAS	120	**CURR_NE_24_UAS**	144
CURR_FE_24_UAS	120	**CURR_FE_24_UAS**	144

Summary		
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream SHINE Type 1 (100m) Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Downstream SHINE Type 1 (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_CV counter increase SHALL be 0 (Downstream SHINE Type 1 (100m) Test).	Reported No CV Increase: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be 0 (Downstream SHINE Type 1 (100m) Test).	Reported No ES Increase: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be 0 (Downstream SHINE Type 1 (100m) Test).	Reported No SES Increase: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Downstream SHINE Type 1 (100m) Test).	Reported No UAS Increase: 0	pass

For SHINE Type 1, CURR_NE/FE_24_FRA counter increase SHALL be 0. (Downstream SHINE Type 1 (100m) Test).	Reported No FRA Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Downstream SHINE Type 1 (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream SHINE Type 1 (100m) Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Upstream SHINE Type 1 (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_CV counter increase SHALL be 0 (Upstream SHINE Type 1 (100m) Test).	Reported No CV Increase: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be 0 (Upstream SHINE Type 1 (100m) Test).	Reported No ES Increase: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be 0 (Upstream SHINE Type 1 (100m) Test).	Reported No SES Increase: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Upstream SHINE Type 1 (100m) Test).	Reported No UAS Increase: 0	pass
For SHINE Type 1, CURR_NE/FE_24_FRA counter increase SHALL be 0. (Upstream SHINE Type 1 (100m) Test).	Reported No FRA Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Upstream SHINE Type 1 (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 0.000e+000 |Dropped Frames: 0	pass

The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream SHINE Type 2 (100m) Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Downstream SHINE Type 2 (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be <= 24. (Downstream SHINE Type 2 (100m) Test).	Reported ES Value In Range: 12	pass
The CURR_NE/FE_24_SES counter increase SHALL be <= 24. (Downstream SHINE Type 2 (100m) Test).	Reported SES Value In Range: 12	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Downstream SHINE Type 2 (100m) Test).	Reported No UAS Increase: 0	pass
The FLRds/us SHALL not exceed 1.5%. (Downstream SHINE Type 2 (100m) Test).	Reported FLR In Range: 0.00758423736514582	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream SHINE Type 2 (100m) Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Upstream SHINE Type 2 (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be <= 24. (Upstream SHINE Type 2 (100m) Test).	Reported ES Value In Range: 12	pass
The CURR_NE/FE_24_SES counter increase SHALL be <= 24. (Upstream SHINE Type 2 (100m) Test).	Reported SES Value In Range: 12	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Upstream SHINE Type 2 (100m) Test).	Reported No UAS Increase: 0	pass

The FLRds/us SHALL not exceed 1.5%. (Upstream SHINE Type 2 (100m) Test).	Reported FLR In Range: 0.000225301830512131	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream SHINE Type 3 (100m) Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Downstream SHINE Type 3 (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be <= 24. (Downstream SHINE Type 3 (100m) Test).	Reported ES Value In Range: 23	pass
The CURR_NE/FE_24_SES counter increase SHALL be <= 24. (Downstream SHINE Type 3 (100m) Test).	Reported SES Value In Range: 23	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Downstream SHINE Type 3 (100m) Test).	Reported No UAS Increase: 0	pass
The FLRds/us SHALL not exceed 5.5%. (Downstream SHINE Type 3 (100m) Test).	Reported FLR In Range: 0.046768544272145	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream SHINE Type 3 (100m) Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Upstream SHINE Type 3 (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be <= 24. (Upstream SHINE Type 3 (100m) Test).	Reported ES Value In Range: 24	pass
The CURR_NE/FE_24_SES counter increase SHALL be <= 24. (Upstream SHINE Type 3 (100m) Test).	Reported SES Value In Range: 24	pass

The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Upstream SHINE Type 3 (100m) Test).	Reported No UAS Increase: 0	pass
The FLRds/us SHALL not exceed 5.5%. (Upstream SHINE Type 3 (100m) Test).	Reported FLR In Range: 0.0326494871153365	pass

7.2 REIN Stability Test	PARTS	RESULTS
		PASS

PURPOSE

This test assesses DUT physical layer and traffic stability under repetitive noise burst conditions (REIN). Because retransmission is a mandatory capability for a G.fast transceiver, this impulse noise protection mechanism is tested. The REIN noise is injected after the link has trained. After a stabilization period the error metrics SHALL be recorded for the measurement period.

Results - Downstream REIN Type 1 (100m)		Results - Upstream REIN Type 1 (100m)	
Total Transmitted Frames us	1271025	**Total Transmitted Frames us**	1059382
Total Transmitted Frames ds	4120009	**Total Transmitted Frames ds**	5264244
Total Received Frames us	1271025	**Total Received Frames us**	1059382
Total Received Frames ds	4120009	**Total Received Frames ds**	5264244
FLRds	0	**FLRds**	0
FLRus	0	**FLRus**	0
Results before REIN noise			
CURR_24_FULL_INITS	6	**CURR_24_FULL_INITS**	7
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_CV	0	**CURR_NE_24_CV**	0
CURR_FE_24_CV	0	**CURR_FE_24_CV**	0
CURR_NE_24_ES	48	**CURR_NE_24_ES**	48
CURR_FE_24_ES	47	**CURR_FE_24_ES**	47
CURR_NE_24_SES	47	**CURR_NE_24_SES**	47
CURR_FE_24_SES	47	**CURR_FE_24_SES**	47
CURR_NE_24_UAS	144	**CURR_NE_24_UAS**	168
CURR_FE_24_UAS	144	**CURR_FE_24_UAS**	168
Results after REIN noise			
CURR_24_FULL_INITS	6	**CURR_24_FULL_INITS**	7

CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_CV	0	**CURR_NE_24_CV**	0
CURR_FE_24_CV	0	**CURR_FE_24_CV**	0
CURR_NE_24_ES	48	**CURR_NE_24_ES**	48
CURR_FE_24_ES	47	**CURR_FE_24_ES**	47
CURR_NE_24_SES	47	**CURR_NE_24_SES**	47
CURR_FE_24_SES	47	**CURR_FE_24_SES**	47
CURR_NE_24_UAS	144	**CURR_NE_24_UAS**	168
CURR_FE_24_UAS	144	**CURR_FE_24_UAS**	168

Results - Downstream REIN Type 2 (100m)		**Results - Upstream REIN Type 2 (100m)**	
Total Transmitted Frames us	1285513	**Total Transmitted Frames us**	1063983
Total Transmitted Frames ds	4121469	**Total Transmitted Frames ds**	5257392
Total Received Frames us	1285513	**Total Received Frames us**	1063983
Total Received Frames ds	4121469	**Total Received Frames ds**	5257392
FLRds	0	**FLRds**	0
FLRus	0	**FLRus**	0
Results before REIN noise			
CURR_24_FULL_INITS	8	**CURR_24_FULL_INITS**	9
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_CV	0	**CURR_NE_24_CV**	0
CURR_FE_24_CV	0	**CURR_FE_24_CV**	0
CURR_NE_24_ES	48	**CURR_NE_24_ES**	48
CURR_FE_24_ES	47	**CURR_FE_24_ES**	47
CURR_NE_24_SES	47	**CURR_NE_24_SES**	47
CURR_FE_24_SES	47	**CURR_FE_24_SES**	47
CURR_NE_24_UAS	192	**CURR_NE_24_UAS**	216

CURR_FE_24_UAS	192	**CURR_FE_24_UAS**	216
Results after REIN noise			
CURR_24_FULL_INITS	8	**CURR_24_FULL_INITS**	9
CURR_24_FAST_INITS	0	**CURR_24_FAST_INITS**	0
CURR_NE_24_CV	0	**CURR_NE_24_CV**	0
CURR_FE_24_CV	0	**CURR_FE_24_CV**	0
CURR_NE_24_ES	48	**CURR_NE_24_ES**	48
CURR_FE_24_ES	47	**CURR_FE_24_ES**	47
CURR_NE_24_SES	47	**CURR_NE_24_SES**	47
CURR_FE_24_SES	47	**CURR_FE_24_SES**	47
CURR_NE_24_UAS	192	**CURR_NE_24_UAS**	216
CURR_FE_24_UAS	192	**CURR_FE_24_UAS**	216

Summary		
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream REIN Type 1 (100m) Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Downstream REIN Type 1 (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_CV counter increase SHALL be 0 (Downstream REIN Type 1 (100m) Test).	Reported No CV Increase: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be 0 (Downstream REIN Type 1 (100m) Test).	Reported No ES Increase: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be 0 (Downstream REIN Type 1 (100m) Test).	Reported No SES Increase: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Downstream REIN Type 1 (100m) Test).	Reported No UAS Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Downstream REIN Type 1 (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream REIN Type 1 (100m) Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Upstream REIN Type 1 (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_CV counter increase SHALL be 0 (Upstream REIN Type 1 (100m) Test).	Reported No CV Increase: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be 0 (Upstream REIN Type 1 (100m) Test).	Reported No ES Increase: 0	pass

The CURR_NE/FE_24_SES counter increase SHALL be 0 (Upstream REIN Type 1 (100m) Test).	Reported No SES Increase: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Upstream REIN Type 1 (100m) Test).	Reported No UAS Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Upstream REIN Type 1 (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream REIN Type 2 (100m) Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Downstream REIN Type 2 (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_CV counter increase SHALL be 0 (Downstream REIN Type 2 (100m) Test).	Reported No CV Increase: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be 0 (Downstream REIN Type 2 (100m) Test).	Reported No ES Increase: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be 0 (Downstream REIN Type 2 (100m) Test).	Reported No SES Increase: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Downstream REIN Type 2 (100m) Test).	Reported No UAS Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Downstream REIN Type 2 (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 0.000e+000 |Dropped Frames: 0	pass

The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream REIN Type 2 (100m) Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Upstream REIN Type 2 (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_CV counter increase SHALL be 0 (Upstream REIN Type 2 (100m) Test).	Reported No CV Increase: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be 0 (Upstream REIN Type 2 (100m) Test).	Reported No ES Increase: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be 0 (Upstream REIN Type 2 (100m) Test).	Reported No SES Increase: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Upstream REIN Type 2 (100m) Test).	Reported No UAS Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Upstream REIN Type 2 (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 0.000e+000 |Dropped Frames: 0	pass

7.3 Fluctuating Broadband RFI Noise Present at Initialization Test	PARTS	RESULTS
		PASS

PURPOSE

This test verifies the stability of the G.fast link in the downstream direction, when RFI noise is injected at the DUT (CPE) while the FTU-O and FTU-R initialize and after they reach the Showtime state.

Results - Downstream (100m)		Results - Upstream (100m)	
Total Transmitted Frames us	908446	**Total Transmitted Frames us**	906767
Total Transmitted Frames ds	3724054	**Total Transmitted Frames ds**	3716571
Total Received Frames us	908446	**Total Received Frames us**	906767
Total Received Frames ds	3724054	**Total Received Frames ds**	3716571
FLRds	0	**FLRds**	0
FLRus	0	**FLRus**	0
Results Before Steady State Period			
CURR_24_FULL_INITS	28	**CURR_24_FULL_INITS**	29
CURR_24_FAST_INITS	13	**CURR_24_FAST_INITS**	13
CURR_NE_24_CV	5	**CURR_NE_24_CV**	5
CURR_FE_24_CV	50	**CURR_FE_24_CV**	50
CURR_NE_24_ES	41	**CURR_NE_24_ES**	41
CURR_FE_24_ES	39	**CURR_FE_24_ES**	39
CURR_NE_24_SES	38	**CURR_NE_24_SES**	38
CURR_FE_24_SES	32	**CURR_FE_24_SES**	32
CURR_NE_24_UAS	779	**CURR_NE_24_UAS**	803
CURR_FE_24_UAS	776	**CURR_FE_24_UAS**	800
Results After Steady State Period			
CURR_24_FULL_INITS	28	**CURR_24_FULL_INITS**	29
CURR_24_FAST_INITS	13	**CURR_24_FAST_INITS**	13

CURR_NE_24_CV	5	**CURR_NE_24_CV**	5
CURR_FE_24_CV	50	**CURR_FE_24_CV**	50
CURR_NE_24_ES	41	**CURR_NE_24_ES**	41
CURR_FE_24_ES	39	**CURR_FE_24_ES**	39
CURR_NE_24_SES	38	**CURR_NE_24_SES**	38
CURR_FE_24_SES	32	**CURR_FE_24_SES**	32
CURR_NE_24_UAS	779	**CURR_NE_24_UAS**	803
CURR_FE_24_UAS	776	**CURR_FE_24_UAS**	800

Summary		
The CURR_NE/FE_24_CV counter increase SHALL be <= 1 (Downstream (100m) Test).	Reported CV Value In Range: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be <= 1. (Downstream (100m) Test).	Reported ES Value In Range: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be 0 (Downstream (100m) Test).	Reported No SES Increase: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0. (Downstream (100m) Test).	Reported No UAS Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0. (Downstream (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test).	Reported No Full Inits Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Downstream (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 0.000e+000 |Dropped Frames: 0	pass

The CURR_NE/FE_24_CV counter increase SHALL be <= 1 (Upstream (100m) Test).	Reported CV Value In Range: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be <= 1. (Upstream (100m) Test).	Reported ES Value In Range: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be 0 (Upstream (100m) Test).	Reported No SES Increase: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0. (Upstream (100m) Test).	Reported No UAS Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0. (Upstream (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test).	Reported No Full Inits Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Upstream (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 0.000e+000 |Dropped Frames: 0	pass

7.4 Stationary Broadband RFI Noise Present at Initialization Test	PARTS	RESULTS
		PASS

PURPOSE

This test verifies the stability of the G.fast link in the downstream and upstream direction, when RFI noise is injected at the CPE while the FTU-O and FTU-R initialize and after they reach the Showtime state.

Results - Downstream (100m)		Results - Upstream (100m)	
Total Transmitted Frames us	903623	**Total Transmitted Frames us**	906725
Total Transmitted Frames ds	3710887	**Total Transmitted Frames ds**	3717960
Total Received Frames us	903623	**Total Received Frames us**	906725
Total Received Frames ds	3710887	**Total Received Frames ds**	3717960
FLRds	0	**FLRds**	0
FLRus	0	**FLRus**	0
Results Before Steady State Period			
CURR_24_FULL_INITS	28	**CURR_24_FULL_INITS**	29
CURR_24_FAST_INITS	13	**CURR_24_FAST_INITS**	13
CURR_NE_24_CV	5	**CURR_NE_24_CV**	5
CURR_FE_24_CV	50	**CURR_FE_24_CV**	50
CURR_NE_24_ES	41	**CURR_NE_24_ES**	41
CURR_FE_24_ES	39	**CURR_FE_24_ES**	39
CURR_NE_24_SES	38	**CURR_NE_24_SES**	38
CURR_FE_24_SES	32	**CURR_FE_24_SES**	32
CURR_NE_24_UAS	779	**CURR_NE_24_UAS**	803
CURR_FE_24_UAS	776	**CURR_FE_24_UAS**	800
Results After Steady State Period			
CURR_24_FULL_INITS	28	**CURR_24_FULL_INITS**	29
CURR_24_FAST_INITS	13	**CURR_24_FAST_INITS**	13

CURR_NE_24_CV	5	**CURR_NE_24_CV**	5
CURR_FE_24_CV	50	**CURR_FE_24_CV**	50
CURR_NE_24_ES	41	**CURR_NE_24_ES**	41
CURR_FE_24_ES	39	**CURR_FE_24_ES**	39
CURR_NE_24_SES	38	**CURR_NE_24_SES**	38
CURR_FE_24_SES	32	**CURR_FE_24_SES**	32
CURR_NE_24_UAS	779	**CURR_NE_24_UAS**	803
CURR_FE_24_UAS	776	**CURR_FE_24_UAS**	800

Summary		
The CURR_NE/FE_24_CV counter increase SHALL be <= 1 (Downstream (100m) Test).	Reported CV Value In Range: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be <= 1. (Downstream (100m) Test).	Reported ES Value In Range: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be 0 (Downstream (100m) Test).	Reported No SES Increase: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0. (Downstream (100m) Test).	Reported No UAS Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0. (Downstream (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test).	Reported No Full Inits Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Downstream (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 0.000e+000 |Dropped Frames: 0	pass

The CURR_NE/FE_24_CV counter increase SHALL be <= 1 (Upstream (100m) Test).	Reported CV Value In Range: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be <= 1. (Upstream (100m) Test).	Reported ES Value In Range: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be 0 (Upstream (100m) Test).	Reported No SES Increase: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0. (Upstream (100m) Test).	Reported No UAS Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0. (Upstream (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test).	Reported No Full Inits Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Upstream (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 0.000e+000 |Dropped Frames: 0	pass

7.5 Fluctuating Broadband RFI Noise Present at Showtime Test	PARTS	RESULTS
		PASS

PURPOSE

This test verifies the stability of the G.fast link in the downstream direction, when RFI noise is injected at the DUT (CPE) after the FTU-O and FTU-R reach the Showtime state.

Results - Downstream (100m)		Results - Upstream (100m)	
Total Transmitted Frames us	908394	**Total Transmitted Frames us**	905376
Total Transmitted Frames ds	3728937	**Total Transmitted Frames ds**	3720310
Total Received Frames us	908394	**Total Received Frames us**	905376
Total Received Frames ds	3728937	**Total Received Frames ds**	3720310
FLRds	0	**FLRds**	0
FLRus	0	**FLRus**	0
Results Before Noise			
CURR_24_FULL_INITS	29	**CURR_24_FULL_INITS**	30
CURR_24_FAST_INITS	13	**CURR_24_FAST_INITS**	13
CURR_NE_24_CV	5	**CURR_NE_24_CV**	7
CURR_FE_24_CV	50	**CURR_FE_24_CV**	50
CURR_NE_24_ES	41	**CURR_NE_24_ES**	42
CURR_FE_24_ES	39	**CURR_FE_24_ES**	39
CURR_NE_24_SES		**CURR_NE_24_SES**	
CURR_FE_24_SES	32	**CURR_FE_24_SES**	32
CURR_NE_24_UAS	803	**CURR_NE_24_UAS**	827
CURR_FE_24_UAS	800	**CURR_FE_24_UAS**	824
Results After Noise			
CURR_24_FULL_INITS	29	**CURR_24_FULL_INITS**	30
CURR_24_FAST_INITS	13	**CURR_24_FAST_INITS**	13
CURR_NE_24_CV	5	**CURR_NE_24_CV**	7

CURR_FE_24_CV	50	**CURR_FE_24_CV**	50
CURR_NE_24_ES	41	**CURR_NE_24_ES**	42
CURR_FE_24_ES	39	**CURR_FE_24_ES**	39
CURR_NE_24_SES	38	**CURR_NE_24_SES**	38
CURR_FE_24_SES	32	**CURR_FE_24_SES**	32
CURR_NE_24_UAS	803	**CURR_NE_24_UAS**	827
CURR_FE_24_UAS	800	**CURR_FE_24_UAS**	824

Summary		
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Downstream (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_CV counter increase SHALL be <= 1 (Downstream (100m) Test).	Reported CV Value In Range: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be <= 1 (Downstream (100m) Test).	Reported ES Value In Range: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be <=2 (Downstream (100m) Test).	Reported SES Value In Range: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Downstream (100m) Test).	Reported No UAS Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Downstream (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test).	Reported No Full Inits Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 0 (Upstream (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_CV counter increase SHALL be <= 1 (Upstream (100m) Test).	Reported CV Value In Range: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be <= 1 (Upstream (100m) Test).	Reported ES Value In Range: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be <=2 (Upstream (100m) Test).	Reported SES Value In Range: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Upstream (100m) Test).	Reported No UAS Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Upstream (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 0.000e+000 |Dropped Frames: 0	pass

7.6 Stationary Broadband RFI Noise Present at Showtime Test	PARTS	RESULTS
		PASS

PURPOSE

This test verifies the stability of the G.fast link in the downstream direction, when RFI noise is injected at the DUT (CPE) after the FTU-O and FTU-R reach the Showtime state.

Results - Downstream (100m)		Results - Upstream (100m)	
Total Transmitted Frames us	902127	Total Transmitted Frames us	903729
Total Transmitted Frames ds	3706265	Total Transmitted Frames ds	3708173
Total Received Frames us	902127	Total Received Frames us	903728
Total Received Frames ds	3706265	Total Received Frames ds	3708163
FLRds	0	FLRds	2.69674580986378E-06
FLRus	0	FLRus	1.10652640337977E-06
Results Before Noise			
CURR_24_FULL_INITS	31	CURR_24_FULL_INITS	32
CURR_24_FAST_INITS	13	CURR_24_FAST_INITS	13
CURR_NE_24_CV	7	CURR_NE_24_CV	8
CURR_FE_24_CV	50	CURR_FE_24_CV	52
CURR_NE_24_ES	42	CURR_NE_24_ES	43
CURR_FE_24_ES	39	CURR_FE_24_ES	40
CURR_NE_24_SES		CURR_NE_24_SES	
CURR_FE_24_SES	32	CURR_FE_24_SES	32
CURR_NE_24_UAS	851	CURR_NE_24_UAS	875
CURR_FE_24_UAS	848	CURR_FE_24_UAS	872
Results After Noise			
CURR_24_FULL_INITS	31	CURR_24_FULL_INITS	32
CURR_24_FAST_INITS	13	CURR_24_FAST_INITS	13
CURR_NE_24_CV	7	CURR_NE_24_CV	8
CURR_FE_24_CV	50	CURR_FE_24_CV	52

CURR_NE_24_ES	42	**CURR_NE_24_ES**	43
CURR_FE_24_ES	39	**CURR_FE_24_ES**	40
CURR_NE_24_SES	38	**CURR_NE_24_SES**	38
CURR_FE_24_SES	32	**CURR_FE_24_SES**	32
CURR_NE_24_UAS	851	**CURR_NE_24_UAS**	875
CURR_FE_24_UAS	848	**CURR_FE_24_UAS**	872

Summary		
The CURR_24_FULL_INITS counter increase SHALL be 0 (Downstream (100m) Test).	Reported No Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Downstream (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_CV counter increase SHALL be <= 1 (Downstream (100m) Test).	Reported CV Value In Range: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be <= 1 (Downstream (100m) Test).	Reported ES Value In Range: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be <=2 (Downstream (100m) Test).	Reported SES Value In Range: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Downstream (100m) Test).	Reported No UAS Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Downstream (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Upstream (100m) Test).	Reported No Full Inits Increase: 0	pass

The CURR_24_FAST_INITS counter increase SHALL be 0 (Upstream (100m) Test).	Reported No Fast Inits Increase: 0	pass
The CURR_NE/FE_24_CV counter increase SHALL be <= 1 (Upstream (100m) Test).	Reported CV Value In Range: 0	pass
The CURR_NE/FE_24_ES counter increase SHALL be <= 1 (Upstream (100m) Test).	Reported ES Value In Range: 0	pass
The CURR_NE/FE_24_SES counter increase SHALL be <=2 (Upstream (100m) Test).	Reported SES Value In Range: 0	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (Upstream (100m) Test).	Reported No UAS Increase: 0	pass
The number of dropped ds/us (i.e., transmitted minus received) frames SHALL not exceed 5, or the FLRds/us SHALL not exceed the background FLR (Upstream (100m) Test).	Measured FLR / Dropped Frames Out of Range: FLR: 1.107e-006 |Dropped Frames: 1	pass

8.1.1 Single-line Basic Throughput Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT can establish a stable link of sufficient performance and stability to pass data traffic at the required data rates with the Link Partner.

Results - with Loop Set to 20	
NDRds (kbit/s)	1499012
NDRus (kbit/s)	390895
GDRds	1499012
GDRus	390895
SNRMds	6.9
SNRMus	5.9
Results - with Loop Set to 20 After Traffic	
Frames Transmitted ds	27338494
Frames Transmitted us	10694464
Frames Received ds	27338494
Frames Received us	10694463
FLRds	0
FLRus	9.35063225235037E-08

Results - with Loop Set to 50	
NDRds (kbit/s)	1447959
NDRus (kbit/s)	347885
GDRds	1447959
GDRus	347885
SNRMds	6.1
SNRMus	6
Results - with Loop Set to 50 After Traffic	
Frames Transmitted ds	27338494
Frames Transmitted us	9518338
Frames Received ds	27338494
Frames Received us	9518338
FLRds	0
FLRus	0

Results - with Loop Set to 100	
NDRds (kbit/s)	1070924
NDRus (kbit/s)	263893
GDRds	1070924
GDRus	263893
SNRMds	6.4
SNRMus	6.3
Results - with Loop Set to 100 After Traffic	
Frames Transmitted ds	27338494
Frames Transmitted us	7220945
Frames Received ds	27338494
Frames Received us	7220945

FLRds	0
FLRus	0

Results - with Loop Set to 200	
NDRds (kbit/s)	546043
NDRus (kbit/s)	148989
GDRds	546043
GDRus	148989
SNRMds	5.8
SNRMus	6.3
Results - with Loop Set to 200 After Traffic	
Frames Transmitted ds	14936182
Frames Transmitted us	4077392
Frames Received ds	14936182
Frames Received us	4077392
FLRds	0
FLRus	0

Results - with Loop Set to 300	
NDRds (kbit/s)	281428
NDRus (kbit/s)	87963
GDRds	281428
GDRus	87963
SNRMds	5.7
SNRMus	6.4

Results - with Loop Set to 300 After Traffic	
Frames Transmitted ds	7700587
Frames Transmitted us	2407485
Frames Received ds	7700587
Frames Received us	2407485
FLRds	0
FLRus	0

Results - with Loop Set to 400	
NDRds (kbit/s)	225136
NDRus (kbit/s)	59039
GDRds	225136
GDRus	59039
SNRMds	5.8
SNRMus	6.4
Results - with Loop Set to 400 After Traffic	
Frames Transmitted ds	6160744
Frames Transmitted us	1615933
Frames Received ds	6160744
Frames Received us	1615933
FLRds	0
FLRus	0

Summary		
The aggregate net data rate SHALL be >= 1450000kbps (20m Test).	Reported NDR Value In Range: DS: 1499012 US: 390895	pass
The GDRus SHALL be >= 0.99 x NDRus(20m Test).	Reported GDR Value In Range: 390895	pass
The GDRds SHALL be >= 0.99 x NDRds (20m Test).	Reported GDR Value In Range: 1499012	pass
The SNRMus SHALL be above the configured SRA downshift margin (20m Test).	SNRM In Bounds: 5.9	pass
The SNRMds SHALL be above the configured SRA downshift margin (20m Test).	SNRM In Bounds: 6.9	pass
The number of dropped upstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRus SHALL not exceed the background FLR (20m Test).	Measured FLR Value Outside Range: FLR: 9.351e-008 |Dropped Frames: 1	pass
The number of dropped downstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRds SHALL not exceed the background FLR (20m Test).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The aggregate net data rate SHALL be >= 1200000kbps (50m Test).	Reported NDR Value In Range: DS: 1447959 US: 347885	pass
The GDRus SHALL be >= 0.99 x NDRus(50m Test).	Reported GDR Value In Range: 347885	pass
The GDRds SHALL be >= 0.99 x NDRds (50m Test).	Reported GDR Value In Range: 1447959	pass
The SNRMus SHALL be above the configured SRA downshift margin (50m Test).	SNRM In Bounds: 6	pass
The SNRMds SHALL be above the configured SRA downshift margin (50m Test).	SNRM In Bounds: 6.1	pass
The number of dropped upstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRus SHALL not exceed the background FLR (50m Test).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass

The number of dropped downstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRds SHALL not exceed the background FLR (50m Test).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The aggregate net data rate SHALL be >= 950000kbps (100m Test).	Reported NDR Value In Range: DS: 1070924 US: 263893	pass
The GDRus SHALL be >= 0.99 x NDRus(100m Test).	Reported GDR Value In Range: 263893	pass
The GDRds SHALL be >= 0.99 x NDRds (100m Test).	Reported GDR Value In Range: 1070924	pass
The SNRMus SHALL be above the configured SRA downshift margin (100m Test).	SNRM In Bounds: 6.3	pass
The SNRMds SHALL be above the configured SRA downshift margin (100m Test).	SNRM In Bounds: 6.4	pass
The number of dropped upstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRus SHALL not exceed the background FLR (100m Test).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The number of dropped downstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRds SHALL not exceed the background FLR (100m Test).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The aggregate net data rate SHALL be >= 325000kbps (200m Test).	Reported NDR Value In Range: DS: 546043 US: 148989	pass
The GDRus SHALL be >= 0.99 x NDRus(200m Test).	Reported GDR Value In Range: 148989	pass
The GDRds SHALL be >= 0.99 x NDRds (200m Test).	Reported GDR Value In Range: 546043	pass
The SNRMus SHALL be above the configured SRA downshift margin (200m Test).	SNRM In Bounds: 6.3	pass
The SNRMds SHALL be above the configured SRA downshift margin (200m Test).	SNRM In Bounds: 5.8	pass

The number of dropped upstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRus SHALL not exceed the background FLR (200m Test).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The number of dropped downstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRds SHALL not exceed the background FLR (200m Test).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The aggregate net data rate SHALL be >= 200000kbps (300m Test).	Reported NDR Value In Range: DS: 281428 US: 87963	pass
The GDRus SHALL be >= 0.99 x NDRus(300m Test).	Reported GDR Value In Range: 87963	pass
The GDRds SHALL be >= 0.99 x NDRds (300m Test).	Reported GDR Value In Range: 281428	pass
The SNRMus SHALL be above the configured SRA downshift margin (300m Test).	SNRM In Bounds: 6.4	pass
The SNRMds SHALL be above the configured SRA downshift margin (300m Test).	SNRM In Bounds: 5.7	pass
The number of dropped upstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRus SHALL not exceed the background FLR (300m Test).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The number of dropped downstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRds SHALL not exceed the background FLR (300m Test).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The aggregate net data rate SHALL be >= 125000kbps (400m Test).	Reported NDR Value In Range: DS: 225136 US: 59039	pass
The GDRus SHALL be >= 0.99 x NDRus(400m Test).	Reported GDR Value In Range: 59039	pass
The GDRds SHALL be >= 0.99 x NDRds (400m Test).	Reported GDR Value In Range: 225136	pass
The SNRMus SHALL be above the configured SRA downshift margin (400m Test).	SNRM In Bounds: 6.4	pass

The SNRMds SHALL be above the configured SRA downshift margin (400m Test).	SNRM In Bounds: 5.8	pass
The number of dropped upstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRus SHALL not exceed the background FLR (400m Test).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The number of dropped downstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRds SHALL not exceed the background FLR (400m Test).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass

8.1.1 Single-line Basic Throughput Test (HNBT Loop)	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT can establish a stable link of sufficient performance and stability to pass data traffic at the required data rates with the Link Partner.

Results - with Loop Set to HNBT	
NDRds (kbit/s)	768493
NDRus (kbit/s)	191975
GDRds	768493
GDRus	191975
SNRMds	6
SNRMus	6.1
Results - with Loop Set to HNBT After Traffic	
Frames Transmitted ds	21015555
Frames Transmitted us	5253543
Frames Received ds	21015555
Frames Received us	5253543
FLRds	0
FLRus	0

Summary		
The aggregate net data rate SHALL be >= 450000kbps (HNBT Test).	Reported NDR Value In Range: DS: 768493 US: 191975	pass
The GDRus SHALL be >= 0.99 x NDRus(HNBT Test).	Reported GDR Value In Range: 191975	pass
The GDRds SHALL be >= 0.99 x NDRds (HNBT Test).	Reported GDR Value In Range: 768493	pass
The SNRMus SHALL be above the configured SRA downshift margin (HNBT Test).	SNRM In Bounds: 6.1	pass
The SNRMds SHALL be above the configured SRA downshift margin (HNBT Test).	SNRM In Bounds: 6	pass
The number of dropped upstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRus SHALL not exceed the background FLR (HNBT Test).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The number of dropped downstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRds SHALL not exceed the background FLR (HNBT Test).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass

8.2.1 Multi-line Basic Throughput Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT can establish N stable links of sufficient performance and stability to pass data traffic at the required data rates with the Link Partner. The required net data rates within the expected results for this test require the devices to implement vectoring. This test is performed with all devices connecting over a mix of loop lengths, also known as non co-located CPE devices.

Results - CURR_NE_24_UAS		
Line #	**CURR_NE_24_UAS Before G.fast connection**	**CURR_NE_24_UAS After G.fast connection**
Line #1	19	39
Line #2	19	39
Line #3	30	60
Line #4	30	60
Line #5	19	39
Line #6	19	39
Line #7	30	60
Line #8	30	60

Results - CURR_24_FULL_INITS, CURR_24_FAST_INITS				
Line #	**CURR_24_FULL_INITS Before Traffic**	**CURR_24_FAST_INITs Before Traffic**	**CURR_24_FULL_INITS After Traffic**	**CURR_24_FAST_INITs After Traffic**
Line #1	2	0	2	0
Line #2	2	0	2	0
Line #3	2	0	2	0
Line #4	2	0	2	0
Line #5	3	0	3	0
Line #6	2	0	2	0
Line #7	2	0	2	0
Line #8	2	0	2	0

Results										
Line #	**NDRds**	**NDRus**	**GDRds**	**GDRus**	**SNRMds**	**SNRMus**	**Downstream Frames Received**	**Upstream Frames Sent**	**Downstream FLR**	**Upstream FLR**
Line #1	1426939	362645	1426939	362645	6.4	6.1	27338593	9922643	0	1.0077960075758E-07
Line #2	1458296	362255	1458296	362255	6.3	6.4	27338593	9911933	0	1.0088849470633E-07
Line #3	812743	210051	812743	210051	6.3	6.3	22224537	5748746	0	0
Line #4	599577	169439	599577	169439	6.2	6.2	16173652	4479870	0	4.46441526205002E-07
Line #5	1457325	372039	1457325	372039	6.6	6.2				

Line #6	1452857	362121	1452857	362121	6.5	6.2				
Line #7	823120	224292	823120	224292	6.4	6.3				
Line #8	591289	163673	591289	163673	6.3	6.6				

Summary		
The CURR_NE_24_UAS counter increase SHALL be <= 60 (Line #1 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported UAS Increase: 20	pass
The aggregate net data rate SHALL be >= the required aggregate net data rate for the associated loop length. (Line #1 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Aggregate NDR: 1789584	pass
The GDRds SHALL be >= 0.8 x NDRds (Line #1 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRds: 1426939, Reported NDRds: 1426939	pass
The GDRus SHALL be >= 0.8 x NDRus (Line #1 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRus: 362645, Reported NDRus: 362645	pass
The SNRMds SHALL be above the configured SRA downshift margin (Line #1 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMds: 6.4	pass
The SNRMus SHALL be above the configured SRA downshift margin (Line #1 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMus: 6.1	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Line #1 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Line #1 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Fast Inits Increase: 0	pass
The CURR_NE_24_UAS counter increase SHALL be <= 60 (Line #2 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported UAS Increase: 20	pass

The aggregate net data rate SHALL be >= the required aggregate net data rate for the associated loop length. (Line #2 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Aggregate NDR: 1820551	pass
The GDRds SHALL be >= 0.8 x NDRds (Line #2 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRds: 1458296, Reported NDRds: 1458296	pass
The GDRus SHALL be >= 0.8 x NDRus (Line #2 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRus: 362255, Reported NDRus: 362255	pass
The SNRMds SHALL be above the configured SRA downshift margin (Line #2 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMds: 6.3	pass
The SNRMus SHALL be above the configured SRA downshift margin (Line #2 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMus: 6.4	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Line #2 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Line #2 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Fast Inits Increase: 0	pass
The CURR_NE_24_UAS counter increase SHALL be <= 60 (Line #3 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported UAS Increase: 30	pass
The aggregate net data rate SHALL be >= the required aggregate net data rate for the associated loop length. (Line #3 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Aggregate NDR: 1022794	pass
The GDRds SHALL be >= 0.8 x NDRds (Line #3 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRds: 812743, Reported NDRds: 812743	pass
The GDRus SHALL be >= 0.8 x NDRus (Line #3 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRus: 210051, Reported NDRus: 210051	pass
The SNRMds SHALL be above the configured SRA downshift margin (Line #3 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMds: 6.3	pass
The SNRMus SHALL be above the configured SRA downshift margin (Line #3 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMus: 6.3	pass

The CURR_24_FULL_INITS counter increase SHALL be 0 (Line #3 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Line #3 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Fast Inits Increase: 0	pass
The CURR_NE_24_UAS counter increase SHALL be <= 60 (Line #4 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported UAS Increase: 30	pass
The aggregate net data rate SHALL be >= the required aggregate net data rate for the associated loop length. (Line #4 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Aggregate NDR: 769016	pass
The GDRds SHALL be >= 0.8 x NDRds (Line #4 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRds: 599577, Reported NDRds: 599577	pass
The GDRus SHALL be >= 0.8 x NDRus (Line #4 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRus: 169439, Reported NDRus: 169439	pass
The SNRMds SHALL be above the configured SRA downshift margin (Line #4 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMds: 6.2	pass
The SNRMus SHALL be above the configured SRA downshift margin (Line #4 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMus: 6.2	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Line #4 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Line #4 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Fast Inits Increase: 0	pass
The CURR_NE_24_UAS counter increase SHALL be <= 60 (Line #5 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported UAS Increase: 20	pass
The aggregate net data rate SHALL be >= the required aggregate net data rate for the associated loop length. (Line #5 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Aggregate NDR: 1829364	pass
The GDRds SHALL be >= 0.8 x NDRds (Line #5 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRds: 1457325, Reported NDRds: 1457325	pass

The GDRus SHALL be >= 0.8 x NDRus (Line #5 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRus: 372039, Reported NDRus: 372039	pass
The SNRMds SHALL be above the configured SRA downshift margin (Line #5 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMds: 6.6	pass
The SNRMus SHALL be above the configured SRA downshift margin (Line #5 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMus: 6.2	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Line #5 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Line #5 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Fast Inits Increase: 0	pass
The CURR_NE_24_UAS counter increase SHALL be <= 60 (Line #6 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported UAS Increase: 20	pass
The aggregate net data rate SHALL be >= the required aggregate net data rate for the associated loop length. (Line #6 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Aggregate NDR: 1814978	pass
The GDRds SHALL be >= 0.8 x NDRds (Line #6 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRds: 1452857, Reported NDRds: 1452857	pass
The GDRus SHALL be >= 0.8 x NDRus (Line #6 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRus: 362121, Reported NDRus: 362121	pass
The SNRMds SHALL be above the configured SRA downshift margin (Line #6 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMds: 6.5	pass
The SNRMus SHALL be above the configured SRA downshift margin (Line #6 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMus: 6.2	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Line #6 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Line #6 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Fast Inits Increase: 0	pass
The CURR_NE_24_UAS counter increase SHALL be <= 60 (Line #7 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported UAS Increase: 30	pass

The aggregate net data rate SHALL be >= the required aggregate net data rate for the associated loop length. (Line #7 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Aggregate NDR: 1047412	pass
The GDRds SHALL be >= 0.8 x NDRds (Line #7 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRds: 823120, Reported NDRds: 823120	pass
The GDRus SHALL be >= 0.8 x NDRus (Line #7 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRus: 224292, Reported NDRus: 224292	pass
The SNRMds SHALL be above the configured SRA downshift margin (Line #7 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMds: 6.4	pass
The SNRMus SHALL be above the configured SRA downshift margin (Line #7 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMus: 6.3	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (Line #7 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Line #7 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Fast Inits Increase: 0	pass
The CURR_NE_24_UAS counter increase SHALL be <= 60 (Line #8 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported UAS Increase: 30	pass
The aggregate net data rate SHALL be >= the required aggregate net data rate for the associated loop length. (Line #8 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Aggregate NDR: 754962	pass
The GDRds SHALL be >= 0.8 x NDRds (Line #8 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRds: 591289, Reported NDRds: 591289	pass
The GDRus SHALL be >= 0.8 x NDRus (Line #8 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported GDRus: 163673, Reported NDRus: 163673	pass
The SNRMds SHALL be above the configured SRA downshift margin (Line #8 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMds: 6.3	pass
The SNRMus SHALL be above the configured SRA downshift margin (Line #8 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported SNRMus: 6.6	pass

The CURR_24_FULL_INITS counter increase SHALL be 0 (Line #8 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Full Inits Increase: 0	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (Line #8 |Loop 20m,50m,100m,200m,20m,50m,100m,200m).	Reported Fast Inits Increase: 0	pass
The number of dropped downstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRds SHALL not exceed the background FLR (Line #1 |Loop 20).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The number of dropped upstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRus SHALL not exceed the background FLR (Line #1 |Loop 20).	Measured FLR Value Outside Range: FLR: 1.008e-007 |Dropped Frames: 1	pass
The number of dropped downstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRds SHALL not exceed the background FLR (Line #2 |Loop 50).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The number of dropped upstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRus SHALL not exceed the background FLR (Line #2 |Loop 50).	Measured FLR Value Outside Range: FLR: 1.009e-007 |Dropped Frames: 1	pass
The number of dropped downstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRds SHALL not exceed the background FLR (Line #3 |Loop 100).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The number of dropped upstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRus SHALL not exceed the background FLR (Line #3 |Loop 100).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The number of dropped downstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRds SHALL not exceed the background FLR (Line #8 |Loop 200).	Measured FLR Value Outside Range: FLR: 0.000e+000 |Dropped Frames: 0	pass
The number of dropped upstream (i.e., transmitted minus received) frames SHALL not exceed 5, or, the FLRus SHALL not exceed the background FLR (Line #8 |Loop 200).	Measured FLR Value Outside Range: FLR: 4.464e-007 |Dropped Frames: 2	pass

8.2.2 Multi-line Disorderly Shutdown Test	PARTS	RESULTS
		PASS

PURPOSE

The purpose of this test is to verify that the DUT is able to sustain with the Link Partner N-1 stable links when a disorderly shutdown occurs on one line. This test is performed with all devices connecting over a mix of loop lengths, also known as non co-located CPE devices.

Results - CURR_24_FULL_INITS, CURR_24_FAST_INITS				
Line #	**CURR_24_FAST_INITS**	**CURR_24_FULL_INITS**	**CURR_NE_24_UAS**	**CURR_FE_24_UAS**
Line #1	0	1	20	20
Line #2	0	1	20	20
Line #3	0	1	30	30
Line #4	0	1	30	30
Line #5	0	1	20	20
Line #6	0	1	20	20
Line #7	0	1	30	30
Line #8	0	2	62	62
Results - after Line Disconnect				
Line #	**CURR_24_FAST_INITS**	**CURR_24_FULL_INITS**	**CURR_NE_24_UAS**	**CURR_FE_24_UAS**
Line #1	0	1	20	20
Line #2	0	1	20	20
Line #3	0	1	30	30
Line #4	0	1	30	30
Line #5	0	1	20	20
Line #6	0	1	20	20
Line #7	0	1	30	30
Line #8	1	3	113	113

Results - after line AC Mains Disconnect				
Line #	**CURR_24_FAST_INITS**	**CURR_24_FULL_INITS**	**CURR_NE_24_UAS**	**CURR_FE_24_UAS**
Line #1	0	1	20	20
Line #2	0	1	20	20
Line #3	0	1	30	30
Line #4	0	1	30	30
Line #5	0	1	20	20
Line #6	0	2	77	77
Line #7	0	1	30	30
Line #8	1	3	113	113

Summary		
The CURR_24_FAST_INITS counter increase SHALL be 0 (20m,50m,100m,200m,20m,50m,100m,200m, line disconnect).	Reported No Increase	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (20m,50m,100m,200m,20m,50m,100m,200m, line disconnect).	Reported No Increase	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (20m,50m,100m,200m,20m,50m,100m,200m, line disconnect).	Reported No Increase	pass
The CURR_24_FAST_INITS counter increase SHALL be 0 (20m,50m,100m,200m,20m,50m,100m,200m, AC disconnect).	Reported No Increase	pass
The CURR_24_FULL_INITS counter increase SHALL be 0 (20m,50m,100m,200m,20m,50m,100m,200m, AC disconnect).	Reported No Increase	pass
The CURR_NE/FE_24_UAS counter increase SHALL be 0 (20m,50m,100m,200m,20m,50m,100m,200m, AC disconnect).	Reported No Increase	pass

APPENDICES

APPENDIX 1: RESULT KEY

The following table contains possible results and their meanings.

RESULT	MEANING	INTERPRETATION
PASS	Pass	The Device Under Test (DUT) was observed to exhibit conformant behavior.
FAIL	Fail	The Device Under Test (DUT) was observed to exhibit non-conformant behavior.
RTC	Refer to Comments	From the observations, a valid pass or fail was not determined. An additional explanation of the situation is included.
WARN	Warning	The DUT was observed to exhibit behavior that is not recommended.
N/S	Not Supported	The Device Under Test (DUT) was not observed to support the necessary functionality required to perform these tests or the requirement is optional and not supported by this device.
N/T	Not Tested	This test was not performed and therefore this is not a complete test report. Please see the comments for additional reasons.
N/A	Not Applicable	This test does not apply to the device type or is not applicable to the testing program selected.
INFO	Informative	Test is designed for informational purposes only. The results may help ensure the interoperability of the DUT, but are not standards requirements.

APPENDIX 2: DIGITAL SIGNATURE INFORMATION

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